

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology
- ★ 100% EAS Guaranteed

Product Summary

RoHS

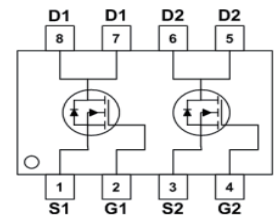
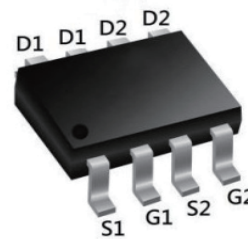
BVDSS	RDS(on)	ID
40V	12mΩ	12A

Description

The 4884 is the high cell density trenched N-ch MOSFETs, which provide excellent $R_{DS(on)}$ and gate charge for most of the synchronous buck converter applications.

The 4884 meet the RoHS and Green Product, requirement 100% EAS guaranteed with full function reliability approved.

SOP8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current ₁	12	A
$I_D@T_C=100^\circ C$	Continuous Drain Current ₁	7	A
I_{DM}	Pulsed Drain Current ₂	40	A
EAS	Single Pulse Avalanche Energy ₃	31	mJ
I_{AS}	Avalanche Current	10	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ₄	2.9	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Thermal Resistance Junction-ambient ₁ ($t_s \leq 10s$)	---	40	$^\circ C/W$
	Thermal Resistance Junction-ambient ₁	---	65	$^\circ C/W$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1	1.5	2	V
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =8A	-	12	16	mΩ
		V _{GS} =4.5V, I _D =4A	-	18.9	24	mΩ
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =8A	33	-	-	S
Dynamic Characteristics ^(Note4)						
C _{ISS}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, F=1.0MHz	-	964	-	PF
C _{OSS}	Output Capacitance		-	109	-	PF
C _{rss}	Reverse Transfer Capacitance		-	96	-	PF
Switching Characteristics ^(Note 4)						
t _{d(on)}	Turn-on Delay Time	V _{DD} =20V, R _L =2.5Ω	-	5.5	-	nS
t _r	Turn-on Rise Time		-	14	-	nS
t _{d(off)}	Turn-Off Delay Time	V _{GS} =10V, R _{GEN} =3Ω	-	24	-	nS
t _f	Turn-Off Fall Time		-	12	-	nS
Q _g	Total Gate Charge	V _{DS} =20V, I _D =8A, V _{GS} =10V	-	22.9	-	nC
Q _{gs}	Gate-Source Charge		-	3.5	-	nC
Q _{gd}	Gate-Drain Charge		-	5.3	-	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage ^(Note 3)	V _{GS} =0V, I _S =9A	-	0.8	1.2	V

N-Channel Typical Electrical and Thermal Characteristics (Curves)

Figure1: Switching Test Circuit

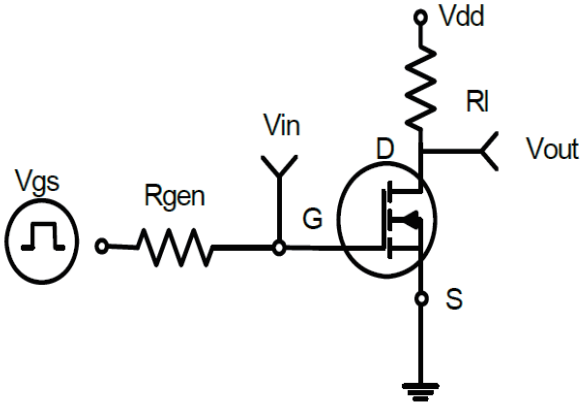


Figure 2: Switching Waveforms

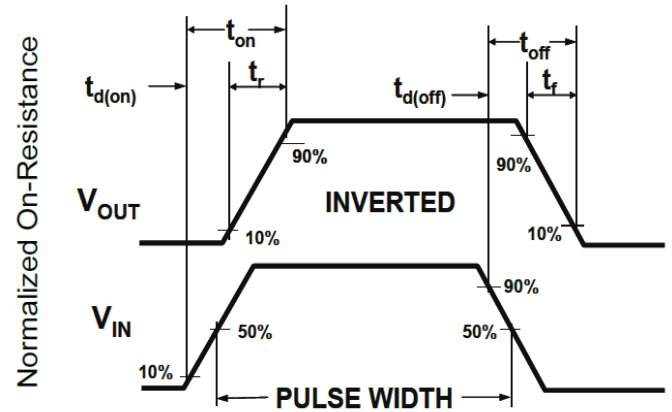


Figure 3: Output Characteristics

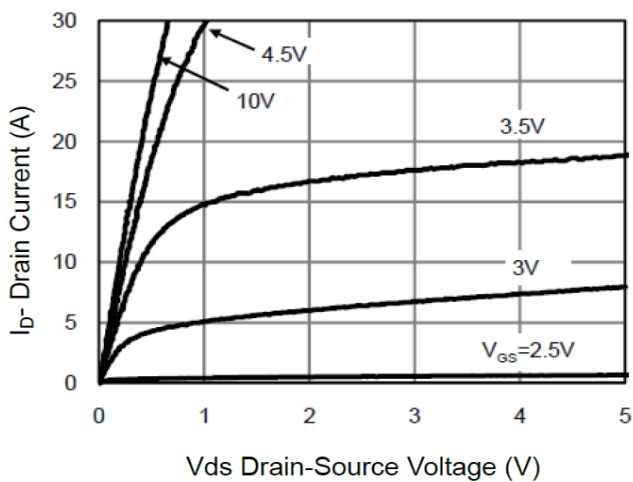


Figure 4: Transfer Characteristics

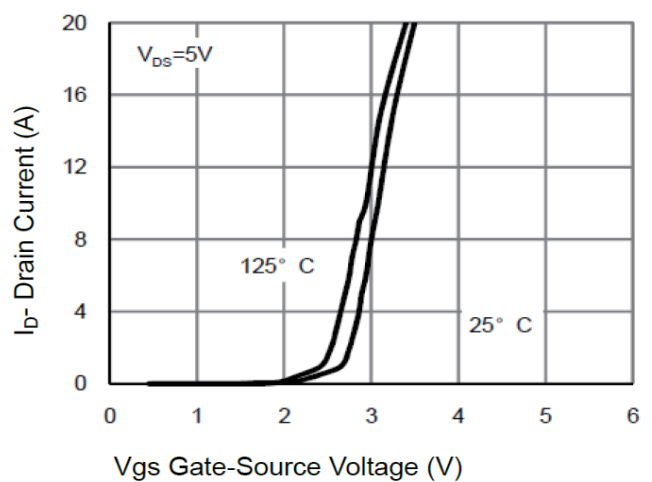


Figure 5: Drain-Source On-Resistance

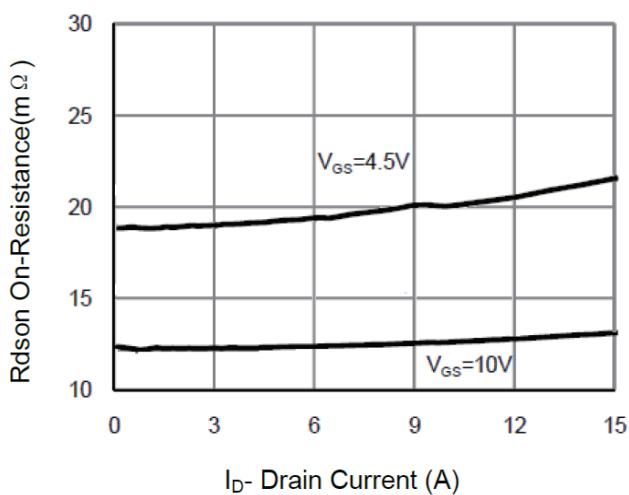
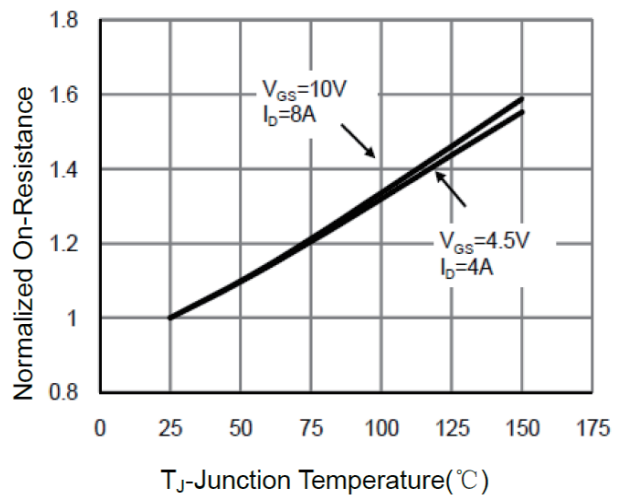


Figure 6: Drain-Source On-Resistance



Typical Performance Characteristics

Figure 7: Rdson vs Vgs Temperature

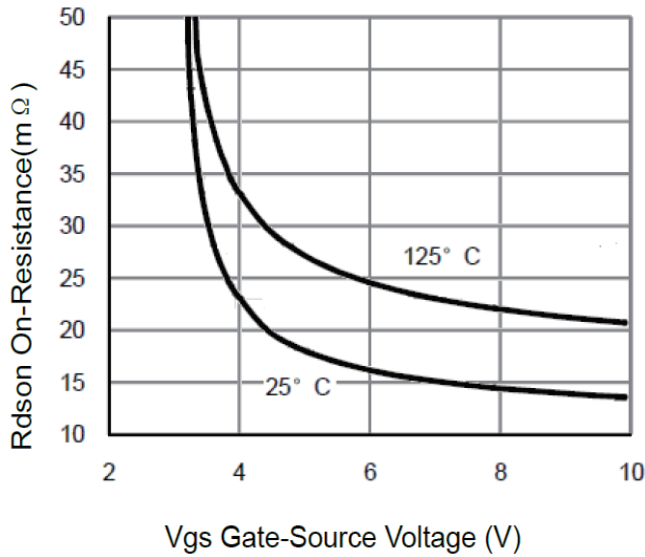


Figure 8: Power Dissipation

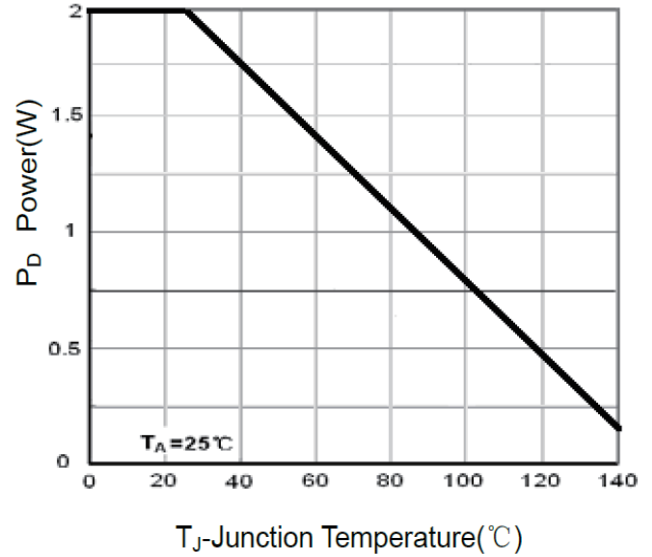


Figure 9: Gate Charge

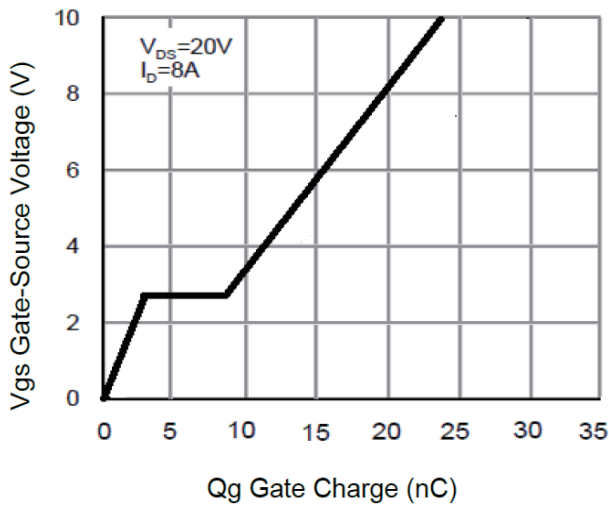


Figure 10: Source- Drain Diode Forward

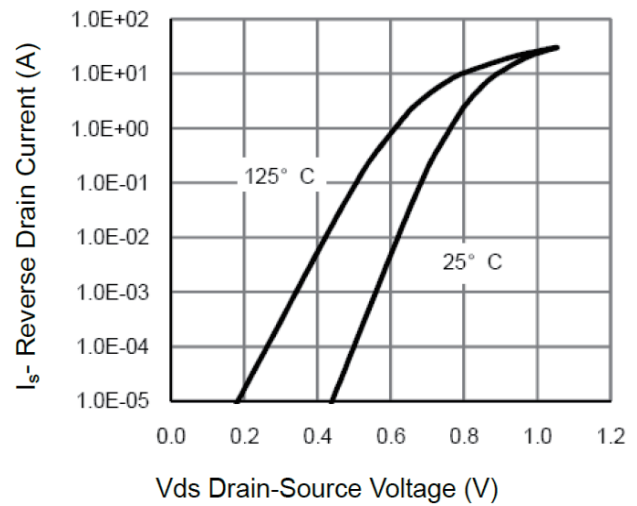


Figure.11: Capacitance vs Vds

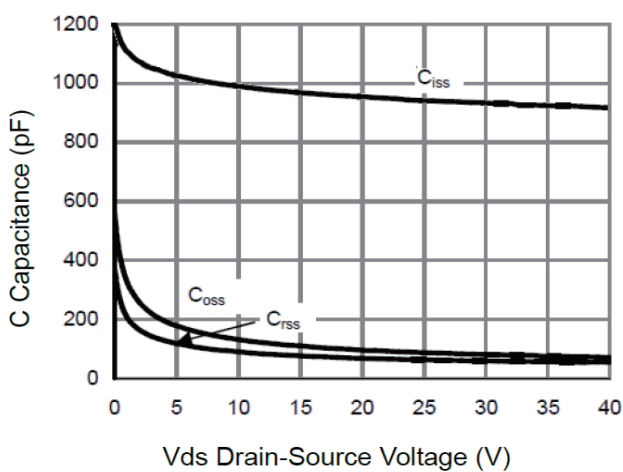
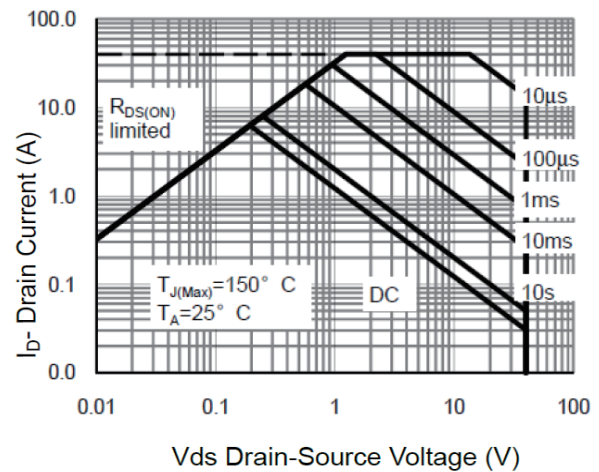
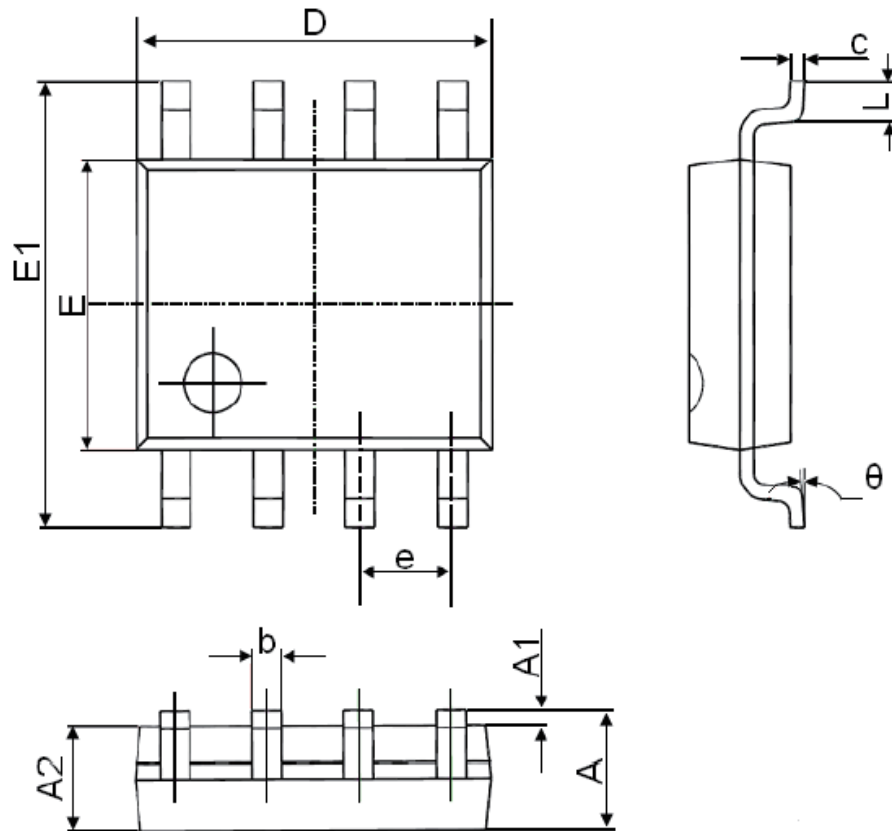


Figure.12: Safe Operation Area



SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°