

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology
- ★ 100% EAS Guaranteed

### Product Summary

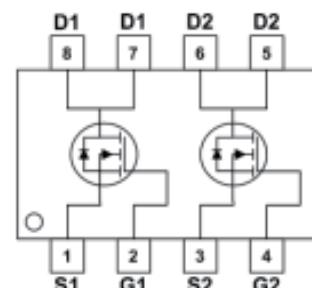
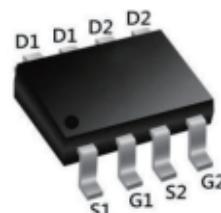
BVDSS	RDS(ON)	ID
60V	12mΩ	15A

### Description

The S15V06S is the high cell density trenched N-ch MOSFETs, which provide excellent RDS(ON) and gate charge for most of the synchronous buck converter applications.

The S15V06S meet the RoHS and Green Product, requirement 100% EAS guaranteed with full function reliability approved.

### Dual SOP8 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Max.	Unit
V <sub>DSS</sub>	Drain-Source Voltage	60	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current T <sub>c</sub> = 25°C	15	A
		29	A
I <sub>DM</sub>	Pulsed Drain Current <sup>note1</sup>	180	A
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>note2</sup>	36	mJ
P <sub>D</sub>	Power Dissipation T <sub>c</sub> = 25°C	60	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	2.5	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +175	°C

Electrical Characteristics ( $T_{J}=25^{\circ}\text{C}$  unless otherwise specified)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	60	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1	$\mu\text{A}$
$I_{GS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}= \pm 20\text{V}$	-	-	$\pm 100$	$\text{nA}$
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1	1.6	2.5	V
$R_{DS(\text{on})}$	Static Drain-Source on-Resistance <small>note3</small>	$V_{GS}=10\text{V}$ , $I_D=20\text{A}$	-	12	15	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=10\text{A}$	-	15	20	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	930	-	pF
$C_{oss}$	Output Capacitance		-	230	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	8	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=30\text{V}$ , $I_D=20\text{A}$ , $V_{GS}=10\text{V}$	-	22	-	nC
$Q_{gs}$	Gate-Source Charge		-	4.5	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	3.5	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=30\text{V}$ , $I_D=20\text{A}$ , $R_G=1.6\Omega$ , $V_{GS}=10\text{V}$	-	4.5	-	ns
$t_r$	Turn-on Rise Time		-	2.7	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	13.8	-	ns
$t_f$	Turn-off Fall Time		-	2.7	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	15	-	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	180	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=30\text{A}$	-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$T_J=25^{\circ}\text{C}$ , $IF=20\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	18	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	12	-	nC

Note :

1.Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2.EAS condition:  $T_J=25^{\circ}\text{C}$ ,  $V_{DD}=30\text{V}$ ,  $V_G=10\text{V}$ ,  $R_G=25\Omega$ ,  $L=0.5\text{mH}$ ,  $I_{AS}=12\text{A}$ 3.Pulse Test: Pulse Width $\leqslant 300\mu\text{s}$ , Duty Cycle $\leqslant 0.5\%$

### Typical Performance Characteristics

Figure 1: Output Characteristics

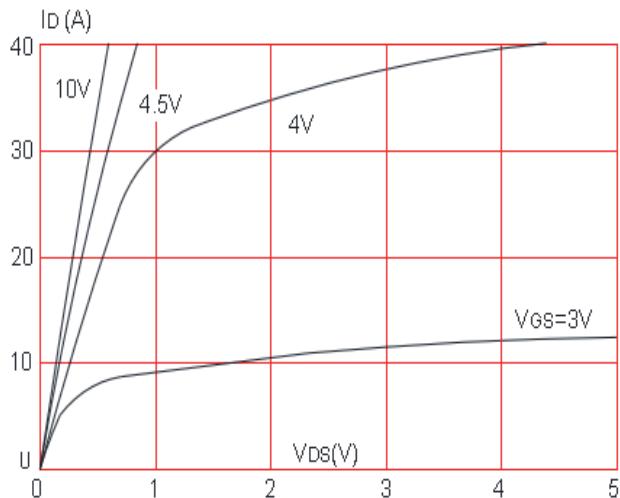


Figure 2: Typical Transfer Characteristics

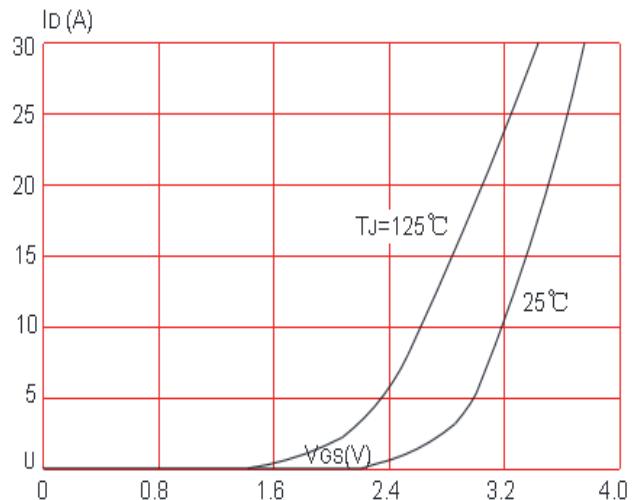


Figure 3: On-resistance vs. Drain Current

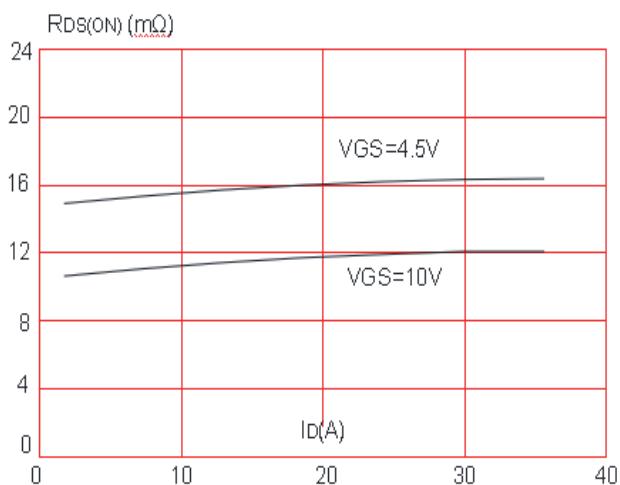


Figure 5: Gate Charge Characteristics

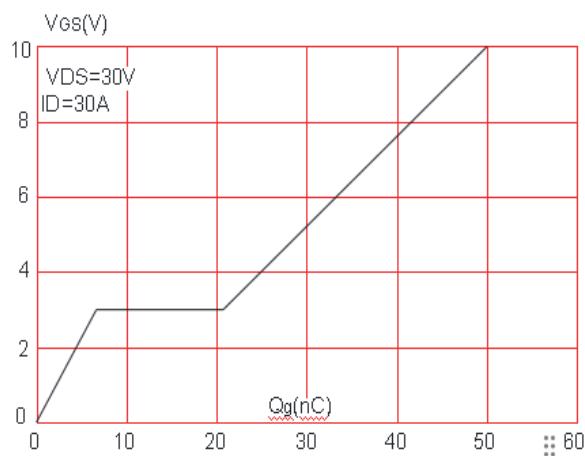


Figure 4: Body Diode Characteristics

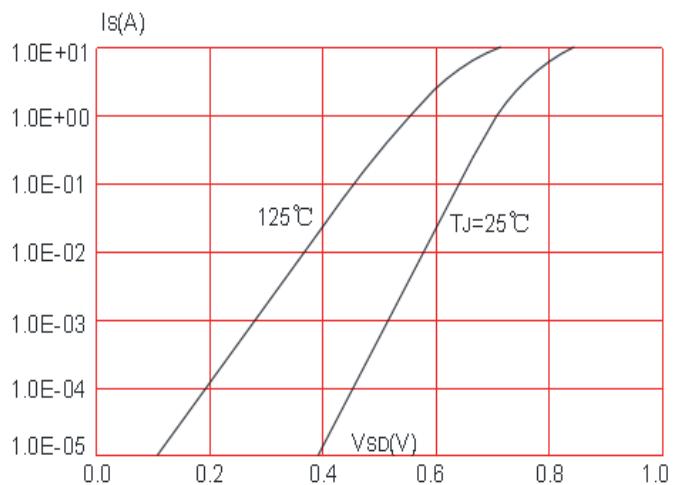
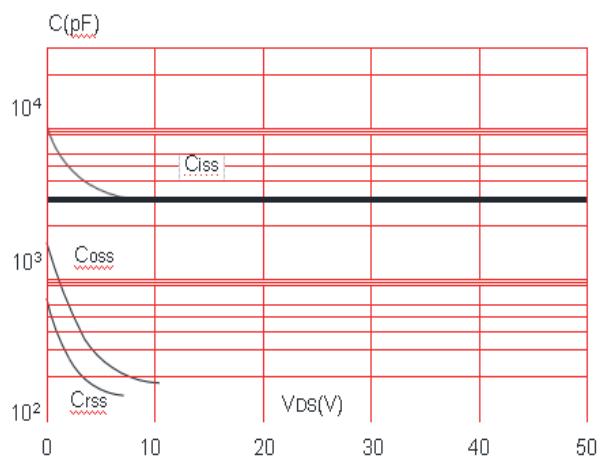


Figure 6: Capacitance Characteristics



### Typical Performance Characteristics

Figure 7: Normalized Breakdown Voltage

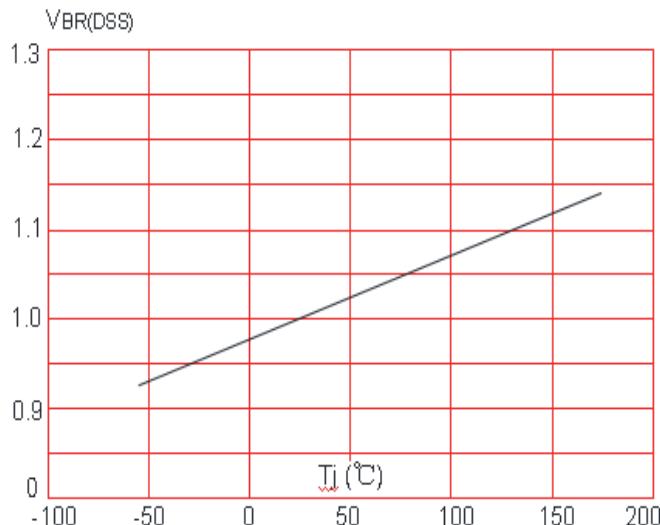


Figure 8: Normalized on Resistance vs.  $J_D$

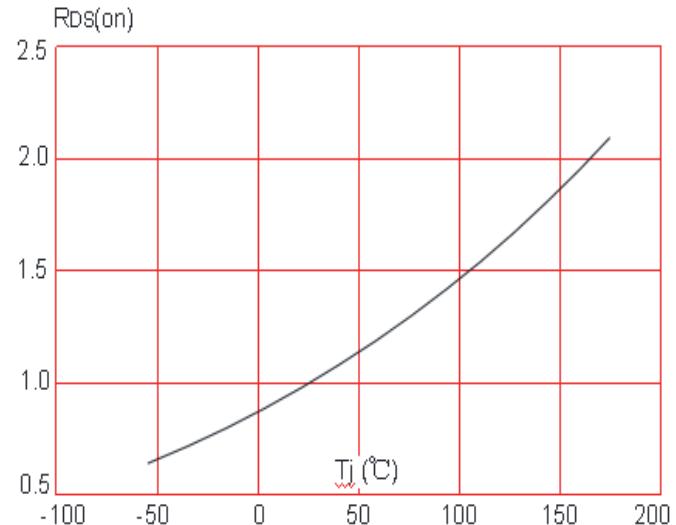


Figure 9: Maximum Safe Operating Area

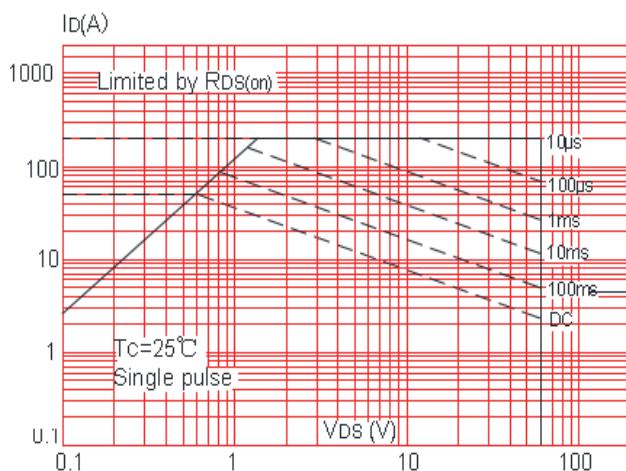


Figure 10: Maximum Continuous Drain Current

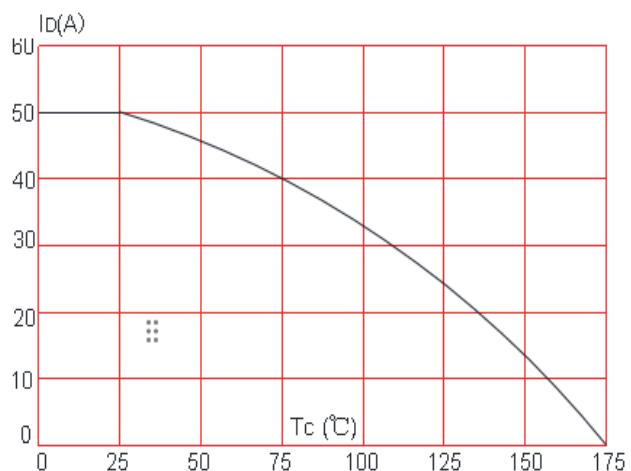
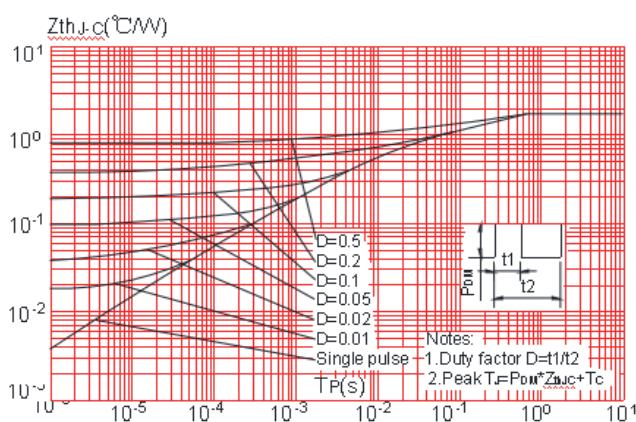
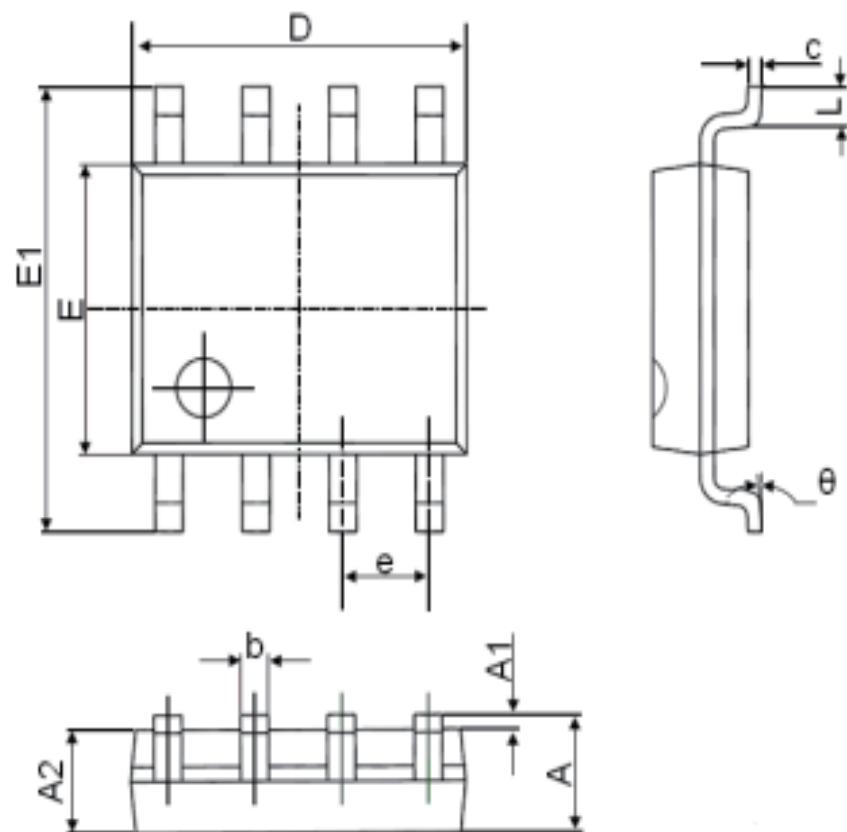


Figure 11: Maximum Effective Transient Thermal Impedance



## Package Mechanical Data- SOP-8



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (ISC)		0.050 (ISC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°