

#### **100V N-Channel MOSFET**

### **General Description**

The 540P uses advanced process technology and design to provide excellent RDS(ON). These devices are well suited for low voltage applications such as audio amplifier, high efficiency switching DC/DC converters, and DC motor control.

### **Features**

- Fast Switching
- 100% Avalanche tested
- RoHS compliant

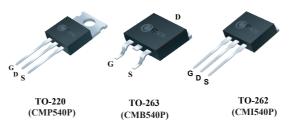
### **Product Summary**

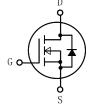
BVDSS	RDSON	ID
100V	45mΩ	33A

### **Applications**

- DC-DC converters
- UPS
- Power Supply
- Motor Control

### TO-220/263/262 Pin Configuration





### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage		
$V_{GS}$	Gate-Source Voltage		
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current	33	Α
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current	Continuous Drain Current 23	
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	100	Α
EAS	Single Pulse Avalanche Energy <sup>2</sup>	324	mJ
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	130	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	$^{\circ}$
$T_J$	Operating Junction Temperature Range	-55 to 175	${\mathbb C}$

## **Thermal Data**

Symbol	Symbol Parameter		Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient		62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case		1.15	°C/W

# CMP540P/CMB540P/CMI540P



### **100V N-Channel MOSFET**

### **Electrical Characteristics (TJ=25℃, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	100			V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =5A			45	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2		4	V
	Drain-Source Leakage Current	V <sub>DS</sub> =100V , V <sub>GS</sub> =0V			25	- uA
I <sub>DSS</sub>		$V_{DS}$ =80V, $V_{GS}$ =0V, $T_{C}$ =150 $^{\circ}$ C			250	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V ,V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	$V_{DS}$ =5 V , $I_D$ =10A		23		S
$R_g$	Gate Resistance	$V_{DS}$ =0V , $V_{GS}$ =0V , f=1MHz		2		Ω
Qg	Total Gate Charge	I <sub>D</sub> =16 A		44		
$Q_{gs}$	Gate-Source Charge	V <sub>DD</sub> =80 V		9		nC
$Q_{gd}$	Gate-Drain Charge	V <sub>GS</sub> =10 V		19		
$T_{d(on)}$	Turn-On Delay Time	V <sub>DD</sub> =50V		13		
Tr	Rise Time	I <sub>D</sub> =16 A		35		
T <sub>d(off)</sub>	Turn-Off Delay Time	R <sub>G</sub> =5.1Ω		40		ns
T <sub>f</sub>	Fall Time	V <sub>GS</sub> =10V		35		
C <sub>iss</sub>	Input Capacitance			1800		
Coss	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		300		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			50		

### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			33	Α
I <sub>SM</sub>	Pulsed Source Current				100	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =10A , T <sub>J</sub> =25℃			1.2	V

#### Note:

This product has been designed and qualified for the counsumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserver the right to improve product design ,functions and reliability wihtout notice.

<sup>1.</sup>Repetitive rating; pulse width limited by maximum junction temperature

<sup>2.</sup>The EAS data shows Max. rating . The test condition is VD = 50V, VGS = 10V, L = 0.5mH, IAS=36A