

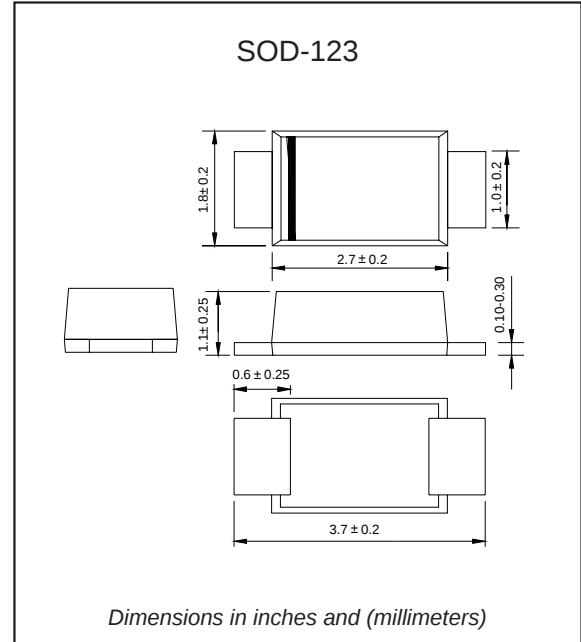
### Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Low reverse leakage
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed: 260°C/10 seconds at terminals
- ◆ Glass passivated chip junction

### Mechanical data

- ◆ **Case:** JEDEC SOD-123 molded plastic body
- ◆ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity:** Color band denotes cathode end
- ◆ **Mounting Position:** Any

### Package outline



### Maximum ratings and Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

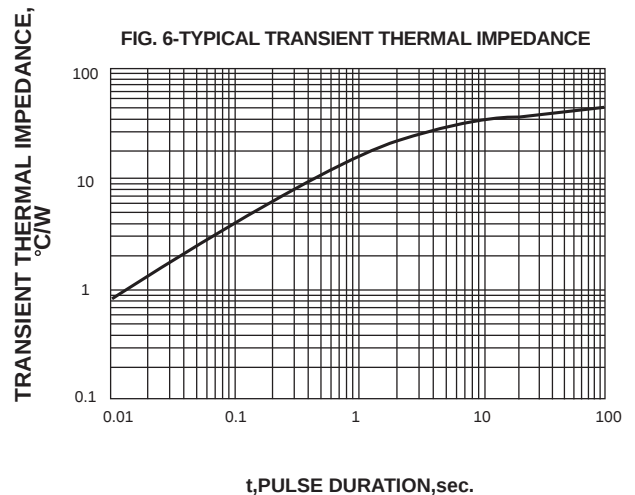
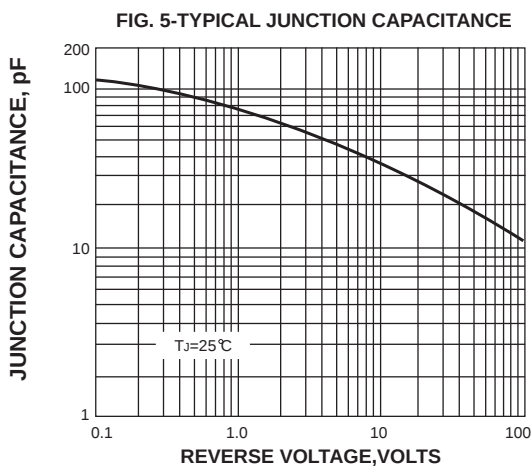
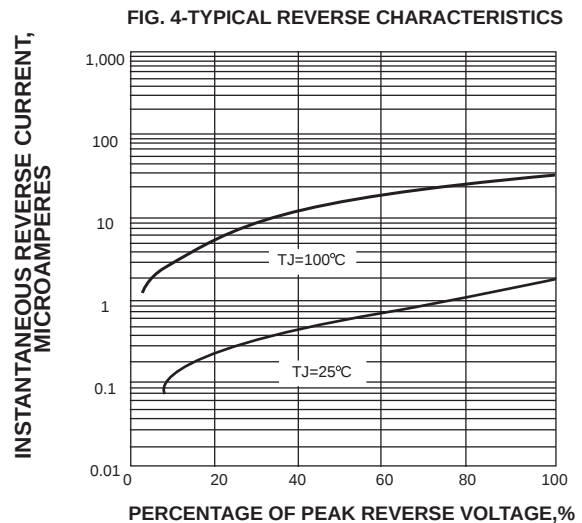
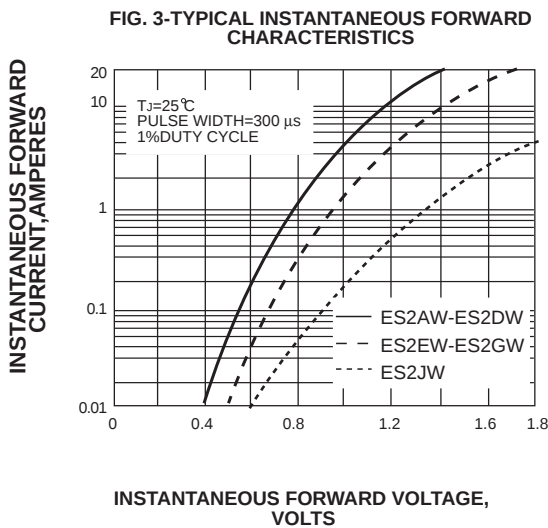
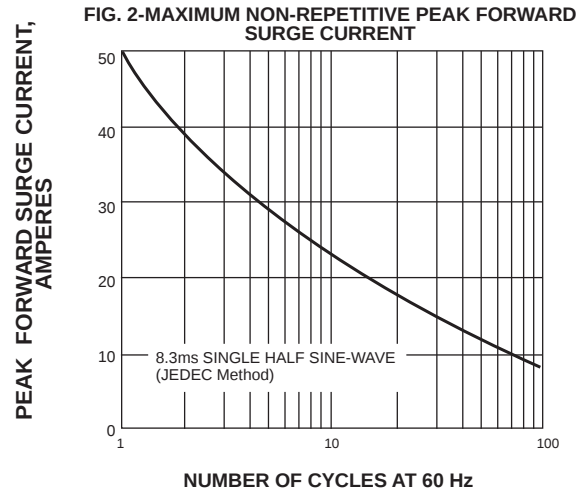
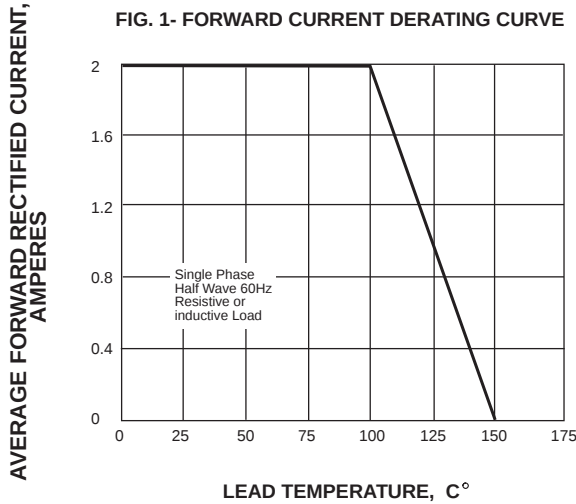
|                                                                                                             | SYMBOLS                          | ES2AW       | ES2BW | ES2CW | ES2DW | ES2EW | ES2GW | ES2JW | UNITS |
|-------------------------------------------------------------------------------------------------------------|----------------------------------|-------------|-------|-------|-------|-------|-------|-------|-------|
| Maximum repetitive peak reverse voltage                                                                     | V <sub>RRM</sub>                 | 50          | 100   | 150   | 200   | 300   | 400   | 600   | V     |
| Maximum RMS voltage                                                                                         | V <sub>RMS</sub>                 | 35          | 70    | 105   | 140   | 210   | 280   | 420   | V     |
| Maximum DC blocking voltage                                                                                 | V <sub>DC</sub>                  | 50          | 100   | 150   | 200   | 300   | 400   | 600   | V     |
| Maximum average forward rectified current at T <sub>L</sub> =75°C                                           | I <sub>(AV)</sub>                | 2.0         |       |       |       |       |       |       | A     |
| Peak forward surge current<br>8.3ms single half sine-wave superimposed on rated load                        | I <sub>FSM</sub>                 | 50.0        |       |       |       |       |       |       | A     |
| Maximum instantaneous forward voltage at 2.0A                                                               | V <sub>F</sub>                   | 0.95        |       |       |       | 1.25  |       | 1.7   | V     |
| Maximum DC reverse current    T <sub>A</sub> =25°C<br>at rated DC blocking voltage    T <sub>A</sub> =100°C | I <sub>R</sub>                   | 5.0<br>50.0 |       |       |       |       |       |       | μA    |
| Maximum reverse recovery time    (NOTE 1)                                                                   | t <sub>rr</sub>                  | 35          |       |       |       |       |       |       | ns    |
| Typical junction capacitance (NOTE 2)                                                                       | C <sub>J</sub>                   | 40.0        |       |       |       |       |       |       | pF    |
| Typical thermal resistance (NOTE 3)                                                                         | R <sub>θJA</sub>                 | 75.0        |       |       |       |       |       |       | °C/W  |
| Operating junction and storage temperature range                                                            | T <sub>J</sub> ,T <sub>STG</sub> | -55 to +150 |       |       |       |       |       |       | °C    |

**Note:** 1. Reverse recovery condition  $I_F=0.5\text{A}$ ,  $I_R=1.0\text{A}$ ,  $I_{rr}=0.25\text{A}$

2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

3. Thermal resistance from junction to ambient at 0.375"(9.5mm) lead length, P.C.B. mounted

### Rating and characteristic curves



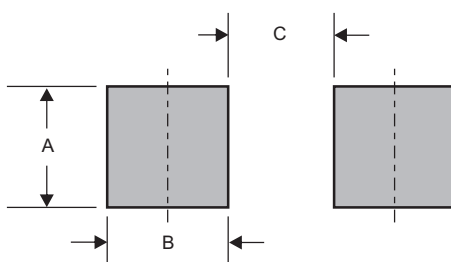
### Pinning information

| Pin                        | Simplified outline | Symbol |
|----------------------------|--------------------|--------|
| Pin1 cathode<br>Pin2 anode |                    |        |

### Marking

| Type number                      | Marking code |
|----------------------------------|--------------|
| ES2AW<br>ES2BW<br>ES2CW<br>ES2DW | E2L          |
| ES2EW<br>ES2GW                   | E2M          |
| ES2JW                            | E2J / E2H    |

### Suggested solder pad layout

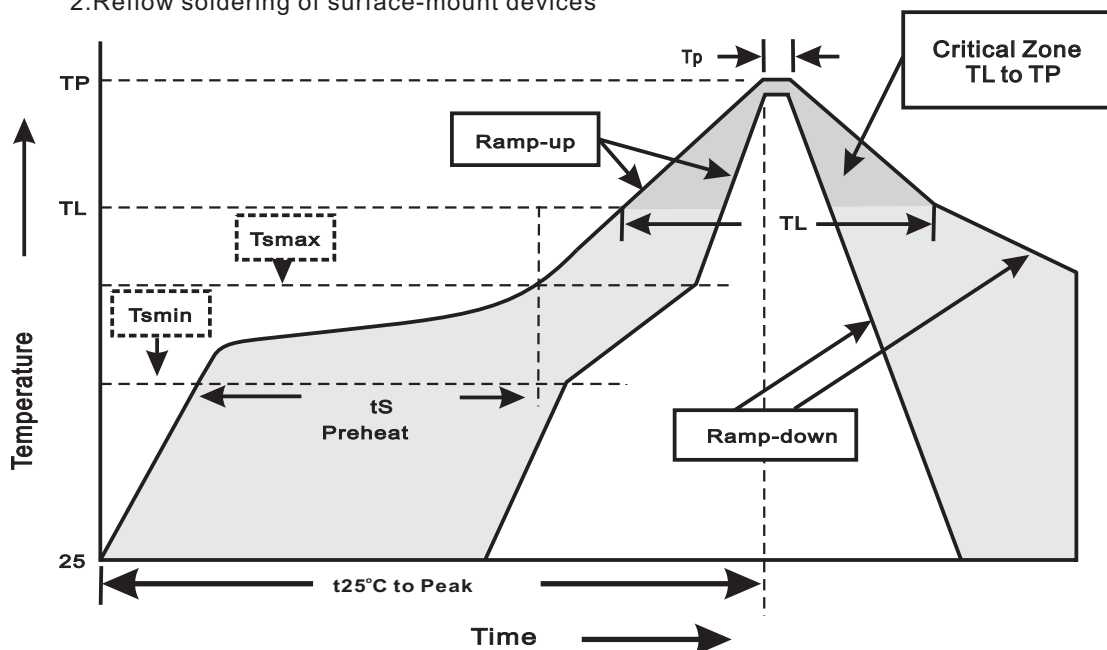


Dimensions in inches and (millimeters)

| PACKAGE | A            | B            | C            |
|---------|--------------|--------------|--------------|
| SOD-123 | 0.075 (1.90) | 0.055 (1.40) | 0.075 (1.90) |

## Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%  
2.Reflow soldering of surface-mount devices



### 3.Reflow soldering

| Profile Feature                                                                                                       | Soldering Condition                                                      |
|-----------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------|
| Average ramp-up rate( $T_L$ to $T_P$ )                                                                                | $<3^{\circ}\text{C}/\text{sec}$                                          |
| Preheat<br>-Temperature Min( $T_{\text{min}}$ )<br>-Temperature Max( $T_{\text{max}}$ )<br>-Time(min to max)( $t_s$ ) | $150^{\circ}\text{C}$<br>$200^{\circ}\text{C}$<br>$60\sim 120\text{sec}$ |
| $T_{\text{max}}$ to $T_L$<br>-Ramp-upRate                                                                             | $<3^{\circ}\text{C}/\text{sec}$                                          |
| Time maintained above:<br>-Temperature( $T_L$ )<br>-Time( $t_L$ )                                                     | $217^{\circ}\text{C}$<br>$60\sim 260\text{sec}$                          |
| Peak Temperature( $T_P$ )                                                                                             | $255^{\circ}\text{C}-0/+5^{\circ}\text{C}$                               |
| Time within $5^{\circ}\text{C}$ of actual Peak Temperature( $t_P$ )                                                   | $10\sim 30\text{sec}$                                                    |
| Ramp-down Rate                                                                                                        | $<6^{\circ}\text{C}/\text{sec}$                                          |
| Time $25^{\circ}\text{C}$ to Peak Temperature                                                                         | $<6\text{minutes}$                                                       |