



#### SCM4520 Series Common Mode Line Filter





### ◆特征:

- 适用大电流的片式共模滤波器
- 所有系列产品的形状均控制在最小限度 同时共模阻抗超过了70Ω~3000Ω(100MHz时) 能够大幅抑制共模噪音
- 产品的高度尺寸也被控制在较低水平 能够适应不断小型轻量化的便携 式设备的高密度化趋势
- 符合 RoHS.无卤和 REACH

### ◆用途:

- 用于应对电子设备的电源线噪音 (笔记本电,伺服器,电池,等)
- 适合车载大电流电路
- 无线充电电源装置

## ◆环境:

工作温度: -40℃ 至+125℃
 (包括线圈自身温升)

## ◆试验设备:

- 电流:HP4284+42841A 或同等仪器
- 阻抗:E4991+ HP16092 测试夹具或同等仪器

4520

● 直流电阻: Chroma 16502 或同等仪器

## ◆产品型号:

SCM

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1	A WHAT
	类型 Type
SCM	贴片共模滤波器 SMD Common Mode Line Filter
4	40

This	包装 Packing		
В	散装Bulk Package		
Т	编带Tape & Reel		

### Features:

- Chip common mode filter for large current applications
- •The shape of all series products is controlled to a minimum and the common mode impedance exceeds  $70\Omega\sim3000\Omega$  (at 100MHz), which can greatly suppress common mode noise
- Compatible with high-density portable devices, which are always being made smaller and lighter, because the height has been reduced
- RoHS, Halogen Free and REACH Compliance

# **Applications:**

- Power line noise countermeasure for electronic equipment (Notebook, server applications, Battery, etc.)
- Best for high current circuit such as car
- wireless charging and power device design

#### **Environmental Data:**

Operating Temperature: -40° to +125° (Including coils self-temperature rise)

# **Test Equipment:**

- Isat & Irms: HP4284+42841A or equivalen
- Impedance: E4991 analyzer with HP16092 test fixture or equivalen
- DCR:Chroma 16502 or equivalent

#### Product Identification:

501

(3)

Impedance 500 Ω

http:www.szlantu.com.cn

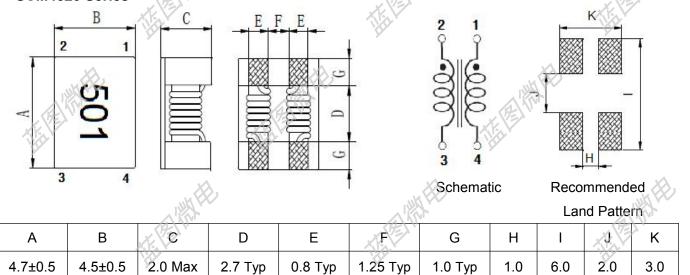




# ◆外观尺寸:

# Shape and Dimensions (dimensions are in mm):

## • SCM4520 Series



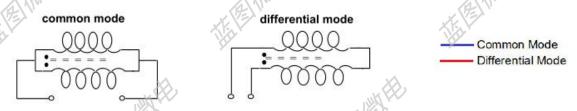
# ◆规格特性:

# Specifications:

• SCM4520 Series Electrical Characteristics (Electrical specifications at 25°C)

	Part Number	Impedance (Ω) @100MHz		DC Resistance	Rated Current	Rated Voltage	Insulatio Resistance	Marking
		Min	Тур	(mΩ) Max	(A) Max	(V) Max	(MΩ) Min	
	SCM4520-900T	60	90	35.0	2.0	50	10M	900
-	SCM4520-151T	90	150	40.0	1.9	50	10M	151
	SCM4520-231T	180	230	45.0	1.8	50	10M	231
	SCM4520-301T	200	300	45.0	1.7	50	10M	301
	SCM4520-421T	300	420	55.0	1.5	50	10M	401
	SCM4520-501T	350	500	57.0	1.3	50	10M	501
	SCM4520-701T	500	700	59.0	1.3	50	10M	701
	SCM4520-901T	650	900	68.0	1.3	50	10M	901
	SCM4520-102T	800	1000	68.0	1.3	50	10M	102
	SCM4520-122T	1000	1200	74.0	1.2	50	10M	122
	SCM4520-142T	1200	1400	81.0	1.2	50	10M	142
	SCM4520-152T	1300	1500	87.0	1.0	50	10M	152

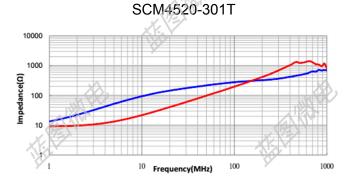
- Rated Current: the actual value of DC current when the temperature rise is  $\Delta T$  40  $^{\circ}$ C (Ta=25  $^{\circ}$ C).
- Circuit: Test Mode



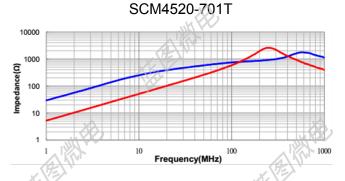


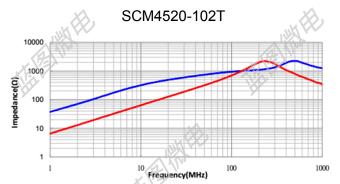


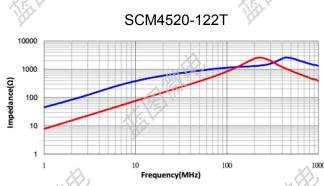
# • Typical Impedance versus Frequency

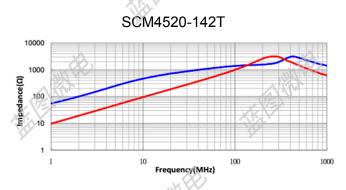








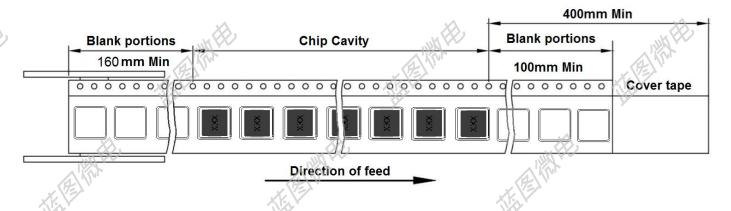




◆产品包装:

# Packaging:

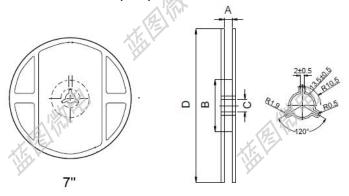
• Tape and Reel Specifications: (Dimensions are in mm)



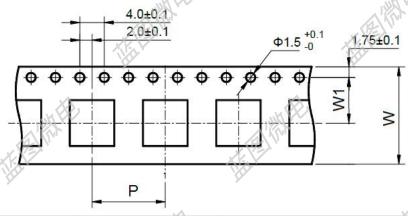




# • Reel Dimensions (mm)

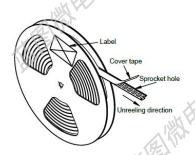


# ●Tape Dimension (mm)

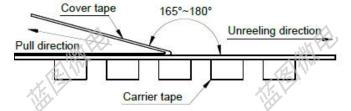


Tape Dimension Part No.		sion	Reel Dimensions			REEL	Inside	Outside	
Fait No.	W	P	W1	Α	В	C	(PCS)	Box(PCS)	Carton(PCS)
SCM4520	12.0	8.0	5.5	12.4	60	13 178	1000	4000	40,000

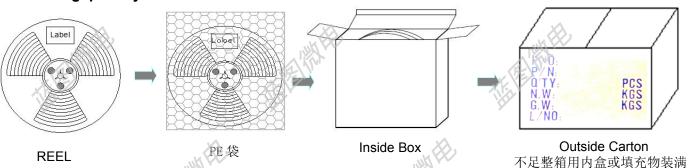
# • Cover tape peel off condition



- a) Cover tape peel force shall be 10 to 120g
- b) Noodle strip peeling angle165° to 180°



# Packing quantity







# ◆可靠性测试:

# Reliability Testing:

	K.Z.	Kenabinty resting:
Items	Requirements	Test Methods and Remarks
Terminal Strength Reference docu ments: GB/T 2423.60-2008 端子強度(SMT)	1. Pulling test:  Define: A: sectional area of terminal  A ≤ 8mm2 force ≥ 5N time:30sec  8mm2 <a 10n="" 10sec="" 2.solder="" 20mm2="" 20mm2<a="" 20n="" 3.meet="" above="" any="" force="" loose="" paste="" requirements="" td="" terminal<="" the="" thickness:0.12mm="" time:="" without="" ≤="" ≥=""><td>Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.</td></a>	Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.
erminal Strength Reference docu ments: GB/T 2423.60-2008 端子強度(DIP)	1.Terminal diameter(d) mm 0.35 < d ≤ 0.50 Applied force:5N Duration: 10sec2.Terminal diameter(d) mm0.50 < d ≤ 0.80 Applied force:10N Duration: 10sec3.Terminal diameter(d) mm0.80 < d ≤ 1.25 Applied force:20N Duration: 10sec4.Terminal diameter(d) mmD > 1.25 Applied force:40N Duration: 10sec5.Meet the above requirements without any loose terminal.	Pull Force:the force shall be applied gradually to the terminal and thenmaintained for 10 seconds.  F  Pulling test
Resistance to Flexure JIS C 5321:1997 抗弯曲性试验	1.No visible mechanical damage.	1.Solder the inductor to the test jig (glass epoxy board 2.shown in Using a leadfree solder. Then apply a force in the direction shown 3.Flexure: 2mm. 4.Pressurizing Speed: 0.5mm/sec. 5.Keep time: 30 sec.
Dropping	1 No case deformation or change	1.Drop the packaged products from 1m high in 1
Reference documents:	inappearance.	angle, 3 ridges and 6surfaces, twice in each
GB/T 2423.7-2018 落下試驗	2.No short and no open.	direction.
Solderability	1.No visible mechanical damage.	1.Solder temperture:240±2℃
Reference documents:	2.Wetting shall exceed 75% coverage for	2.Duration: 3 sec.
GB/T 2423.28-2005	3.Terminals must have 95% minimum solder	3. Solder: Sn/3.0Ag/0.5Cu.
可焊性试验	coverage	
	11F1 Y Y	3161 7

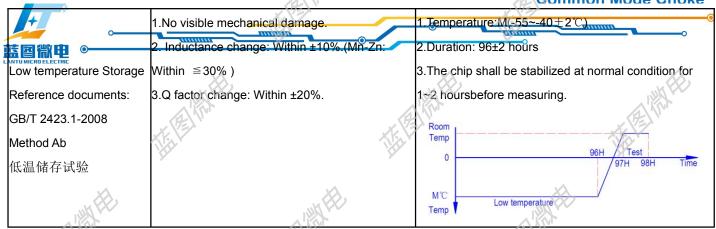
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Items	Requirements	Test Methods and Re	emarks
Vibration Reference documents: GB/T 2423.10-2019 振動試验	1.No visible mechanical damage.  2. Inductance change: Within ±10%.  3.Q factor change: Within ±20%.  Cu pad Solder mask  Glass Epoxy Board	1.Solder the inductor to the boardshown in ) using lea 2.The inductor shall be su harmonic motion having to the frequency being varied approximate limits of 10 a 3.The frequency range from return to 10 Hz shallbe traininute. This motion shall of 2 hours in each 3 mutual directions (total of 6 hours)	dfree solder. bjected to a simple otal amplitude of 1.5mm, duniformly between the nd 55 Hz. om 10 to 55 Hz and oversed in approximately ll be applied for a period ally perpendicular
Thermal Shock Reference documents: GB/T 2423.22-2012 Method Na 冷热冲击试验	<ul> <li>1.No visible mechanical damage.</li> <li>2. Inductance change: Within ±10%.</li> <li>Within ≤30%)</li> <li>3 Q factor change: Within ±20%.</li> </ul>	1.Start at (85~125°C) for (Mn-Zn: (-55~40°C) for T time as o cycles. 2.Transforming interval: M 3.Tested cycle: 100 cycles 4.The chip shall be stabilize for 1~2 hours  125°C/85°C Ambient Temperature -55°C/-40°C	ne cycle, go through100 lax. 20 sec.

# **Common Mode Choke**

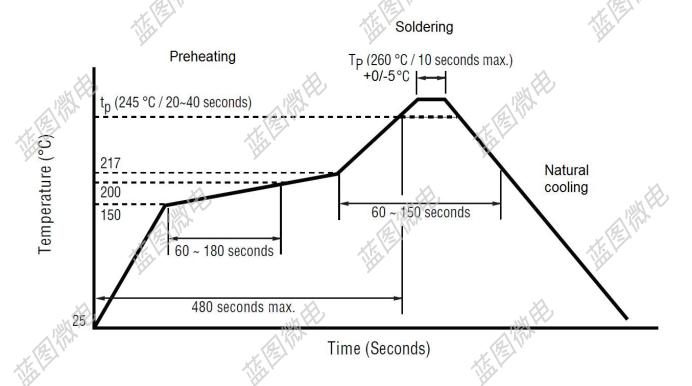


Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	1.Temperature:N(125~85±2℃).
High temperature	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours
Storage	Within ≦30%)	3.The chip shall be stabilized at normal condition
Reference documents: GB/T 2423.2-2008 Method Bb 高温储存试验	3.Q factor change: Within ±20%.	for 1~2 hoursbefore measuring.  Temp N°C  Room Temp 0 96H 97H 98H Time
	1.No visible mechanical damage.	1.Temperature: 60±2℃
	2. Inductance change: Within ±10%.(Mn-Zn:	2.Humidity: 90% to 95% RH.
Damp Heat	Within ≦30%)	3.Duration: 96±2 hours.
(Steady States)	3.Q factor change: Within ±20%.	4.The chip shall be stabilized at normal condition
Reference documents: GB/T 2423.3-2016 恒定湿热试验	推進星機構	for 1~2 hoursbefore measuring.  Temp 60°C 93%RH Room Conditions  Temp & Humidity High temperature High humidity  96H 97H 98H Time
Heat endurance of	1.No significant defects in appearance.	1.Refer to the above reflow curve and go through
Reflow soldering	2. △ L/L ≦ 10% (Mn-Zn: △ L/L ≦ 30% )	the reflow for twice.
Reference documents: GJB 360B-2009 回流焊耐热性试验	<ul> <li>3. △ Q/Q ≤ 30% (SMD series only)</li> <li>4. △ DCR/DCR ≤ 10%</li> </ul>	2.The peak temperature : 260+0/-5°C
Resistance to solvent test Reference documents: IEC 68-2-45:1993 耐溶剂性试验	No case deformation or change in appearance or obliteration of marking	To dip parts into IPA solvent for 5±0.5Min,then drying them at room temp for 5Min,at last ,to brushing making 10 times.

		Commen Mede Chere
Overload test	1.During the test no smoke, no peculiar,	·
Reference documents:	smell, no fire	
JIS C5311-6.13	2.The characteristic is normal after test	Apply twice as rated current for 5 minutes.
过负荷试验		A A A A A A A A A A A A A A A A A A A
voltage resistance test	1.During the test no breakdown	The state of the s
Reference documents:	2.The characteristic is normal after test	A Formanda vida tua asila
MIL-STD-202G Method	With the second	For parts with two coils
301		2. DC1000V, Current: 1mA, Time: 1Min.
	<b>X</b>	Refer to catalogue of specific products
绝缘耐压测试		

# ◆推荐回流焊温度曲线

# Recommended reflow soldering curve:



The recommended reflow conditions as above graph, is set according to our soldering equipment. DUE to various manufactures may have different reflow soldering equipment, products, process conditions, set methods. And so on, when setting the reflow conditions, Please adjust and confirm according to users' environment/equipment.



# 使用注意事项 REMINDERS FOR USING THESE PRODUCTS



● 保存时间为12 个月以内,保存条件(温度5~40°C以下、湿度35 ~ 66%RH 以下),需充分注意 若超过保存时间,端子电极的可焊性将可能老化。

The storage period is within 12 months. Be sure to follow the storage conditions (temperature: 5~40°C, humidity: 35 to 65% RH or less). If the storage period elapses, the soldering of the terminal electrodes may deteriorate.

- 请勿在气体腐蚀环境(盐、酸、碱等)下使用和保存。

  Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).
- 手上的油脂会导致可焊性降低,应避免用手直接接触端子。
   Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering Always ensure optimum conditions for soldering。
- 请小心轻拿轻放,避免由于产品的跌落或取出不当而导致的损坏。

  Please always handle products carefully to prevent any damage caused bydropping down or inappropriate removing。
- 端子过度弯曲会导致断线,请不要过度弯曲端子。
  Don't bend the terminals with excessive stress in case of any wire fracture。
- 不要清洗产品,如需要清洗时请联系我司。
   Don't rinse coils by yourself and please contact SXN if necessary。
- 请勿将本产品靠近磁铁或带有磁力的物体
   Don't expose the products to magnets or magnetic fields
- 在实施焊接前,请务必进行预热。预热温度与焊接温度及芯片温度的温度差要在150°C 以内。
   Before soldering, be sure to preheat components. The preheating temperature should be set so that the temperature difference between the solder temperature and chip temperature does not exceed 150°C.
- 安装后的焊接修正应在规格书规定的条件范围内。若加热过度可能导致短路、性能降低、寿命减少。 Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
- 装置会因通电而自我发热(温度上升),因此在热设计方面需留有充分余地。
   Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.

Common Mode Choke 非磁屏蔽型在基板设计时需注意配置线圈、受到电磁干扰可能会导致误动作。 Carefully lay out the coil for the circuit board design of the non-magnetic shield type. A malfunction may occur due to magnetic interference. 推图機能

推歷機構

推歷機群