

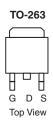
N-Channel 100-V (D-S) MOSFET

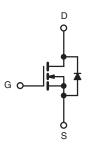
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)			
100	0.010 at V _{GS} = 10 V	100			
100	0.023 at V _{GS} = 4.5 V	85			

FEATURES

- TrenchFET® Power MOSFET
- 175 °C Maximum Junction Temperature
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter			Limit	Unit	
Drain-Source Voltage			100	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I_	100	A	
	T _C = 125 °C	I _D	75 ^a		
Pulsed Drain Current	I _{DM}	300	A		
Avalanche Current	L = 0.1 mH	I _{AS}	75		
Single Pulse Avalanche Energy ^b	L = 0.1 IIII I	E _{AS}	280	mJ	
Mariana Barra Birain ati adb	T _C = 25 °C (TO-220AB and TO-263)	P _D	250 ^c	W	
Maximum Power Dissipation ^b	T _A = 25 °C (TO-263) ^d		3.75	VV	
Operating Junction and Storage Temperate	T_J, T_{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Limit	Unit		
Junction-to-Ambient	PCB Mount (TO-263) ^d	R _{thJA}	40	°C/W		
Junction-to-Ambient	Free Air (TO-220AB)	' 'thJA	62.5			
Junction-to-Case		R _{thJC}	0.6			

Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			v	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = 100 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C			50		
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 175 °C			250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
		V _{GS} = 10 V, I _D = 30 A		0.010		Ω	
		V _{GS} = 4.5 V, I _D = 20 A		0.023			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 30 A, T _J = 125 °C		0.020			
		V _{GS} = 10 V, I _D = 30 A, T _J = 175 °C		0.030		1	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	25			S	
Dynamic ^b							
Input Capacitance	C _{iss}			6550		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		665			
Reverse Transfer Capacitance	C _{rss}			265			
Total Gate Charge ^c	Q_g			105	160		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 85 \text{ A}$		17		nC	
Gate-Drain Charge ^c	Q _{gd}			23			
Turn-On Delay Time ^c	t _{d(on)}			12	25		
Rise Time ^c	t _r	$V_{DD} = 50 \text{ V}, R_{L} = 0.6 \Omega$		90	135	ns	
Turn-Off DelayTime ^c	t _{d(off)}	$I_D \cong 85 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		55	85		
Fall Time ^c	t _f	1		130	195		
Source-Drain Diode Ratings and Char	racteristics T _C	= 25 °C ^b	•				
Continuous Current	I _S				85	Α.	
Pulsed Current	I _{SM}				240	Α	
Forward Voltage ^a	V _{SD}	I _F = 85 A, V _{GS} = 0 V		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			85	140	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 50 A, dI/dt = 100 A/μs		4.5	7	Α	
Reverse Recovery Charge	Q _{rr}	1		0.17	0.35	μС	

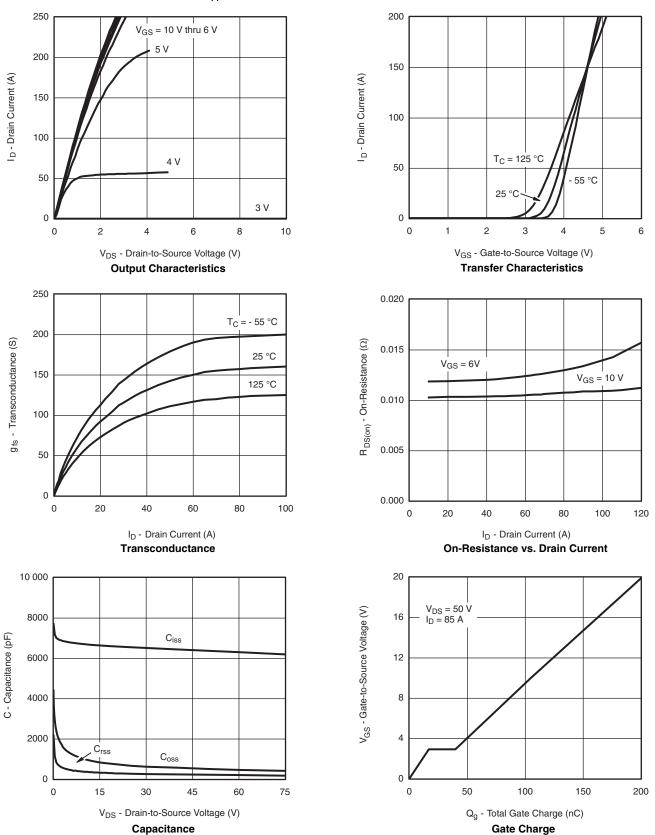
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

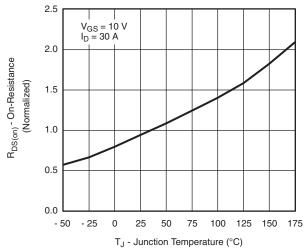


TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted

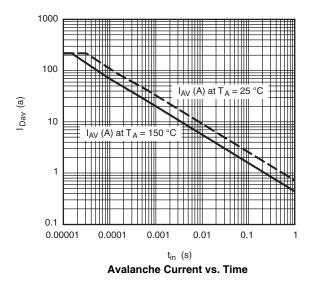




TYPICAL CHARACTERISTICS $T_A = 25 \, ^{\circ}C$, unless otherwise noted

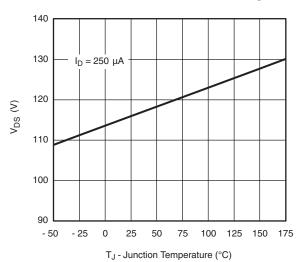


On-Resistance vs. Junction Temperature



T_J = 150 °C T_J = 25 °C T_J

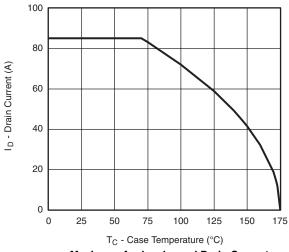
Source-Drain Diode Forward Voltage

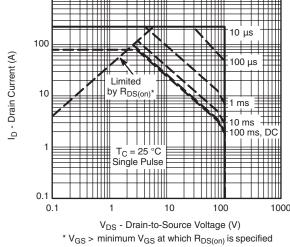


T_J - Drain-Source Breakdown vs. Junction-Temperature



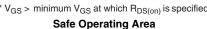
THERMAL RATINGS

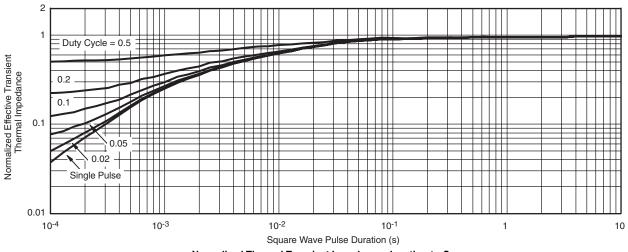




1000

Maximum Avalanche and Drain Current vs. Case Temperature





Normalized Thermal Transient Impedance, Junction-to-Case



MILLIMETERS

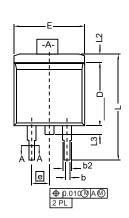
MAX.

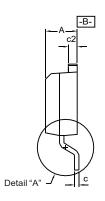
4.826

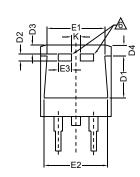
MIN.

4.064

TO-263 (D²PAK): 3-LEAD







INCHES

MAX.

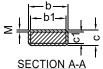
0.190

MIN.

0.160



DETAIL A (ROTATED 90°)



5.	ļ		b — 51—	1 .	ţ
~	1			2	- O
	SF	СТ	ION	A-A	. !

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

 6. This feature is for thick lead.

b		0.020	0.039	0.508	0.990	
b1		0.020	0.035	0.508	0.889	
b2		0.045	0.055	1.143	1.397	
с*	Thin lead	0.013	0.018	0.330	0.457	
	Thick lead	0.023	0.028	0.584	0.711	
с1	Thin lead	0.013	0.017	0.330	0.431	
	Thick lead	0.023	0.027	0.584	0.685	
	c2	0.045	0.055	1.143	1.397	
D		0.340	0.380	8.636	9.652	
D1		0.220	0.240	5.588	6.096	
D2		D2 0.038		0.965	1.067	
D3		D3 0.045		1.143	1.397	
D4		0.044	0.052	1.118	1.321	
E		0.380	0.410	9.652	10.414	
E1		0.245	-	6.223	-	
E2		0.355	0.375	9.017	9.525	
	E3	0.072	0.078	1.829	1.981	
	е	0.100	BSC	2.54 BSC		
K		0.045	0.055	1.143	1.397	
L		0.575	0.625	14.605	15.875	
L1		L1 0.090		2.286	2.794	
L2		L2 0.040		1.016	1.397	
	L3 0.050		0.070	1.270	1.778	
L4		0.010	BSC	0.254 BSC		
М		_	0.002	_	0.050	

ECN: T13-0707-Rev. K, 30-Sep-13

DWG: 5843

DIM.



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.