

N-Channel Enhancement Mode MOSFET

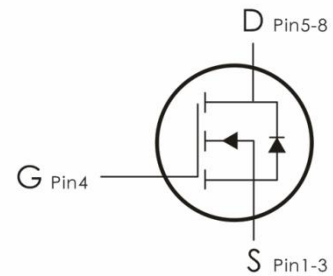
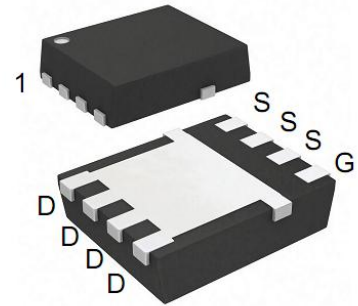
Description:

This N-Channel MOSFET uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=60V, I_D=200A, R_{DS(on)} < 2.3m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹⁾	200	A
$I_{D, pulse}$	Pulsed Drain Current ²⁾	600	
I_S	Continuous diode forward current ¹⁾	200	A
$I_{S, Pulse}$	Diode pulsed current ²⁾	600	A
E_{AS}	Single Pulse Avalanche Energy ⁵⁾	240	mJ
P_D	Power Dissipation ³⁾	132	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^{\circ}C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.95	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance Junction to mbient ⁴⁾	62	

Package Marking and Ordering Information:

Part NO.	Marking	Package
CSD18540Q5B	D18540	DFN5*6-8

Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250 μ A	60	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =60V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	---	2.5	V
R _{DS(ON)}	Drain-Source On Resistance	V _{GS} =10V,I _D =30A	---	2.0	2.3	m Ω
		V _{GS} =4.5V,I _D =30A	---	2.8	3.5	m Ω
R _G	Gate resistance	f=1 MHz, Open drain	---	1.8	---	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	6600	---	pF
C _{oss}	Output Capacitance		---	1250	---	
C _{rss}	Reverse Transfer Capacitance		---	58	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =50V,I _D =50A, R _G =2Ω,V _{GS} =10V	---	32	---	ns
t _r	Rise Time		---	50	---	ns
t _{d(off)}	Turn-Off Delay Time		---	90	---	ns
t _f	Fall Time		---	23	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =50V, I _D =50A	---	100	---	nC
Q _{gs}	Gate-Source Charge		---	17	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	16	---	nC
V _{plateau}	Gate plateau voltage		---	3.1	---	V
Drain-Source Diode Characteristics						

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{SD}	Source-Drain Diode Forward Voltage	$V_{GS}=0V, I_S=20A$	---	---	1.3	V
I_{rrm}	Peak reverse recovery current	$V_R=50V, I_S=50A,$ $di/dt=100A/us$	---	2.5	---	A
Q_{rr}	Reverse Recovery Charge		---	130	---	nC
T_{rr}	Reverse Recovery Time		---	86.8	---	ns

Notes:

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a=25^\circ C$.
- 5) $V_{DD}=50V, V_{GS}=10V, L=0.3mH$, starting $T_j=25^\circ C$.

Typical Characteristics: ($T_c=25^\circ C$ unless otherwise noted)

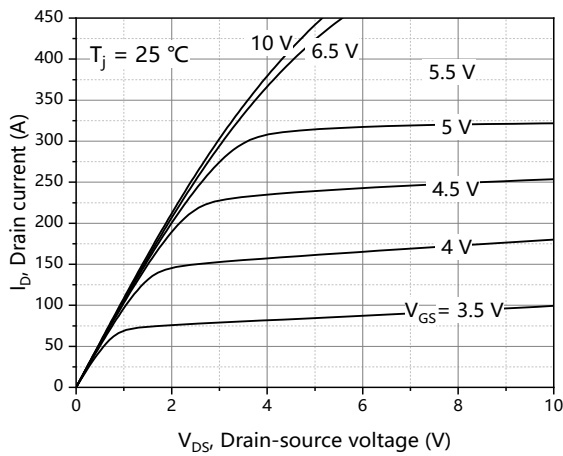


Figure 1. Typ. output characteristics

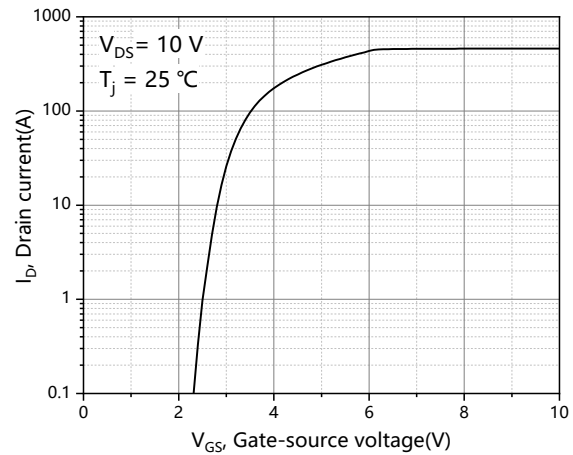


Figure 2. Typ. transfer characteristics

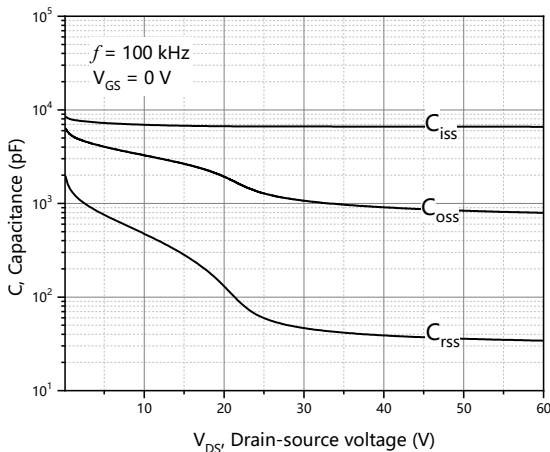


Figure 3. Typ. capacitances

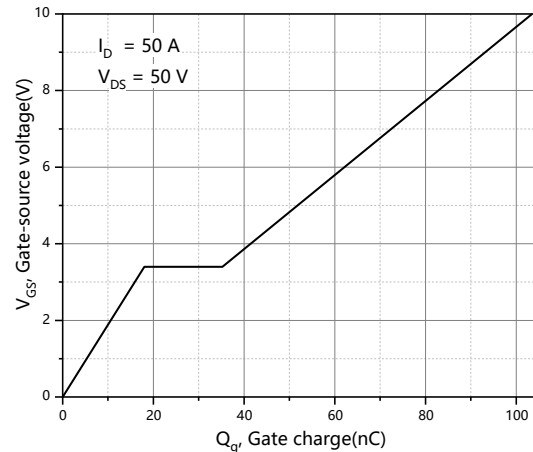


Figure 4. Typ. gate charge

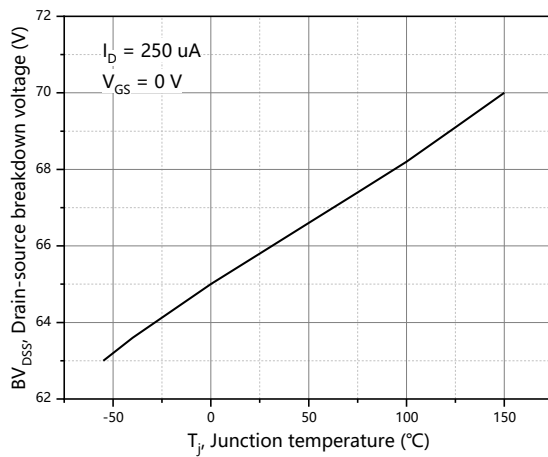


Figure 5. Drain-source breakdown voltage

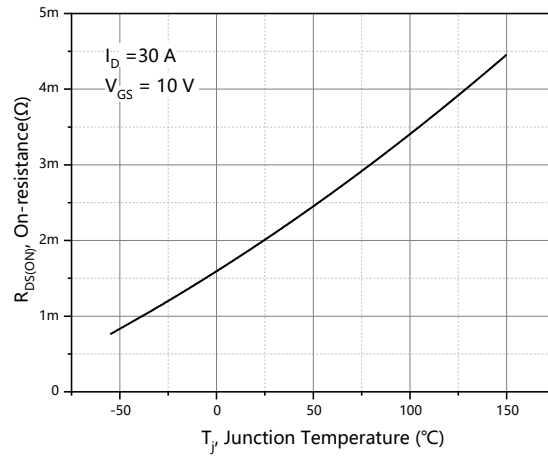


Figure 6. Drain-source on-state resistance

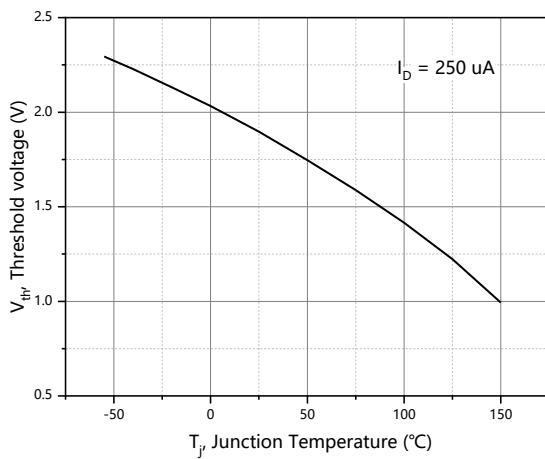


Figure 7. Threshold voltage

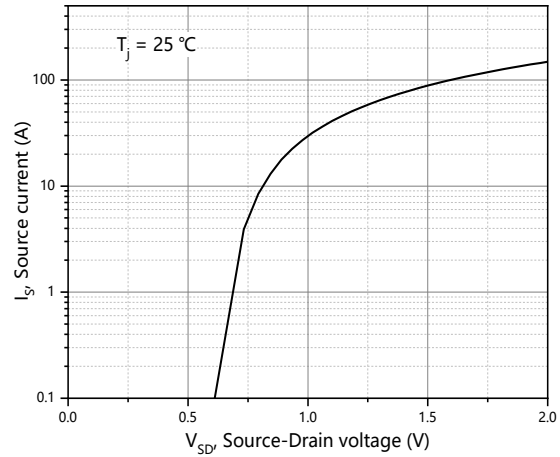


Figure 8. Forward characteristic of body diode

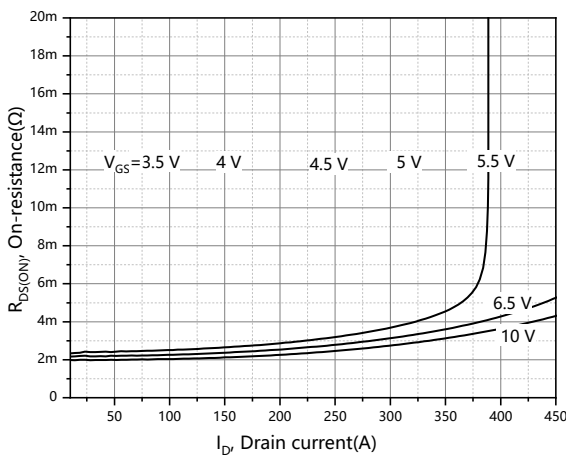


Figure 9. Drain-source on-state resistance

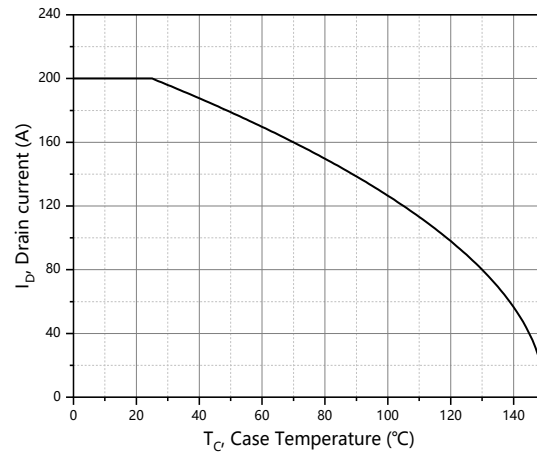


Figure 10. Drain current

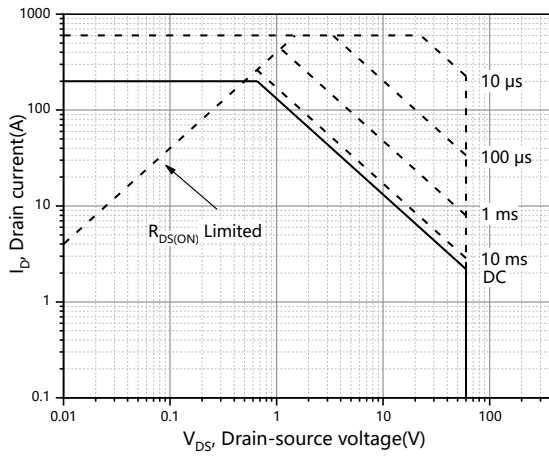


Figure 11. Safe operation area for $T_c = 25^\circ\text{C}$

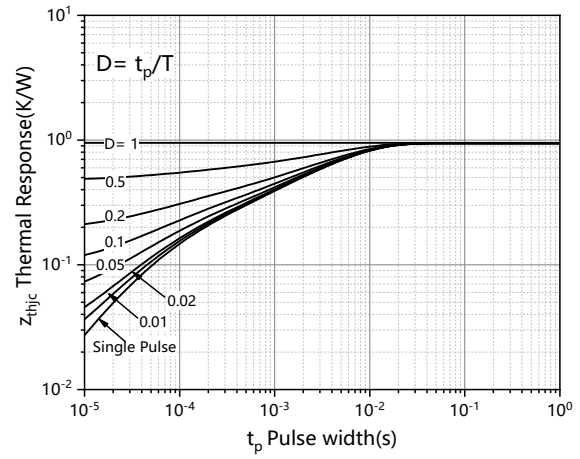


Figure 12. Max transient thermal impedance