

P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
- 100	0.50 at V _{GS} = - 10 V	- 1.5	7.7		
- 100	0.56 at V _{GS} = - 6.0 V	- 1.4	7.7		

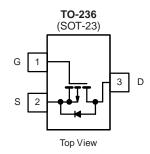
FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET
- Ultra Low On-Resistance
- Small Size



APPLICATIONS

• Active Clamp Circuits in DC/DC Power Supplies



ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 100		
Gate-Source Voltage		V_{GS}	± 20		V
Continuous Dunin Comment (T. 450 °C) a b	T _A = 25 °C	1	- 1.65	- 1.5	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C	- I _D	- 1.55	- 1.4	
Pulsed Drain Current		I _{DM}	- 3.0		Α
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 1.4	- 1.0	
Single Pulse Avalanche Current		I _{AS}	4.5		
Single Pulse Avalanche Energy	L = 1.0 mn	L = 1.0 mH		1.01	
M. D. D. J. J. ah	T _A = 25 °C	В	2.0	0.85	W
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	P _D	1.0	0.58	VV
Operating Junction and Storage Temperature Ran	T _J , T _{stg}	- 55	to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maniana basica ta Anti- da	t ≤ 5 s	D	75	100	
Maximum Junction-to-Ambient ^a	Steady State	R_{thJA}	120	166	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	50	1

Notes:

- a. Surface Mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.

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				Limits			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 100			٧	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Brain Current	1	V _{DS} = - 100 V, V _{GS} = 0 V			- 1	μА	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 100 V, V _{GS} = 0 V, T _J = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -15 \text{ V}, V_{GS} = 10 \text{ V}$	- 1.6			Α	
	D	$V_{GS} = -10 \text{ V}, I_D = -0.5 \text{ A}$		0.50		0	
Drain-Source On-Resistance ^a	R _{DS(on)}	V _{GS} = - 6.0 V, I _D = - 0.5 A		0.56		Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, I_{D} = -0.5 \text{ A}$		2.2		S	
Diode Forward Voltage	V _{SD}	I _S = - 1.0 A, V _{GS} = 0 V		0.7	- 1.2	V	
Dynamic ^b			•				
Total Gate Charge	Q_g	V 50VV 10V		7.7	12		
Gate-Source Charge	Q _{gs}	$V_{DS} = -50 \text{ V}, V_{GS} = 10 \text{ V},$ $I_{D} \cong -0.5 \text{ A}$		1.5		nC	
Gate-Drain Charge	Q_{gd}	1D = - 0.3 A		2.5			
Gate Resistance	R _g	f = 1.0 MHz		9		Ω	
Input Capacitance	C _{iss}			520			
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		40		pF	
Reverse Transfer Capacitance	C _{rss}			20		1	
Switching ^c							
Turn-On Time	t _{d(on)}	V 50V D 75 C		7	11		
Turn-On Time	t _r	$V_{DD} = -50 \text{ V}, R_L = 75 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -10 \text{ V}$	11	17	ns		
Turn-Off Time	t _{d(off)}	$R_a = 6 \Omega$	16 25			25	
Turn-On Time	t _f	· ·y = 3 = -		11	17		
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = 0.5 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$		90	135	nC	

Notes:

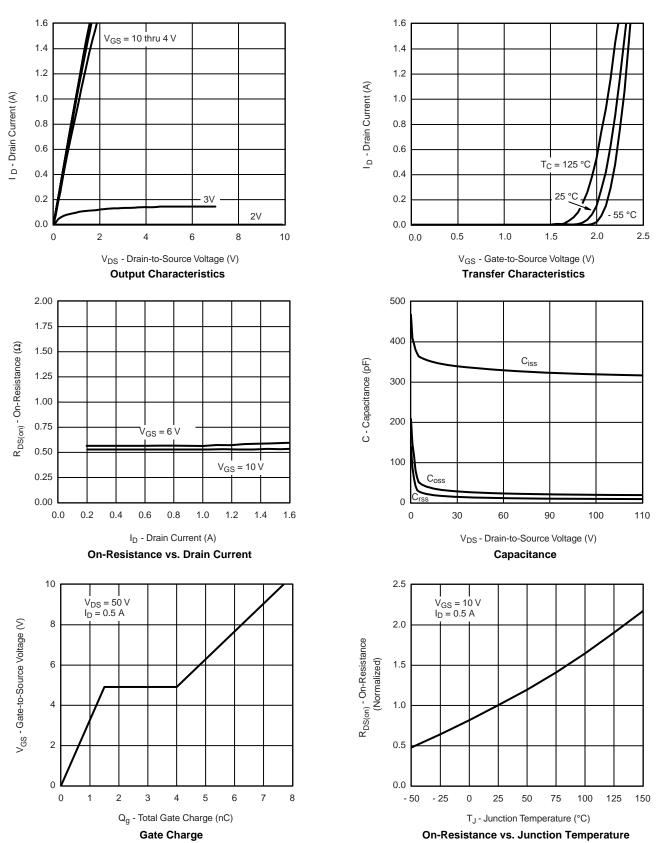
- a. Pulse test: PW \leq 300 μs duty cycle \leq 2 %. b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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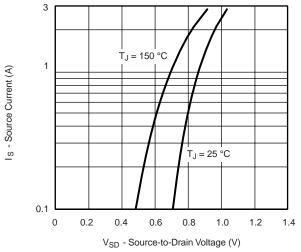


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

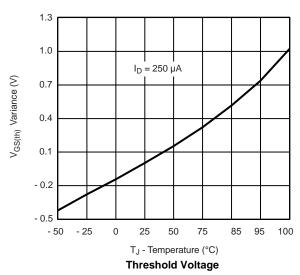


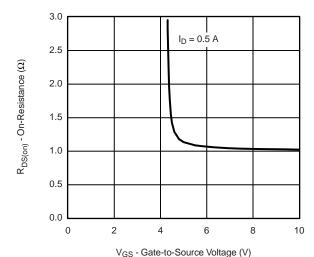


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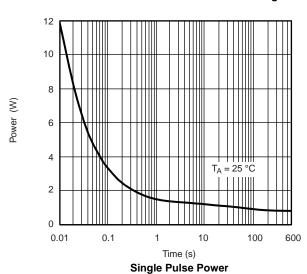


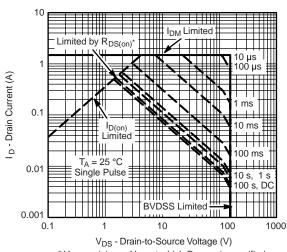
Source-Drain Diode Forward Voltage





On-Resistance vs. Gate-to-Source Voltage



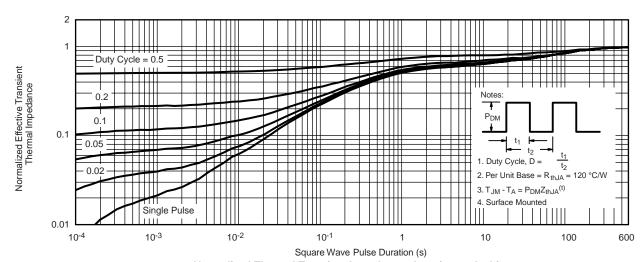


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area



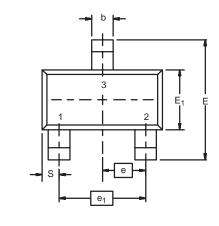
THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

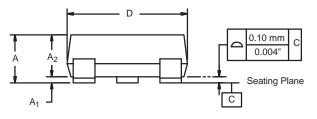


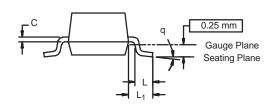
Normalized Thermal Transient Impedance, Junction-to-Ambient



SOT-23 (TO-236): 3-LEAD







Dim -	MILLIM	ETERS	INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e ₁	1.90 BSC		0.074	0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025	Ref		
S	0.50 Ref		0.020) Ref		
q	3°	8°	3°	8°		

DWG: 5479

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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