

MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

3400N-MS

Product specification

Description

The 3400N-MS is the high cell density trench N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

The 3400N-MS meet the RoHS and Green Product requirement with full function reliability approved.

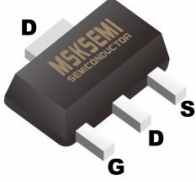
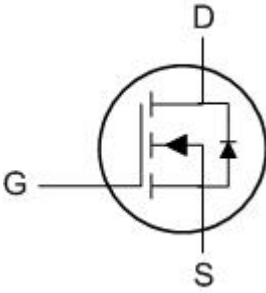

Product Summary

BVDSS	30V
RDSON	15mΩ
ID	7A

FEATURE

- Green Device Available
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Advanced high cell density Trench technology

Reference News

PACKAGE OUTLINE	PIN CONFIGURATION	Marking
 SOT-89		

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D@T_A=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V ¹	7.0	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, V_{GS} @ 10V ¹	4.5	A
I_{DM}	Pulsed Drain Current ²	25.4	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ³	2.5	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	92	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	---	°C/W

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±12V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.5	0.9	1.4	V
R _{DS(on)}	Static Drain-Source on-Resistance <small>note2</small>	V _{GS} = 10V, I _D =4.2A	-	20	28	mΩ
		V _{GS} =4.5V, I _D =4A	-	25	34	
		V _{GS} =2.5V, I _D = 1A	-	35	50	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 15V, V _{GS} =0V, f= 1.0MHz	-	602	-	pF
C _{oss}	Output Capacitance		-	56	-	pF
C _{rss}	Reverse Transfer Capacitance		-	42	-	pF
Q _g	Total Gate Charge	V _{DS} = 15V, I=4A, V _{GS} =4.5V	-	4.8	-	nC
Q _{gs}	Gate-Source Charge		-	1.2	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	1.7	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} = 15V, I _D =4A, R _{GEN} =3Ω, V _{GS} =4.5V	-	12	-	ns
t _r	Turn-on Rise Time		-	52	-	ns
t _{d(off)}	Turn-off Delay Time		-	17	-	ns
t _f	Turn-off Fall Time		-	10	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _s	Maximum Continuous Drain to Source Diode Forward Current		-	-	7.0	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	25.2	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _s =5.8A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

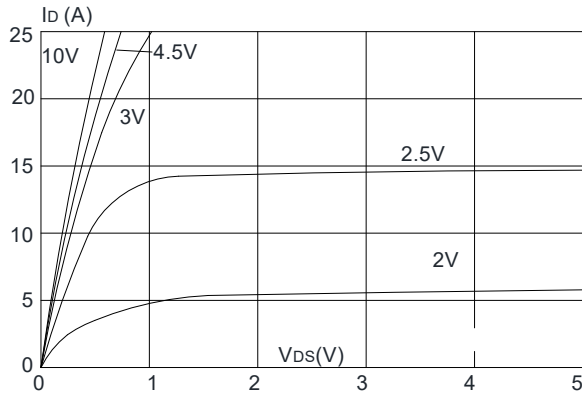


Figure 2: Typical Transfer Characteristics

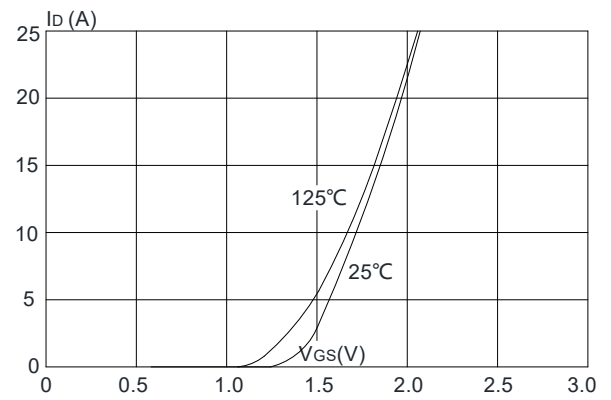


Figure 3: On-resistance vs. Drain Current

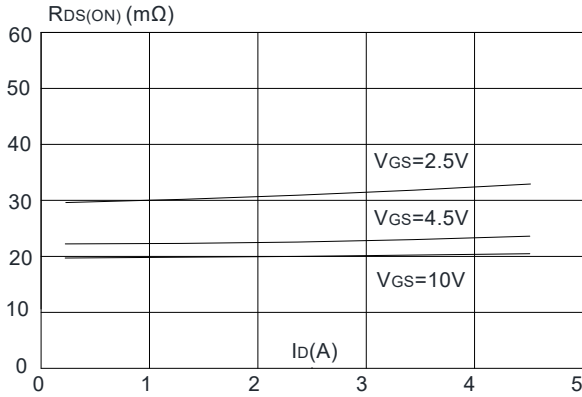


Figure 4: Body Diode Characteristics

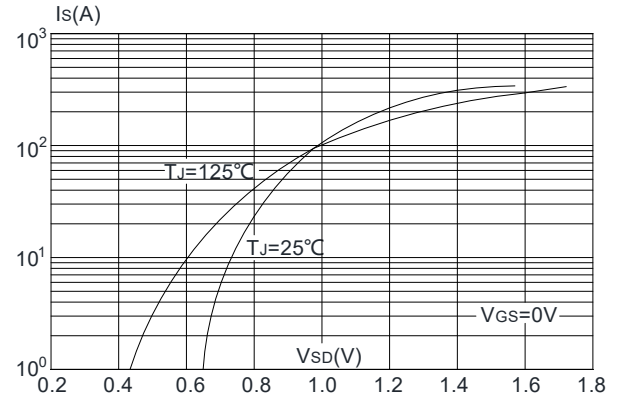


Figure 5: Gate Charge Characteristics

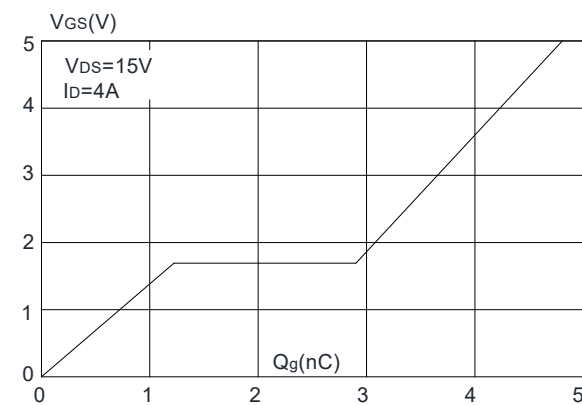


Figure 6: Capacitance Characteristics

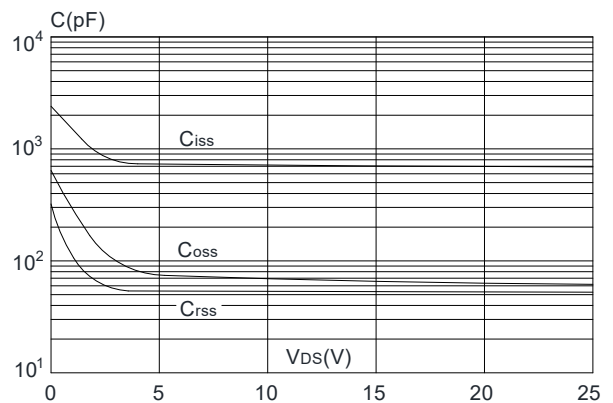


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

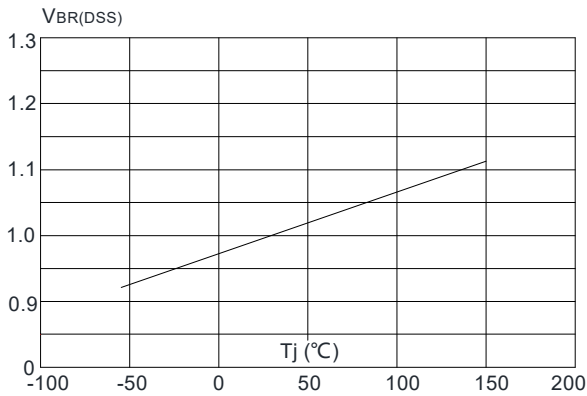


Figure 8: Normalized on Resistance vs. Junction Temperature

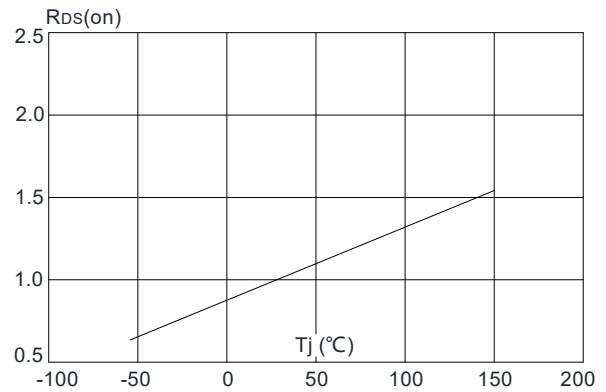


Figure 9: Maximum Safe Operating Area

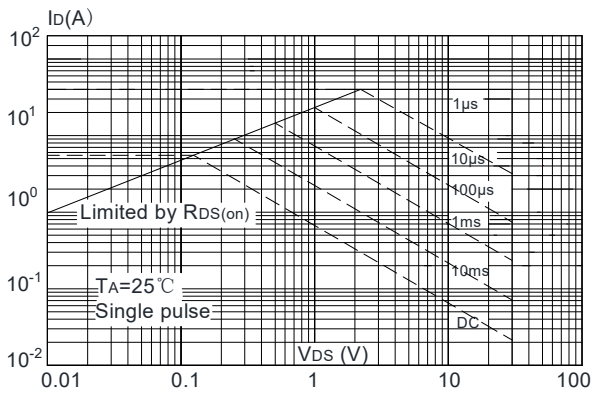


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

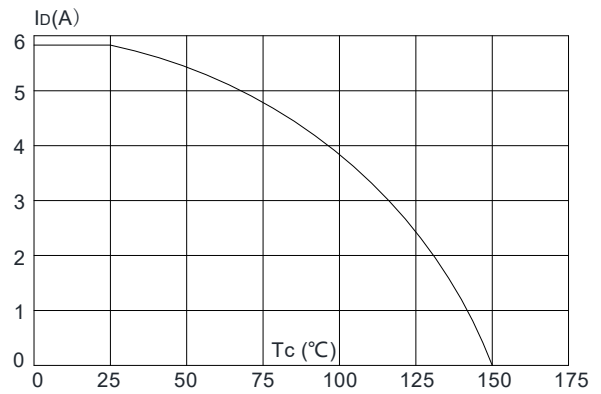
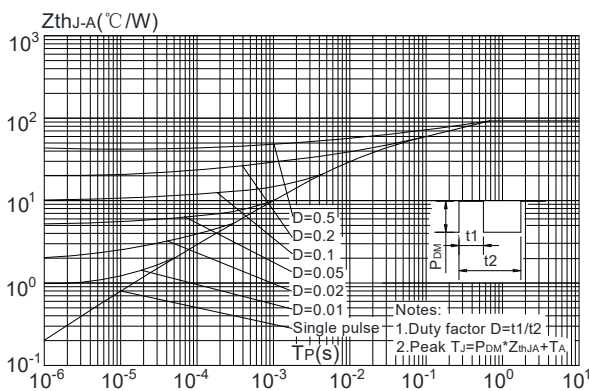
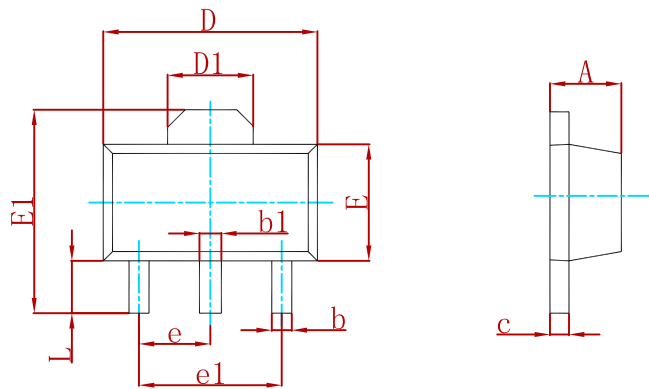


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

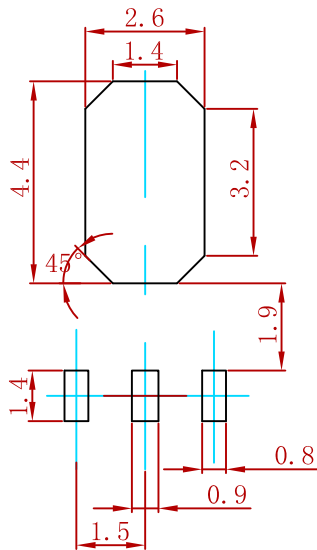


PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047

Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
3400N-MS	SOT-89	1000

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