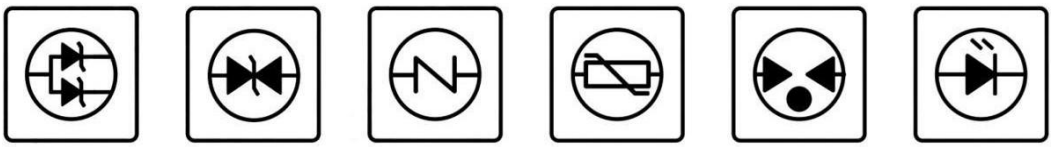
MSKSEMI 美森科
SEMICONDUCTOR



15N06-MS

Product specification

Description

The 15N06-MS is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The 15N06-MS meet the RoHS and Green Product.

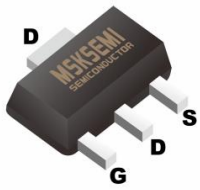
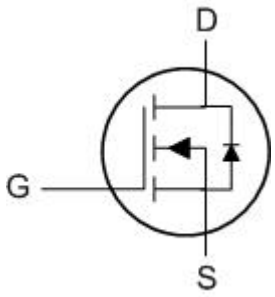
Product Summary

BVDSS	60V
RDSON	26mΩ
ID	15A

FEATURE

- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Reference News

PACKAGE OUTLINE	PIN CONFIGURATION	Marking
 SOT-89		15N06

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	15	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ 10V[1]	7.5	A
I_{DM}	Pulsed Drain Current[2]	22	A
EAS	Single Pulse Avalanche Energy[3]	22	mJ
I_{AS}	Avalanche Current	23	A
P_D@T_A=25℃	Total Power Dissipation[4]	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient [1]	---	85	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	25	℃/W

Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=60V, V_{GS} = 0V,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	1.0	1.6	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	V_{GS}= 10V, I_D=5A	-	26	40	mΩ
		V_{GS}=4.5V, I_D=3A	-	35	50	
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=25V, V_{GS}=0V, f= 1.0MHz	-	1148	-	pF
C_{oss}	Output Capacitance		-	58.5	-	pF
C_{rss}	Reverse Transfer Capacitance		-	49.4	-	pF
Q_g	Total Gate Charge	V_{DS}=30V, I_D=2.5A, V_{GS}= 10V	-	20.3	-	nC
Q_{gs}	Gate-Source Charge		-	3.7	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	5.3	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=30V, I_D=5A, R_G= 1.8Ω, V_{GS}=10V	-	7.6	-	ns
t_r	Turn-on Rise Time		-	20	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	15	-	ns
t_f	Turn-off Fall Time		-	24	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	20	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S=5A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I_F=5A, dI/dt= 100A/μs	-	29	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	43	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25℃ ,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25Ω,I_{AS}=8.7A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

Figure1: Output Characteristics

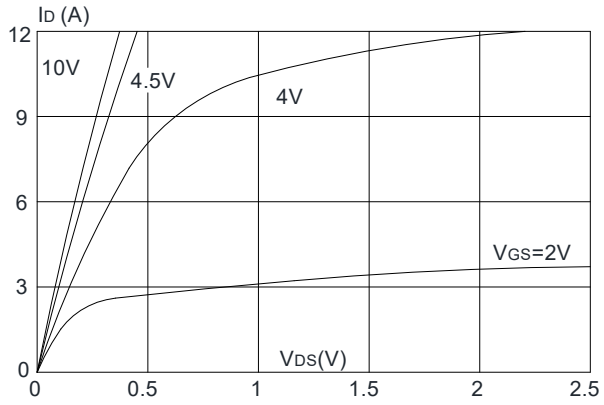


Figure 2: Typical Transfer Characteristics

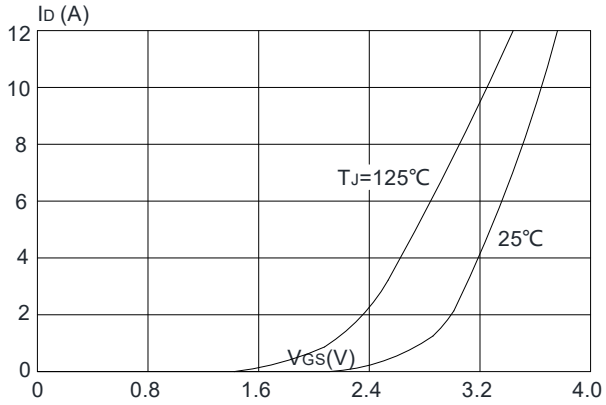


Figure 3: On-resistance vs. Drain Current

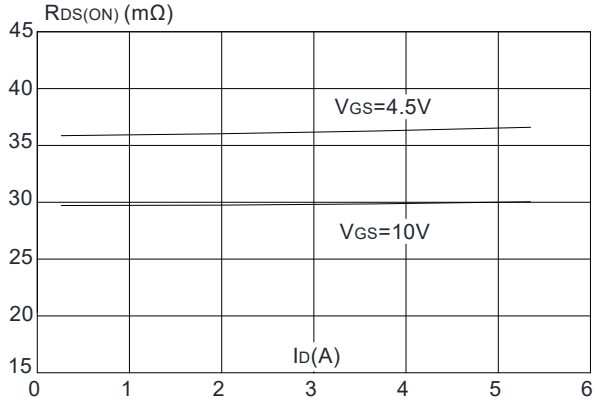


Figure 4: Body Diode Characteristics

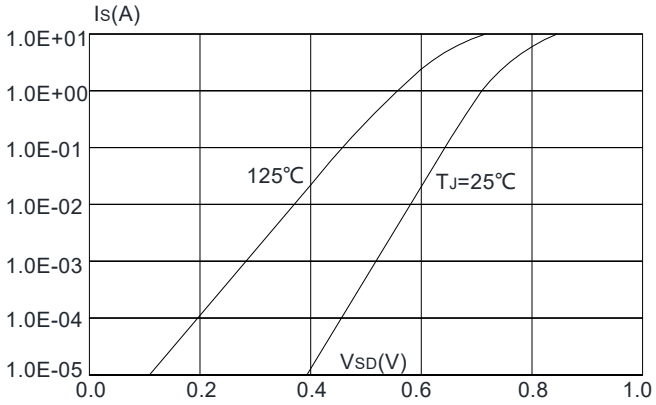


Figure 5: Gate Charge Characteristics

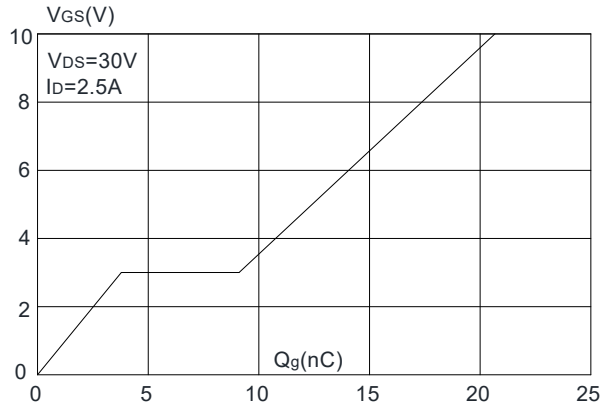


Figure 6: Capacitance Characteristics

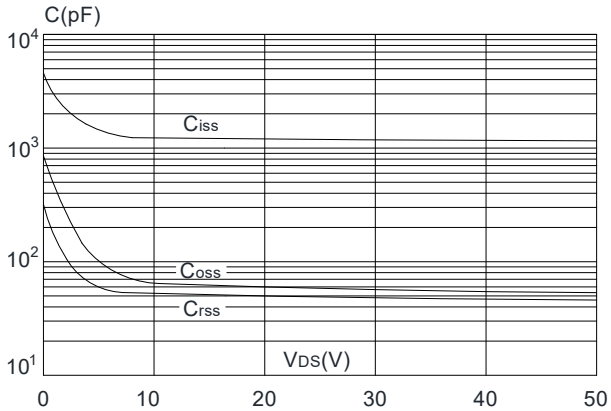


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

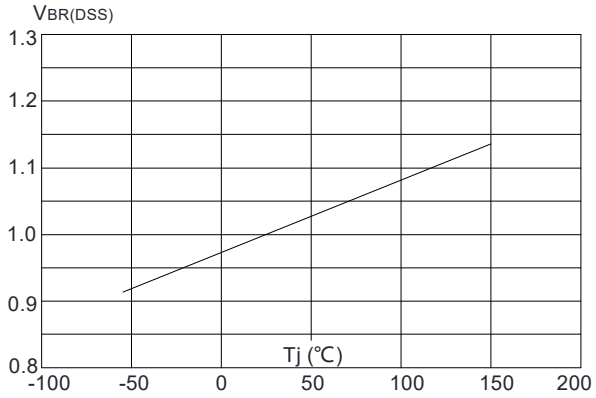


Figure 8: Normalized on Resistance vs. Junction Temperature

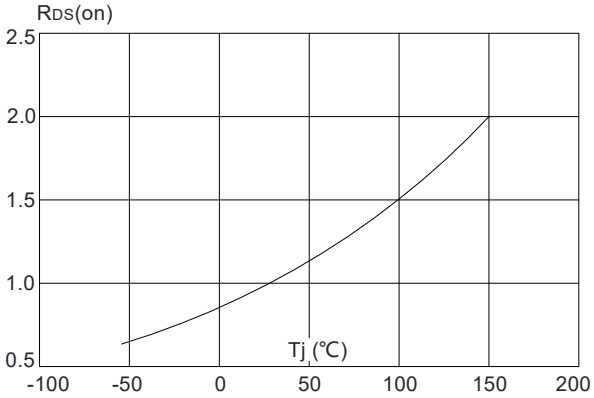


Figure 9: Maximum Safe Operating Area

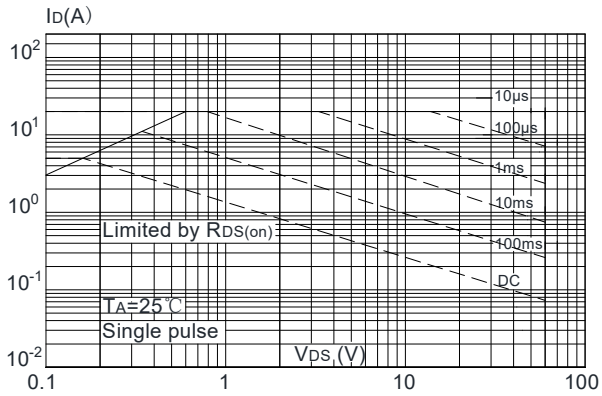


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

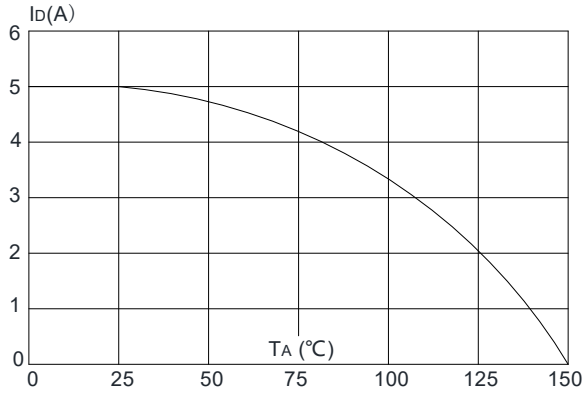
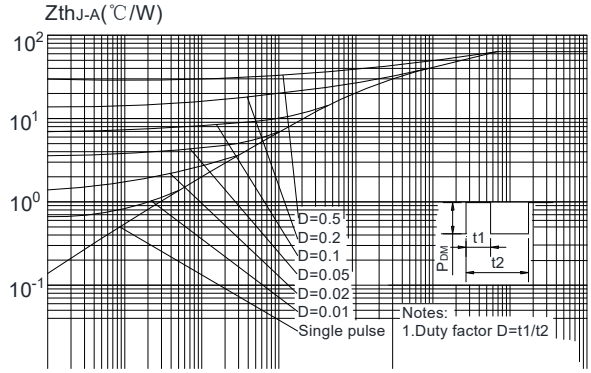
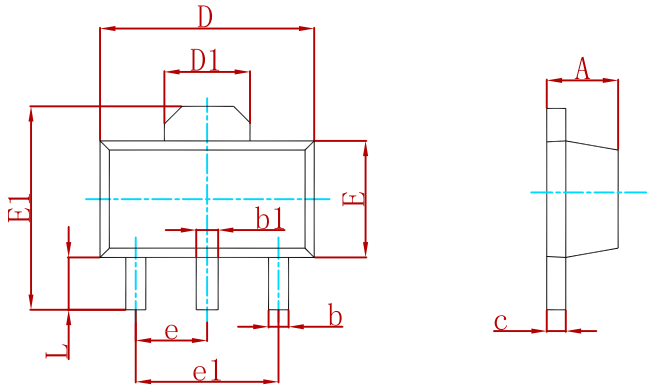


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

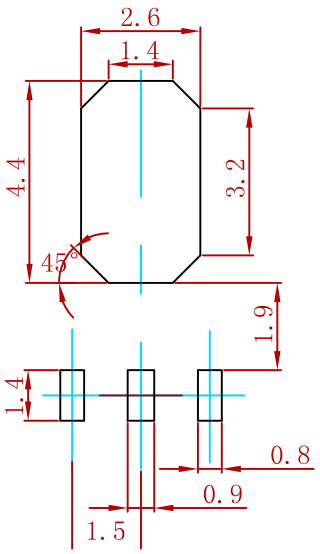


PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047

Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
15N06-MS	SOT-89	1000

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