

**WSR2N65** 

**N-Ch MOSFET** 

#### **General Description**

The WSR2N65 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSR2N65 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

#### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline

Absolute Maximum Ratings

• Green Device Available

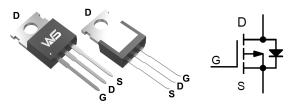
### **Product Summery**

| BV <sub>DSS</sub> | R <sub>DSON</sub> | I <sub>D</sub> |
|-------------------|-------------------|----------------|
| 650V              | 4.8Ω              | 2A             |

#### Applications

- AC/DC Power Conversion in Switched Mode Power Supplies (SMPS).
- Uninterruptible Power Supply(UPS)
- Adapter.

#### **TO-220 Pin Configuration**



#### Symbol **Parameter** Rating Units 650 V **Drain-Source Voltage** $V_{DS}$ V Gate-Source Voltage $\pm 30$ $V_{\text{GS}}$ Continuous Drain Current, V<sub>GS</sub> @ 10V<sup>1.5</sup> 2 I<sub>D</sub>@T<sub>C</sub>=25℃ А Continuous Drain Current, V<sub>GS</sub> @ 10V<sup>1.5</sup> 1 I<sub>D</sub>@T<sub>C</sub>=100℃ А Pulsed Drain Current<sup>1.2.5</sup> 6 А $I_{DM}$ Single Pulse Avalanche Energy<sup>1</sup> EAS 57 mJ PD W Total Power Dissipation<sup>1,5</sup> 25 Storage Temperature Range °C T<sub>STG</sub> -55 to 150 ΤJ °C **Operating Junction Temperature Range** -55 to 150

# Thermal Data

| Symbol           | Parameter  | Тур. | Max. | Unit |  |
|------------------|--|------|------|------|--|
| R <sub>ejA</sub> | Thermal Resistance Junction-ambient <sup>1</sup> |      | 62.5 | °C/W |  |
| R <sub>θJC</sub> | Thermal Resistance Junction-Case <sup>1</sup>    |      | 5    | °C/W |  |



N-Ch MOSFET

#### Electrical Characteristics (T<sub>J</sub>=25<sup>1</sup>C, unless otherwise noted)

| Symbol                               | Parameter                                      | Conditions  | Min. | Тур.  | Max. | Unit |
|--------------------------------------|--|---|------|-------|------|------|
| BV <sub>DSS</sub>                    | Drain-Source Breakdown Voltage                 | V <sub>GS</sub> =0V , I <sub>D</sub> =250uA                       | 650  |       |      | V    |
| $\triangle BV_{DSS} / \triangle T_J$ | BVDSS Temperature Coefficient                  | Reference to $25^{\circ}$ C , I <sub>D</sub> =250uA               |      | 0.6   |      | V/℃  |
| R <sub>DS(ON)</sub>                  | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =10V , I <sub>D</sub> =1A                         |      | 4.0   | 4.8  | Ω    |
| V <sub>GS(th)</sub>                  | Gate Threshold Voltage                         | —V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA         | 2.0  | 3.0   | 4.0  | V    |
| $	riangle V_{GS(th)}$                | V <sub>GS(th)</sub> Temperature Coefficient    |   |      | -4.57 |      | mV/℃ |
|                                      | Drain-Source Leakage Current                   | $V_{DS}$ =650V , $V_{GS}$ =0V , TJ=25 $^\circ$ C                  |      |       | 1    | uA   |
| I <sub>DSS</sub>                     |  | V <sub>DS</sub> =520V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃ |      |       | 10   |      |
| I <sub>GSS</sub>                     | Gate-Source Leakage Current                    | $V_{GS}=\pm30V$ , $V_{DS}=0V$                                     |      |       | ±100 | nA   |
| gfs                                  | Forward Transconductance                       | V <sub>DS</sub> =300V , I <sub>D</sub> =1A                        |      | 5     |      | S    |
| Qg                                   | Total Gate Charge (10V)                        | V <sub>DS</sub> =520V , V <sub>GS</sub> =10V , I <sub>D</sub> =1A |      | 8.0   |      |      |
| Q <sub>gs</sub>                      | Gate-Source Charge                             |   |      | 1.2   |      | nC   |
| Q <sub>gd</sub>                      | Gate-Drain Charge                              |   |      | 5     |      |      |
| T <sub>d(on)</sub>                   | Turn-On Delay Time                             |   |      | 7.8   |      |      |
| Tr                                   | Rise Time                                      | V <sub>DD</sub> =300V , V <sub>GS</sub> =10V ,                    |      | 33    |      | ns   |
| T <sub>d(off)</sub>                  | Turn-Off Delay Time                            | R <sub>G</sub> =25Ω, I <sub>D</sub> =1A                           |      | 23    |      |      |
| T <sub>f</sub>                       | Fall Time                                      |   |      | 59    |      |      |
| C <sub>iss</sub>                     | Input Capacitance                              | V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz               |      | 310   |      |      |
| Coss                                 | Output Capacitance                             |   |      | 39    |      | pF   |
| C <sub>rss</sub>                     | Reverse Transfer Capacitance                   |   |      | 6     |      |      |

### **Diode Characteristics**

| Symbol          | Parameter                                  | Conditions   | Min. | Тур. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| Is              | Continuous Source Current <sup>1,2,5</sup> |  |      |      | 2    | А    |
| I <sub>SM</sub> | Pulsed Source Current <sup>1,2</sup>       | $V_G = V_D = 0V$ , Force Current                               |      |      | 6    | А    |
| V <sub>SD</sub> | Diode Forward Voltage <sup>1</sup>         | V <sub>GS</sub> =0V , I <sub>S</sub> =2A , T <sub>J</sub> =25℃ |      |      | 1.4  | V    |
| t <sub>rr</sub> | Reverse Recovery Time                      |  |      | 80   |      | nS   |
| Qrr             | Reverse Recovery Charge                    | l⊧=2A , dl/dt=100A/µs  |      | 1800 |      | nC   |

#### Notes:

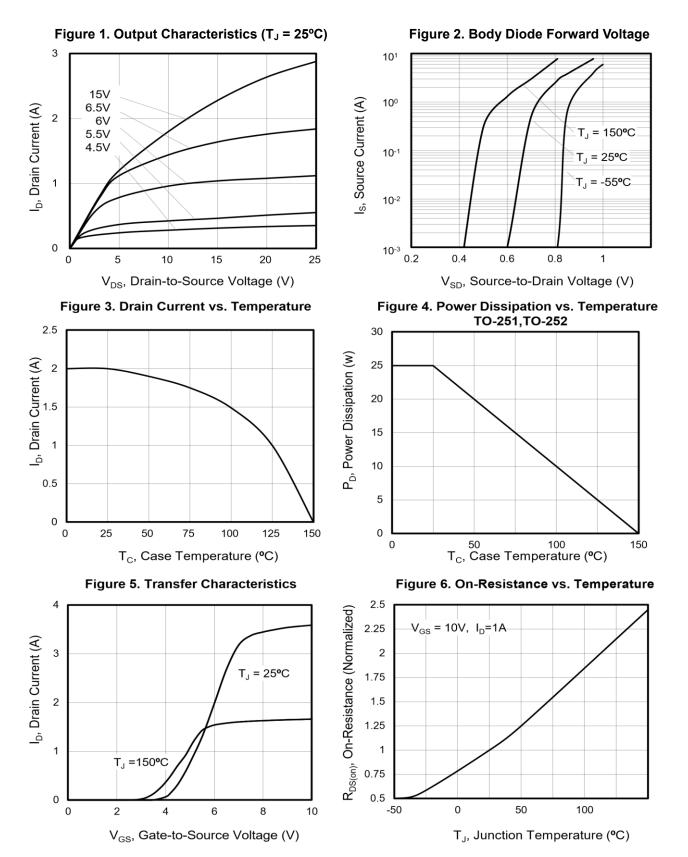
- Note 1 : limited by maximum junction temperature.
- Note 2 : Bond wire current limit.
- Note 3 :  $V_{DS}$ =520V,  $I_D$ =2A.
- Note 4 :  $I_D=1A$ ,  $V_{DD}=50V$ ,  $T_j=25^{\circ}C$ .
- Note 5 : Repetitive Rating : Pulse width limited by maximum junction temperature.



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# **Typical Characteristics**

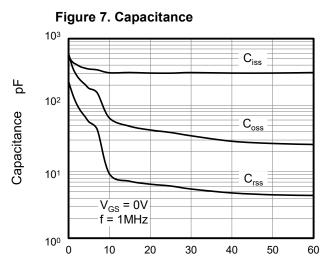




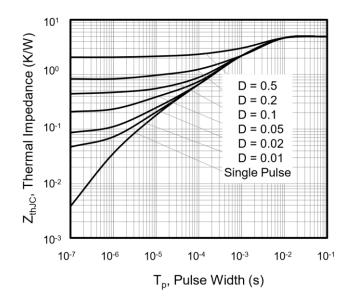
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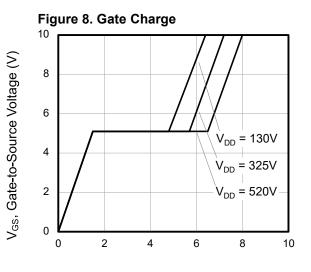
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# **Typical Characteristics**



V<sub>DS</sub>, Drain-to-Source Voltage (V) Figure 9. Transient Thermal Impedance





Qg, Total Gate Charge (nC)



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