

QR-Mode Flyback Converter with Integrated Protections

Features

- Internal 650V GaN HEMT
- Low Start-Up Current
- Soft Driving to Reduce EMI
- Multi-Mode Operation (QR/DCM/Burst)
 - QR Operation @ Full/Medium Load
 - DCM Operation @ Light Load
 - Burst Mode at No Load
- Switching frequency is up to 160KHz
- 8ms Soft-start
- Over Current Protection (OCP)
- Over Load Protection (OLP)
- Short Circuit Protection (SCP)
- Brown in/out on ZCD Pin
- Over Voltage Protection on VCC Pin
- Over Voltage Protection on ZCD Pin
- On Chip OTP Protection
- DFN5x6 Package

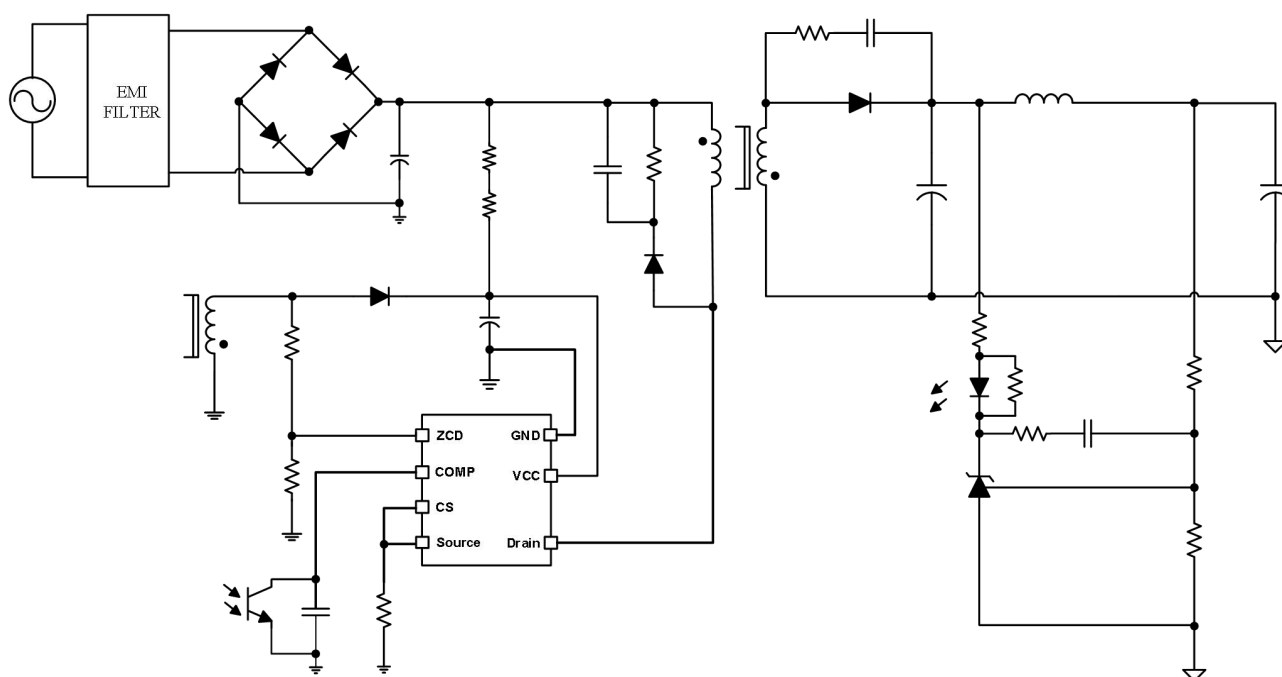
Description

GN9235Q is integrated a high performance multi-mode (QR/DCM/Burst) PWM converter and a 650V GaN MOSFET. It could turn standby mode to green mode for high switching efficiency. It provides functions of low startup current, over current protection, over voltage protection, short circuit protection, brown in/out protection, and internal OTP to prevent the circuit being damaged.

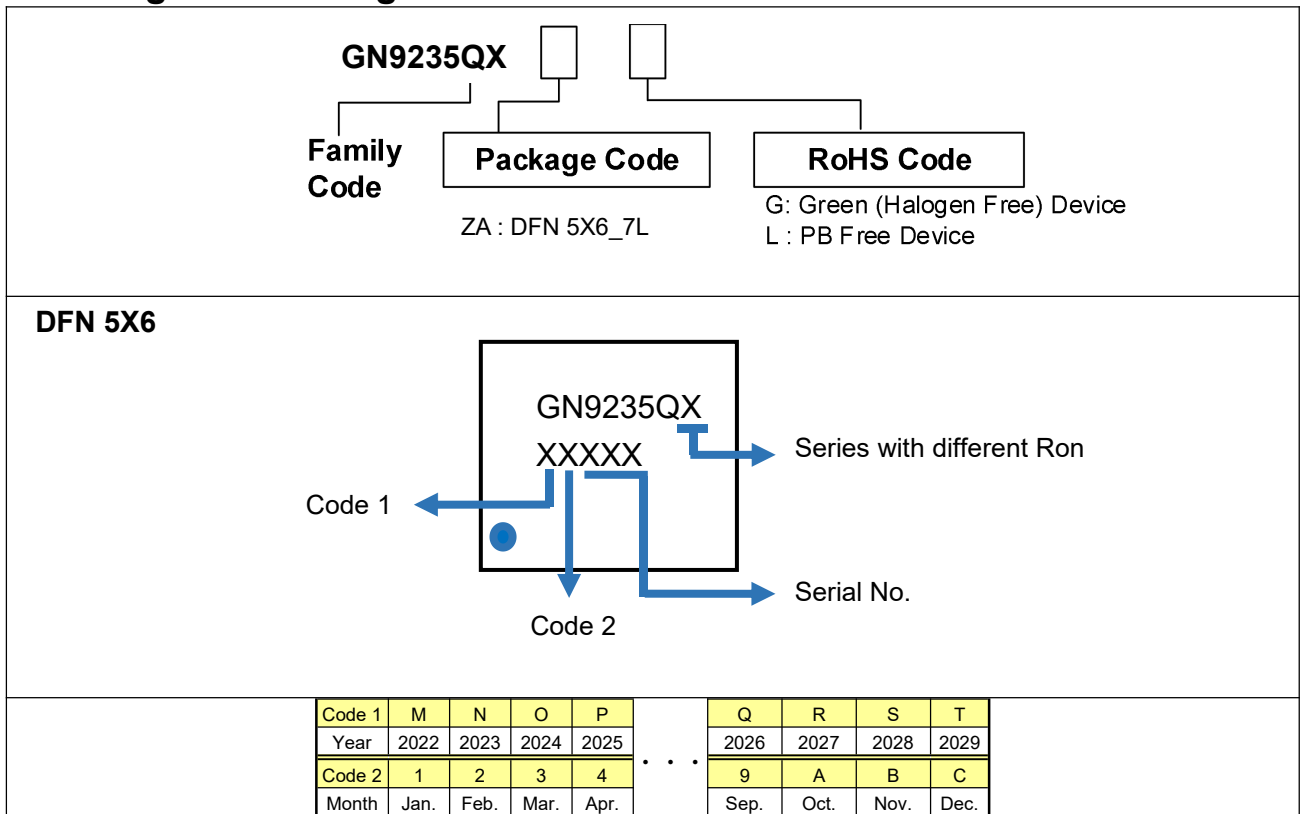
Application

- PD+QC Charger
- Switching Mode Power Supply

Typical Application Information

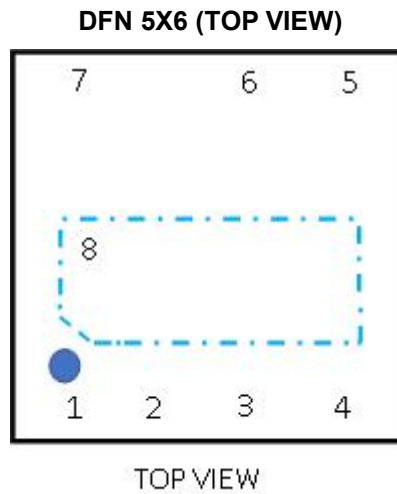


Ordering and Marking Information



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Pin Configuration



Pin Description

Pin No.	Name	Function
1	SOURCE	Source of the power MOSFET
2	CS	Current sense pin, connected to sense resistor for sensing the MOSFET current signal
3	COMP	Voltage feedback pin, by connecting a photo-coupler to control the duty cycle
4	ZCD	This pin is for quasi-resonant detection, OVP and Brown in/out.
5,8	GND	Ground reference pin
6	VCC	Power supply pin
7	DRAIN	Drain of the power MOSFET

Absolute Maximum Ratings

Supply voltage VCC	30V
COMP, CS, ZCD, SOURCE	-0.3~6.0V
DRAIN	-0.3~650V
Junction temperature	150℃
Storage temperature range	-65℃ ~ 150℃
DFN 5X6 package thermal resistance	45℃/W
Power dissipation(DFN 5X6, at ambient temperature 85℃)	250mW
Lead temperature(DFN 5X6 , soldering, 10 sec)	230℃
Lead temperature (All Pb free packages, soldering, 10 sec)	260℃
ESD, human body model	2.5KV
ESD, machine model	250V

Caution: The “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed and may cause permanent damage to the IC. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the Electrical Characteristics section of the specification is not implied. The “Electrical Characteristics” table defines the conditions for actual device operation. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

Recommended Operating Conditions

Item	Min.	Max.	Unit
Operating Junction temperature	-40	125	℃
Operating ambient temperature	-40	85	℃
Start Up Resistor (DC bus side)	3M	4.7M	Ω
Supply voltage VCC	10	26	V
VCC Capacitor	2.2	10	μF
COMP pin paralleling capacitor	0.1	10	nF

Note:

- Not to exceed the maximum junction temperature of the IC, this relates to the operating power of the IC and the thermal resistance of the IC-package as above.
- The small signal components should be placed close to IC pin as possible.
- An electrolytic capacitor or 1206 SMD ceramic capacitor is recommend for VCC-GND to avoid noise of Resonance
- It's essential to connect VCC pin with a SMD ceramic capacitor (0.1μF~0.47μF) to filter out the undesired switching noise for stable operation.
- Connecting a capacitor to COMP pin is also essential to filter out the undesired switching noise for stable operation.

Electrical Characteristics (TA = +25°C unless otherwise stated, VCC = 15.0V)

Parameter		Min.	Typ.	Max.	Unit
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SUPPLY VOLTAGE (VCC Pin)

Startup current VCC=UVLO ON-0.1V			2	5	uA
Operating current (with 1nF load on OUT pin), Vcomp = 0V		0.7	1.0	1.3	mA
Operating current (with 1nF load on OUT pin), Vcomp = 2.5V			5		mA
Operating current (with 1nF load on OUT pin), protection tripped (VCC OVP, OLP, ZCD OVP/ULP)		0.8	1	1.2	mA
UVLO-OFF		7	8	9	V
UVLO-ON		15	16.0	17	V
VCC Mode Entry Point			8.5		V
Hysteresis			0.5		V
OVP level on VCC pin		26.5	28	29.5	V
OVP level on VCC pin Debounce Time*			100		μS

VOLTAGE FEEDBACK (COMP Pin)

Short circuit current, Vcomp = 0V		150	200	250	uA
Open loop voltage, COMP pin open		4.9	5.3	5.7	V
Green Mode Start voltage			3.6		V
Green Mode end voltage			1.9		V
Burst Mode voltage		0.95	1.0	1.05	V
Hysteresis			100		mV
OLP trip level, Vcomp		4.2	4.4	4.6	V
OLP delay time			60		ms

CURRENT SENSING (CS Pin)

Maximum input voltage, VCS(OFF)		0.80	0.85	0.90	V
Leading-edge blanking time			250		nS
Input impedance		1			MΩ
Delay to Output*			100		nS
The protection level of output diode Short circuit			0.9		V
De-bounce time of output diode Short circuit protection			4		Pulse

Electrical Characteristics (TA = +25°C unless otherwise stated, VCC = 15.0V)

Parameter		Min.	Typ.	Max.	Unit
ZCD (ZCD Pin)					
Upper Clamp Level, IZCD=1mA		4	4.6	5.2	V
Lower Clamp Level, IZCD=-1mA		-0.8	-0.5		V
ZCD Blanking Time*			2.5		μs
ZCD OVP		3.4	3.5	3.6	V
OVP OVP De-bounce Time*			4		cycle
UVP Level		0.7	0.8	0.9	V
After UVP De-bounce Time			4		cycle
Brown in tripped level		105	110	115	uA
Brown out tripped level		95	100	105	uA
Brown out de-bounce time			72		mS

OSCILLATOR

QR max. Frequency			160		kHz
Green Mode Frequency		20	22	24	kHz
Jitter Frequency (CCM Mode)			±6		%

Soft Start Time (CS Pin)

Soft Start Time*		6	8	10	ms
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Internal OTP (Guaranteed by Design)

OTP*			150		°C
Hysteresis*			30		°C

GaN Device SECTION

BVdss Vgs=0		650			V
DC Rds_on Vg=10	GN9235QAZ		0.41		Ω
	GN9235QCZ		0.22		
	GN9235QDZ		0.12		

*Guaranteed by Design.

Typical Performance Characteristics

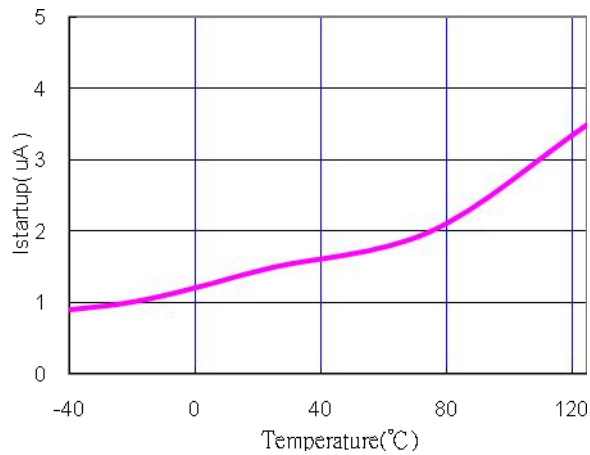


Fig. 1 Istartup current vs. Temperature

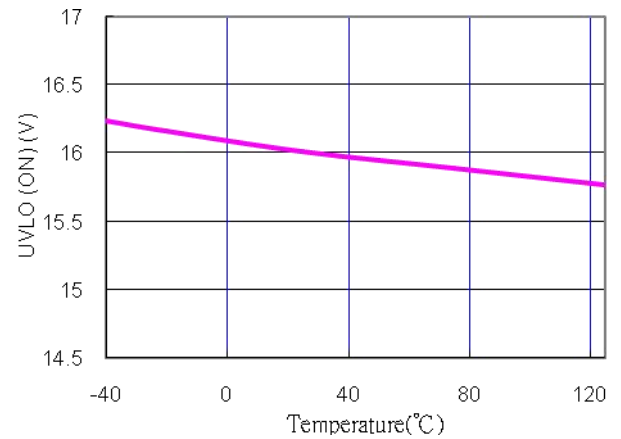


Fig. 2 UVLO (ON) vs. Temperature

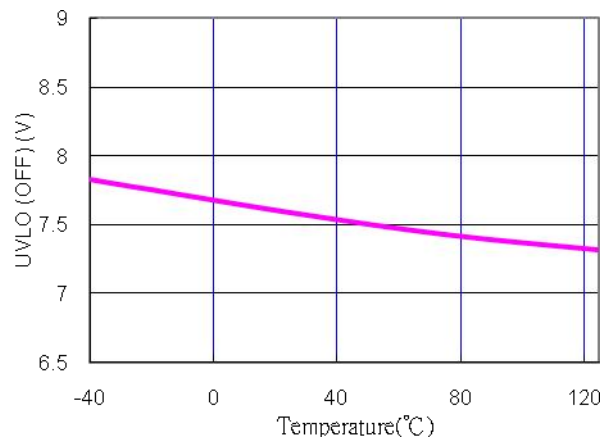


Fig. 3 UVLO (OFF) vs. Temperature

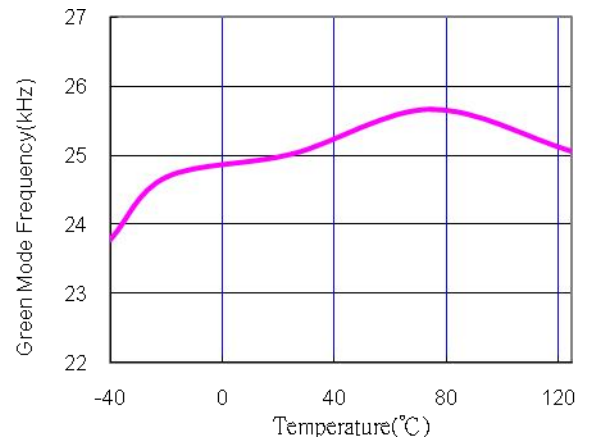


Fig. 4 Green Mode Frequency vs. Temperature

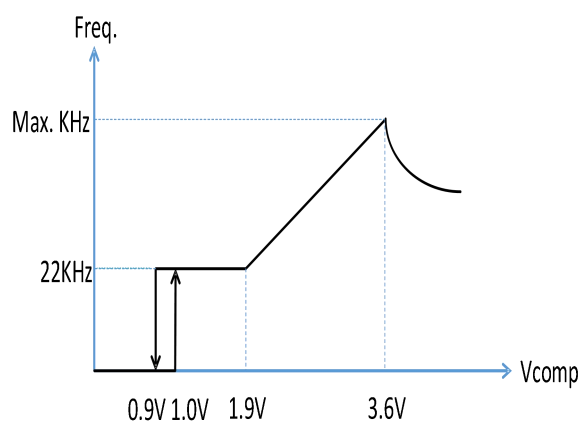


Fig. 5 VCC OVP vs. Temperature

Application Information

Overview

GN9235Q series is a high performance multi-mode (QR/DCM/Burst) PWM converter for flyback application. This results in a low-cost solution for low power AC/DC adapters. It integrated more functions to reduce the external components counts and the size. Its major features are described as below.

Start-up Current

The typical start-up current is 2uA. Very low start-up current allows the PWM converter to increase the value of start-up resistor and then reduce the power dissipation on it.

Under-voltage Lockout (UVLO)

A hysteresis UVLO comparator is implemented in GN9235Q series, then the turn-on and turn-off thresholds level are fixed at 16V and 8V respectively. This hysteresis ensures that the start-up capacitor will be adequate to supply the chip during start-up. The hysteresis is show in Fig. 7.

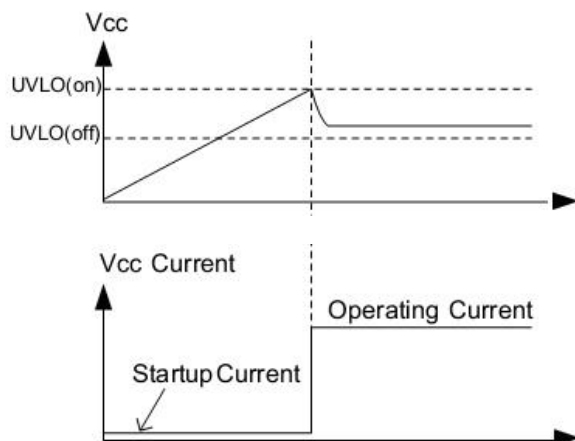


Fig.7

Multi-Mode Operation

GN9235Q is a multi-mode (QR/DCM/Burst) converter. The operation of GN9235Q series is changed by switching frequency depends on comp pin voltage, as show in Fig.8. At heavy load, it

operates on QR mode. At medium load, it operates on skip - valley QR mode. That is, when the load decrease, the system will automatically skips multi-wave valley for decreasing the switching frequency. And, when Vcomp is lower than burst mode voltage, the output of gate driver of GN9235Q series will be shut down for reinforcement Green Energy-Saving.

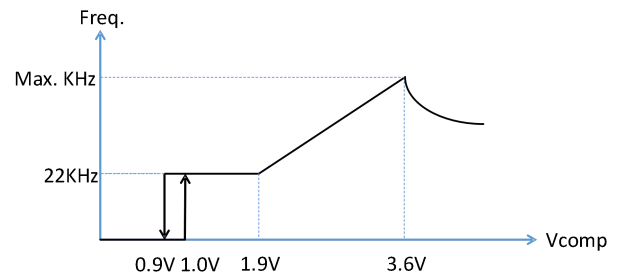


Fig.8

Quasi-Resonant Detection

ZCD pin detects the leak of transformer and resonance of parasitic capacitance of MOSFET and turn on the MOSFET at nearby trough of wave of resonance to reduce the power loss and improve switching efficiency.

Leading-edge Blanking (LEB)

Each time the power MOSFET is switched on, a turn-on spike will inevitably occur at the sense resistor. To avoid the OCP fault trigger, a leading-edge blanking time is built in. During this blanking period, the current-limit comparator is disabled and cannot switch off the gate driver.

Over-voltage Protection (OVP) on VCC Auto Recovery mode

Over-voltage Protection is implemented in GN9235Q for being damaged from abnormal condition. When VCC voltage is higher than OVP level, the output of Gate driver will be shut down. VCC OVP is auto-recovery type. Once the condition of over voltage happened, the Gate driver will be shut down until the next VCC turn on. This

operation is a Hiccup mode. (Fig.9)

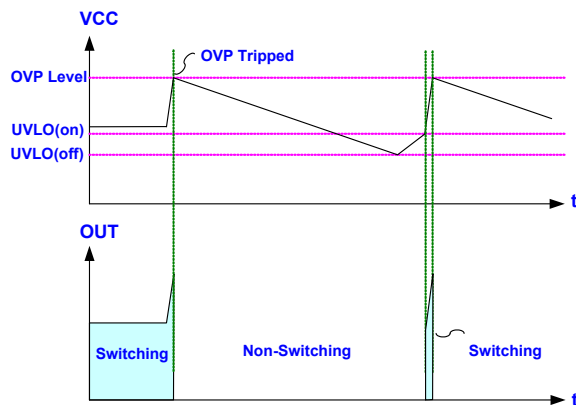


Fig.9

Output OVP on ZCD - Auto Recovery mode

An output overvoltage protection is implemented in the GN9235Q, as shown in Fig. 10. It senses the auxiliary voltage via the divided resistors. The overvoltage protection works by sampling the plateau voltage after a delay time. The sampling voltage level is compared with internal threshold voltage 3.5V. If the sampling voltage exceeds the ZCD OVP trip level, ZCD OVP circuit switches the power MOSFET off. ZCD OVP function is an auto-recovery type protection.

Output Under-voltage Protection (UVP) on ZCD- Auto Recovery mode

To protect the circuit from damage due to output short condition, an auto-recovery type of UVP protection is implemented for it. If the QRD voltage declines below 0.8V for over 4cycle, the protection will be activated to turn off the gate until the next VCC UVLO-ON.

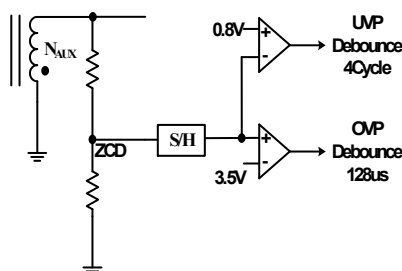


Fig.10

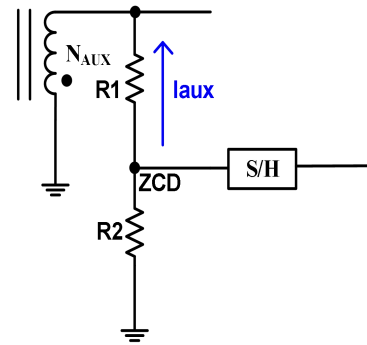


Fig.11

Brown-In/Brown-Out Protection- Auto Recovery mode

GN9235Q series have BNI/BNO function. This function is detected by internal circuit on ZCD pin, as show in Fig.11. If $I_{aux} > I_{BNI}$, the BNI Protection is triggered and the PWM starts to work. The de-bounce time is 2~5 CLK. During the BNI, If $I_{aux} < I_{BNO}$, the BNO protection is triggered and the PWM stops to work. The formula is as follows:

$$V_{ac_BNI} = \frac{N_p}{N_{aux}} * I_{bni} * R1 * \frac{1}{\sqrt{2}}$$

$$V_{ac_BNO} = \frac{N_p}{N_{aux}} * I_{bno} * R1 * \frac{1}{\sqrt{2}}$$

Internal High/Low Line OCP compensation

In order to get the consistency of the output over current protection trig point for the input AC voltage range that should compensate the trig point of the OCP value. The compensation value is proportional to the input AC voltage. The OCP compensation current flows through the internal resistors then makes the compensation voltage ΔV_{cs} offset.

Gate Clamp/Soft Driving

Driver output is clamped by an internal 12.5V clamping circuit to prevent from undesired over-voltage gate signals. And under the conditions listed below, the gate output will turn off immediately

to protect the power circuit.

- OCP
- SCP
- OVP
- OLP
- Brown out

The GN9235Q also has soft driving function to minimize EMI.

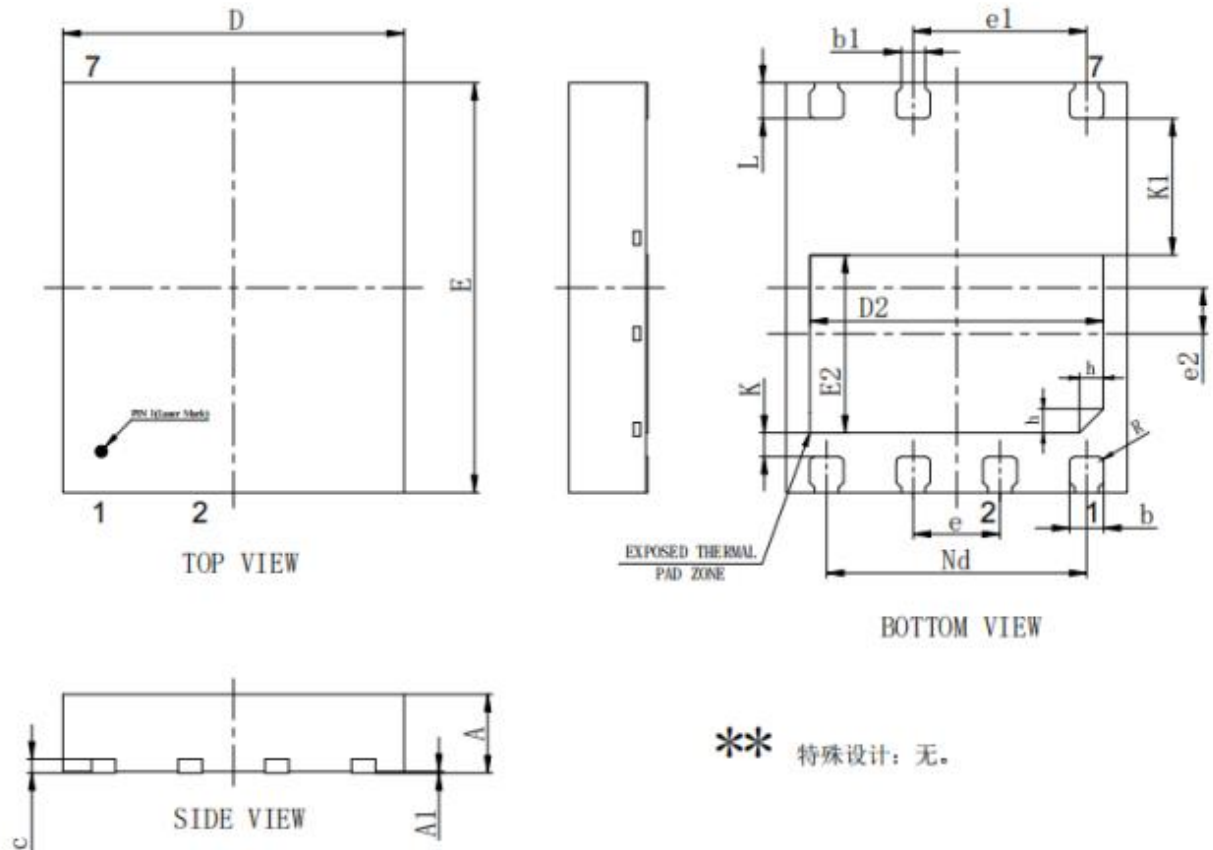
VCC Mode

In order to avoid the output voltage shut down by load changing from full to no load, GN9235Q is built-in the VCC mode function. When the load from full changes to no load, the output voltage will overshoot and pull low the COMP pin by feedback loop (Into burst mode). Thus the PWM duty will disappear and no power delivers to the secondary. If there is without any mechanism to prevent this situation, the VCC pin voltage will down to UVLO off and the IC will re-start again.

The VCC mode function is used to prevent the output re-start again when load changes. So never let the system operate on the VCC mode at no load. The system should operate on burst mode, otherwise the input power maybe become larger.

封装信息

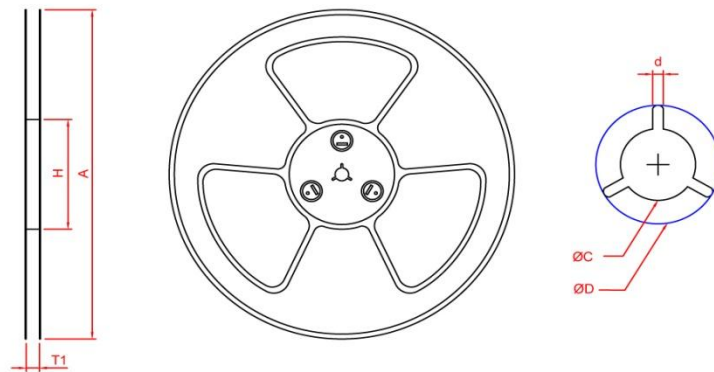
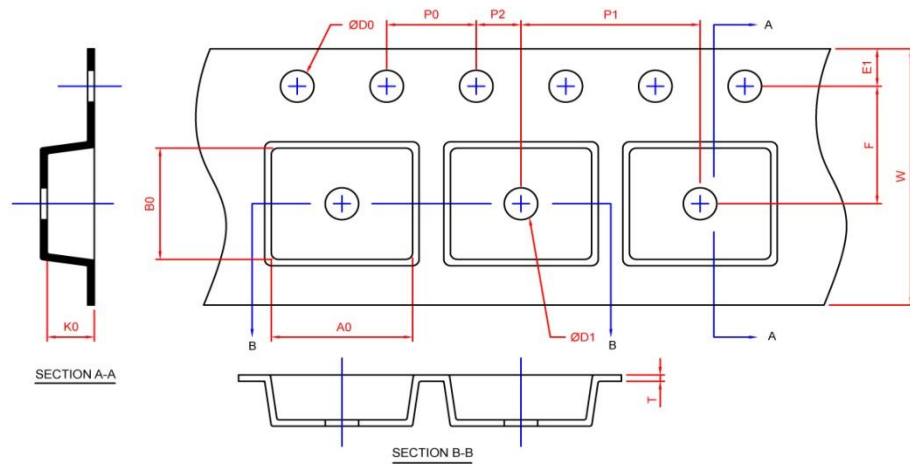
DFN 5X6



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	1.05	1.15	1.20
A1	0	0.02	0.05
b	0.45	0.50	0.55
b1	0.35REF		
c	0.203REF		
D	4.90	5.00	5.10
D2	4.20	4.30	4.40
e	1.27BSC		
e1	2.54BSC		
e2	0.675BSC		
Nd	3.81BSC		
E	5.90	6.00	6.10
E2	2.50	2.60	2.70
L	0.475	0.525	0.575
h	0.30	0.35	0.40
K	0.35REF		
K1	2.00REF		
R	0.07REF		

包装信息

DFN 5X6



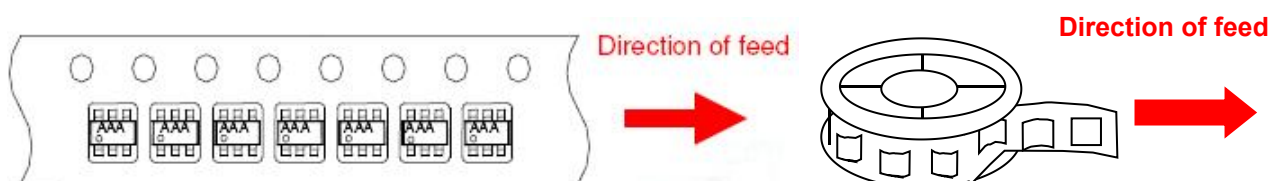
Application	A	H	T1	C	d	D	W	E1	F
DFN 5X6	178.0±2.00	50 MIN.	8.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	8.0±0.30	1.75±0.10	3.5±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0±0.10	4.0±0.10	2.0±0.05	1.5+0.10 -0.00	1.0 M-----IN.	0.6+0.00 -0.40	3.20±0.20	3.10±0.20	1.50±0.20

Application	Carrier Width	Cover Tape Width	Devices Per Reel
DFN 5X6	8	5.3	3000

(mm)

包装信息

DFN 5X6



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