## PT5020 Series

## Positive Input/Negative Output Integrated Switching Regulator

## SLTS025B

## (Revised 12/19/2001)



 $C_1 = Optional ceramic (1-5\mu F)$ 

 $C_2$  = Required Electrolytic (100µF)

# ER TREM

**PT5031** □ = -1.7 Volts

for the dimensions and PC board layout)

NOTE: PT5020 ISRs are not Short-Circuit Protected.

Characteristics	Symbol			PT5020 SERIES			
		Conditions		Min	Тур	Max	Units
Output Current	Io	Over V <sub>in</sub> range		0.25 (1) 0.10 (1) 0.10 (1) 0.10 (1)	 	1.0 0.60 0.50 0.30	А
Input Voltage Range	Vin	Over I <sub>o</sub> range		4.75	_	7 (2)	$\mathbf{V}$
Output Voltage Tolerance	$\Delta V_{o}$	Over $V_{in}$ Range T <sub>a</sub> = -20°C to SOA limit	(3)	_	±1.5	±3	$%V_{o}$
Line Regulation	Reg <sub>line</sub>	Over V <sub>in</sub> range		_	±0.5	±1	$%V_{o}$
Load Regulation	Regload	$I_omin \le I_o \le I_omax$		_	±0.5	±1	$%V_{o}$
Efficiency	η	I <sub>o</sub> =0.5 I <sub>o</sub> max		_	75	_	%
Vo Ripple (pk-pk)	$V_r$	20MHz bandwidth		_	±2	±5	$%V_{o}$
Transient Response	t <sub>tr</sub>	25% load change V <sub>o</sub> over/undershoot		_	500 3.0	5.0	µSec %Vo
Current Limit	Ilim			—	150	_	%Iomax
Inrush Current	I <sub>ir</sub> t <sub>ir</sub>	On start up		_	1.0 <sup>(3)</sup> 1.0	_	A mSec
Switching Frequency	$f_{s}$	Over I <sub>o</sub> range	$\begin{array}{  V_o   = 1.7 \text{ to } 8V \\  V_o  \ge 8 V\end{array}$	0.8 500	1 650	1.2 800	MHz kHz
Operating Temperature Range	$T_a$	-		-20	_	+85 (4)	°C
Thermal Resistance	$\theta_{ja}$	Free Air Convection (40-60LFM)		—	50	—	°C/W
Storage Temperature	Ts			-40	_	+125	°C
Mechanical Shock		Per Mil-STD-883D, Method 2002.3 1 msec, Half Sine, mounted to a fixture		_	500	_	G's
Mechanical Vibration Per Mil-STD-883D, 20-2000 Hz		Suffixes N, A, & C Suffixes L & M		_	5 20	_	G's
Weight		Suffixes N, A, & C Suffixes L & M		_	4.5 6.5 <sup>(5)</sup>	_	grams

**Specifications** (Unless otherwise stated,  $T_a = 25^{\circ}C$ ,  $V_{in} = 5V$ ,  $I_o = I_omax$ ,  $C_2 = 100\mu F$ )

 Notes:
 (1) The ISR will operate at no load with reduced specifications.

 (2) For applications with input voltages greater than 7 VDC, use the PT78NR100 Series.

 (3) The inrush current stated is above the normal input current for the associated output load.

(4) See Safe Operating Area curves or consult the factory for the appropriate derating
 (5) The tab pins on the 5-pin mount package types (suffixes L & M) must be soldered. For more information see the applicable package outline drawing.



# PT5020 Series

# Typical Characteristics

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Note A: Characteristic data has been developed from actual products tested at 25°C. This data is considered typical data for the Converter. Note B: Thermal derating graphs are developed in free-air convection cooling, which corresponds to approximately 40–60LFM of airflow.

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Mailing Address:

Texas Instruments Post Office Box 655303 Dallas, Texas 75265

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