

General Description

The 5972 is N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

Features

- Simple Drive Requirement
- Fast Switching
- Low On-Resistance

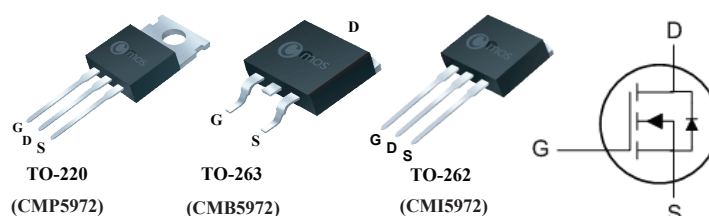
Product Summary

BVDSS	RDSON	ID
70V	8mΩ	80A

Applications

- Motor Control
- DC-DC converters
- General Purpose Power Amplifier

TO-220/263/262 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	70	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current ¹	80	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current ¹	50	A
I_{DM}	Pulsed Drain Current ²	250	A
EAS	Single Pulse Avalanche Energy ³	405	mJ
I_{AS}	Avalanche Current	80	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	260	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-case	0.9	$^\circ\text{C/W}$

N-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	70	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V$, $I_D=80A$	---	6.5	8	$m\Omega$
		$V_{GS}=4.5V$, $I_D=80A$	---	8.2	14	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60V$, $V_{GS}=0V$	---	---	1	μA
		$V_{DS}=60V$, $V_{GS}=0V$, $T_J=125^\circ\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=10V$, $I_D=40A$	---	50	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	1.5	---	Ω
Q_g	Total Gate Charge	$I_D=80A$	---	76	---	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=37.5V$	---	20	---	
Q_{gd}	Gate-Drain Charge	$V_{GS}=0$ to $10V$	---	25	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=37.5V$	---	40	---	ns
T_r	Rise Time	$I_D=80A$	---	150	---	
$T_{d(off)}$	Turn-Off Delay Time	$R_G=3.3\Omega$	---	95	---	
T_f	Fall Time	$V_{GS}=10V$	---	60	---	
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	3800	---	pF
C_{oss}	Output Capacitance		---	815	---	
C_{rss}	Reverse Transfer Capacitance		---	300	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ¹	$V_G=V_D=0V$, Force Current	---	---	80	A
I_{SM}	Pulsed Source Current ²		---	---	250	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V$, $I_S=80A$, $T_J=25^\circ\text{C}$	---	---	1.5	V

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$ 3.The EAS data shows Max. rating . The test condition is $V_{DD}=24V$, $V_{GS}=10V$, $L=0.13\text{mH}$, $I_{AS}=80A$

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