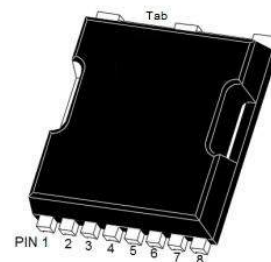


■ PRODUCT CHARACTERISTICS

VDSS	60V
$R_{DS(on)Typ}(V_{GS}=10V)$	0.5mΩ
ID	400A

■ FEATURES

Surface-mounted package Advanced
terch cell design Super trench

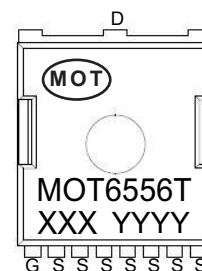


TOLL-8L

■ APPLICATIONS

High power system inverter
Light electric vehicles
BMS
Drones

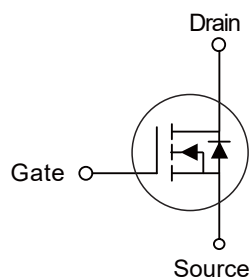
Pin configuration (Top view)



XXX = Lot Number
YYYY = Year Week

Marking

■ SYMBOL



Order information

Device	Package	Shipping
MOT6556T/TR	TOLL-8L	2000/Tape&Reel

■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Max	Unit
Drain-Source Voltage	V_{DS}	$T_C = 25^\circ\text{C}$	60	-	V
Gate-Source Voltage	V_{GS}	$T_C = 25^\circ\text{C}$	-	± 20	V
Drain Current (DC) *	I_D	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	400	A
		$T_C = 100^\circ\text{C}, V_{GS} = 10\text{ V}$	-	300	A
Drain Current (Pulsed) ***	I_{DM}	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	1200	A
Drain power dissipation	P_{tot}	$T_C = 25^\circ\text{C}$	-	300	W
Storage Temperature	T_{stg}		-55	175	$^\circ\text{C}$
Junction Temperature	T_J		-	175	$^\circ\text{C}$
Continuous-Source Current	I_S	$T_C = 25^\circ\text{C}$	-	400	A
Single Pulsed Avalanche Energy	E_{AS}	$V_{DD} = 40\text{ V}, L = 1.0\text{ mH}$	-	1984	mJ
Thermal Resistance- Junction to Ambient**	$R_{\theta JA}$		-	40	$^\circ\text{C/W}$
Thermal Resistance- Junction to Case**	$R_{\theta JC}$		-	0.5	

■ ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_{DS} = 250\text{ }\mu\text{A}$	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{DS} = 250\text{ }\mu\text{A}$	1	-	3	V
Drain Leakage Current	I_{DSS}	$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
On-State Resistance ^a	$R_{DS(ON)}$	$V_{GS} = 10\text{ V}, I_{DS} = 80\text{ A}$	-	0.5	0.6	m Ω
		$V_{GS} = 6\text{ V}, I_{DS} = 40\text{ A}$	-	0.98	1.15	
Diode Characteristics						
Diode Forward Voltage ^a	V_{SD}	$I_{SD} = 80\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.3	V
Reverse Recovery Time	t_{rr}	$I_{DS} = 40\text{ A}, V_{GS} = 0\text{ V}$ $dI_{SD}/dt = 100\text{ A}/\mu\text{s}$	-	60	-	nS
Reverse Recovery Charge	Q_{rr}		-	61	-	nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 20\text{ V}$ Frequency = 1 MHz	-	8725	-	pF
Output Capacitance	C_{oss}		-	3774	-	
Reverse Transfer Capacitance ^b	C_{rss}		-	116	-	
Turn-on Delay Time	$t_d(\text{on})$	$V_{DS} = 20\text{ V}, V_{GEN} = 10\text{ V},$ $R_G = 3.9\text{ }\Omega, R_L = 0.25\text{ }\Omega,$ $I_{DS} = 80\text{ A}$	-	29	-	nS
Turn-on Rise	t_r		-	193	-	
Turn-off Delay Time	$t_d(\text{off})$		-	91	-	
Turn-off Fall Time	t_f		-	144	-	
Gate Charge Characteristics ^b						
Total Gate Charge	Q_g	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V},$ $I_{DS} = 80\text{ A}$	-	145	-	nC
Gate-Source Charge	Q_{gs}		-	47	-	
Gate-Drain Charge	Q_{gd}		-	34	-	

Notes :

* Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

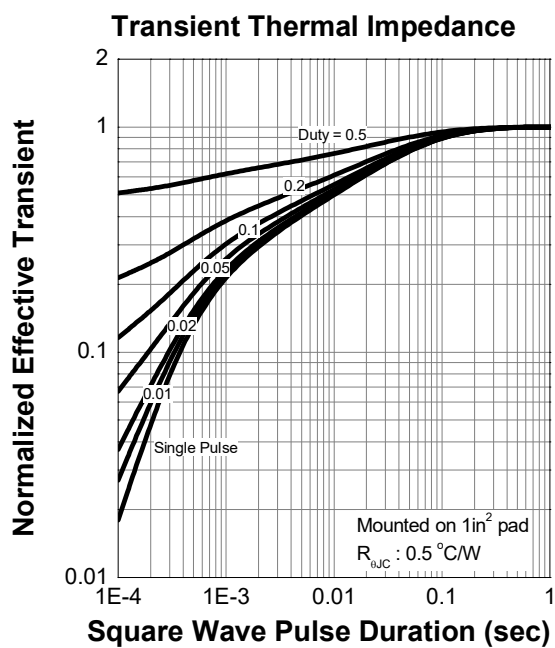
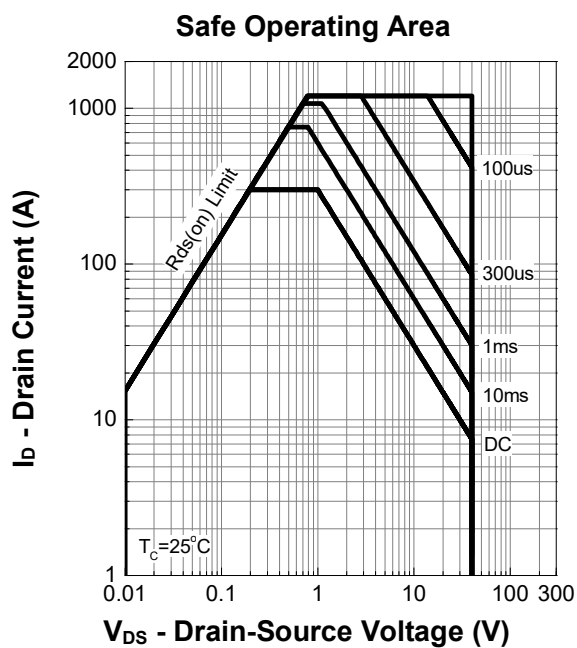
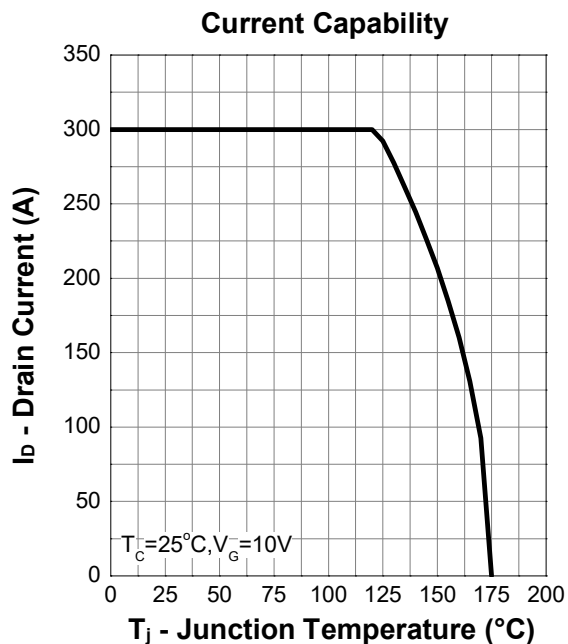
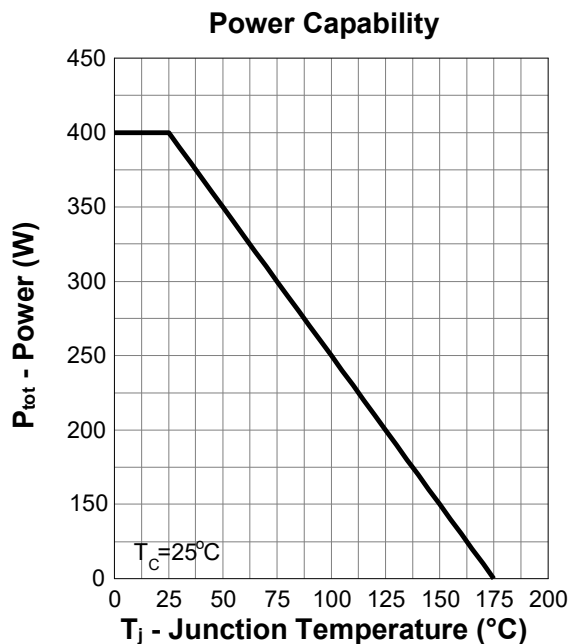
** Surface Mounted on minimum footprint pad area.

*** Limited by bonding wire

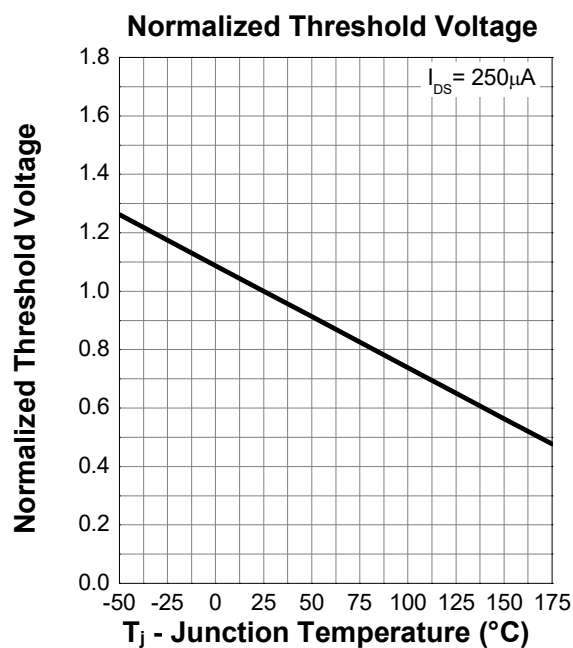
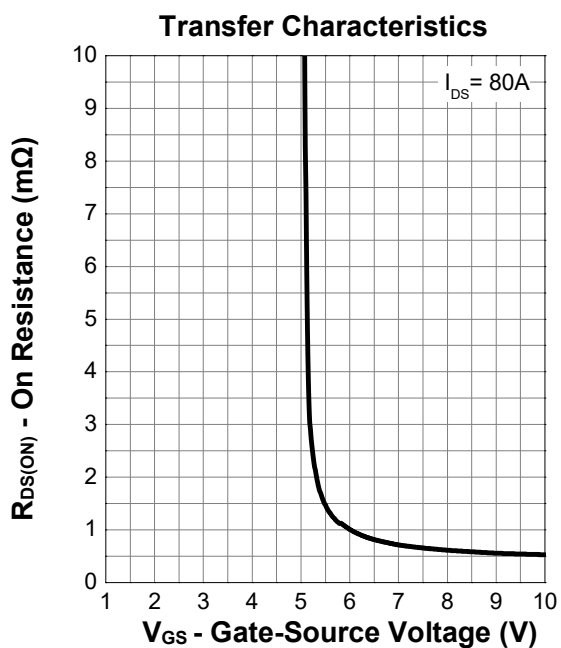
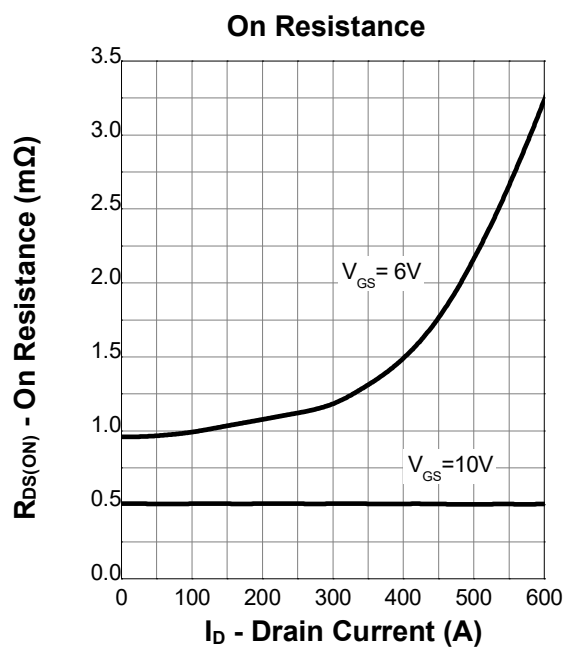
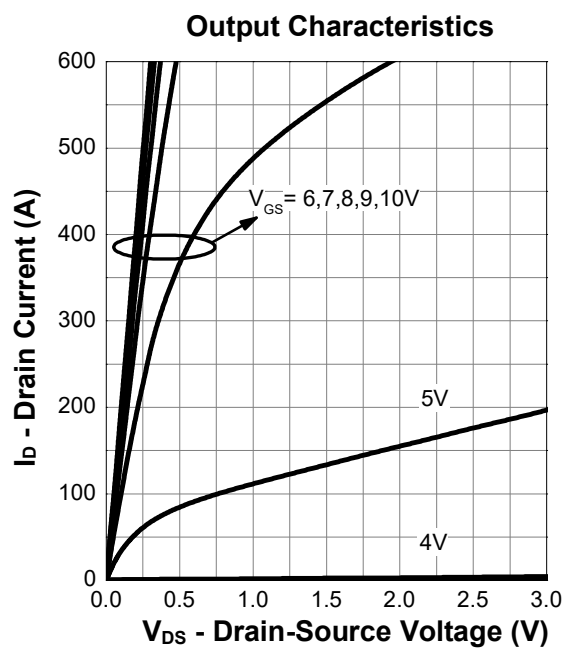
a : Pulse test ; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

b : Guaranteed by design, not subject to production testing

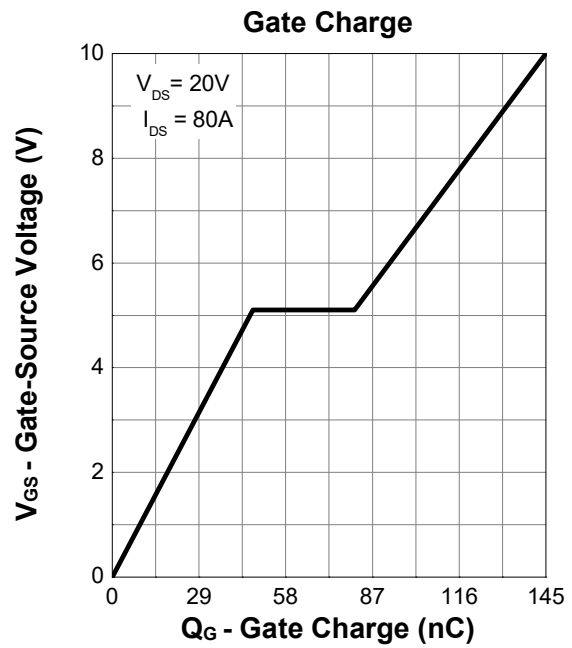
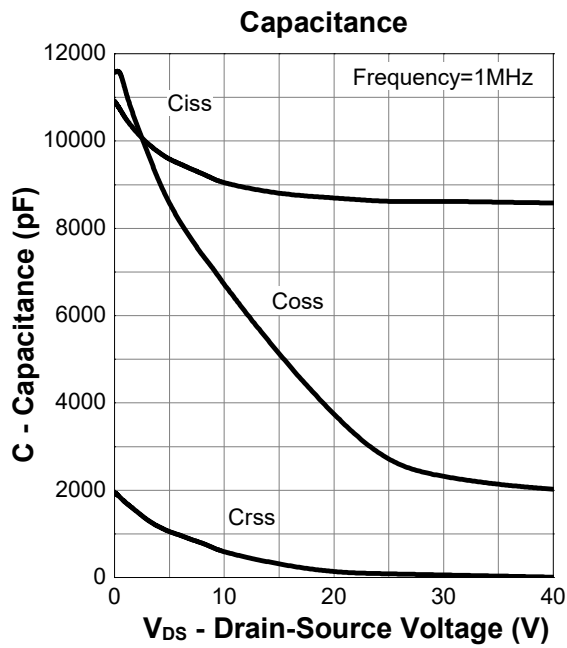
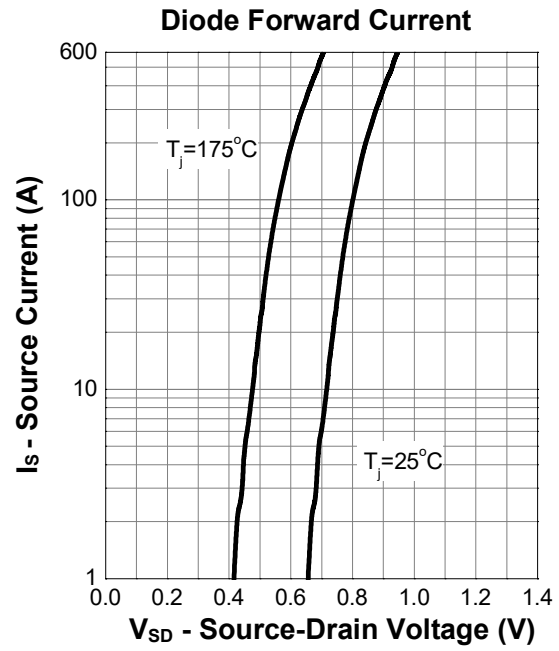
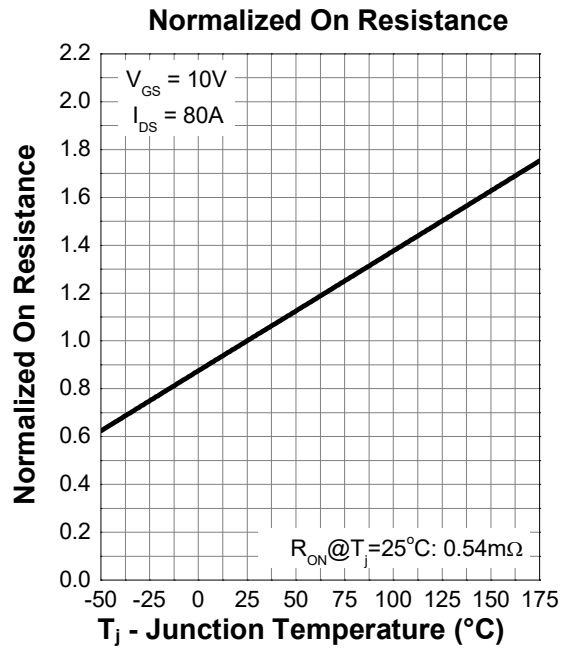
■ TYPICAL CHARACTERISTICS



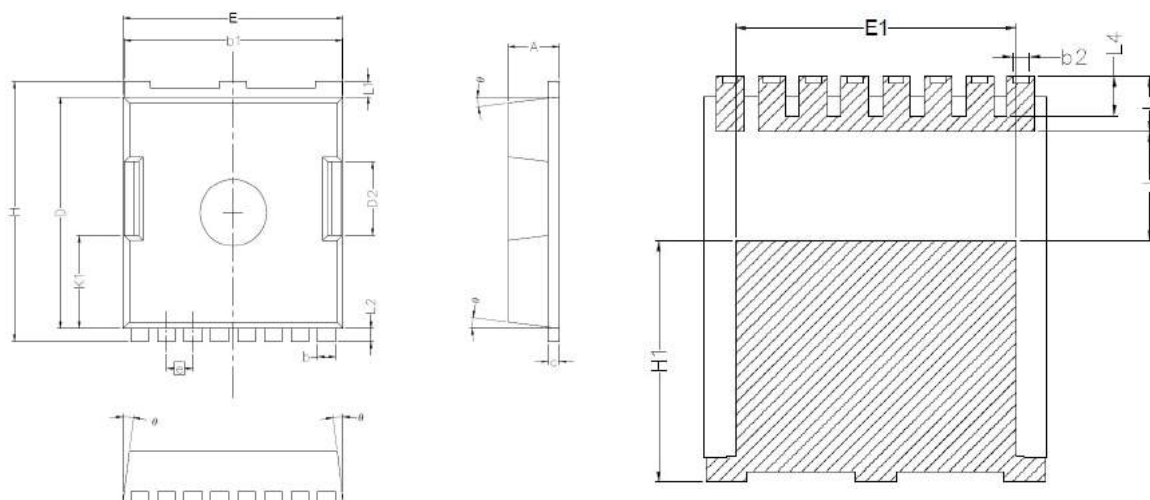
■ TYPICAL CHARACTERISTICS(Cont.)



■ TYPICAL CHARACTERISTICS(Cont.)



■ TOLL-8L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimesions In Millimeters	
	Min.	Max.
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°

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