GENERAL DESCRIPTION

The HP4060 series of devices are highly integrated Li-Ion and Li-Pol linear chargers targeted at small capacity battery for portable applications. It is a complete constant-current/ constant voltage linear charger. No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. It can deliver up to 300mA of charge current (using a good thermal PCB layout) with a final float voltage accuracy of ±1%. The charge voltage is fixed at 4.2V, 4.3V, 4.35V or 4.4V, and the charge current can be programmed externally with a single resistor. The charger function has high accuracy current and voltage regulation loops and charge termination.

The HP4060 automatically terminates the charge cycle when the charge current drops to 1/10 the programmed value after the final float voltage is reached.

When the input supply (wall adapter or USB supply) is removed, the HP4060 will shut off, only 40nA leakage current coming from battery at sleep mode when ambient temperature is 85℃, so it can save energy and improve standby time.

The HP4060 is available in a small package with TDFN1X1-6L. Standard product is Pb-Free and Halogen-free.

FEATURES

Charging

- 1% Charge Voltage Accuracy
- 5% Charge Current Accuracy
- Support Application for Very Low Charge Currents – 1mA to 300mA
- Support minimum 0.1mA Charge Termination Current
- 45nA Maximum Battery Output Leakage Current @ 0℃~85℃
- High Voltage Chemistry Support: up to 4.4V

Others

- Output Short-Circuit Protection
- Soft-Start Limits Inrush Current
- Charge Status Output Pin
- Automatic Recharge
- IEC62368-1:2018 Certif. No. : CN53900

APPLICATIONS

- Fitness Accessories
- Smart Watches
- Bluetooth Handsets
- Wireless Low-Power Handheld Devices

MARKING DESCRIPTION

NWV
•

"N": Product code, here use "H" stands for "HP4060".
"W": The week of manufacturing. "*A*" stands for week 1, "*Z*" stands for week 26, "a" stands for week 27, "z" stands for week 52.
"V" : Product version code.

ORDER INFORMATION

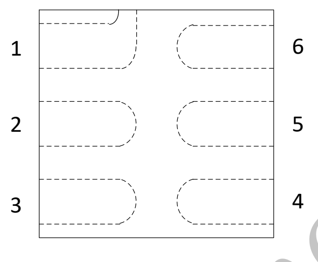
Part NO	Package	CV Voltage	CC Current	Temperature	Tape & Reel
HP4060D6-42	TDFN1X1-6L	4.2V	Programmable Max:300mA	-40 ~ +85℃	10000/REEL
HP4060D6-43		4.3V			
HP4060D6-435		4.35V			
HP4060D6-44		4.4V			

PART NUMBER RULES

HP4060①-②

Code	Description
①	Package: D6: TDFN1X1-6L
②	Voltage version: 42: 4.2V 43: 4.3V 435: 4.35V 44: 4.4V

PIN ASSIGNMENT



1: BAT
2: ENb
3: CHGb
4: GND
5: PROG
6: VCC

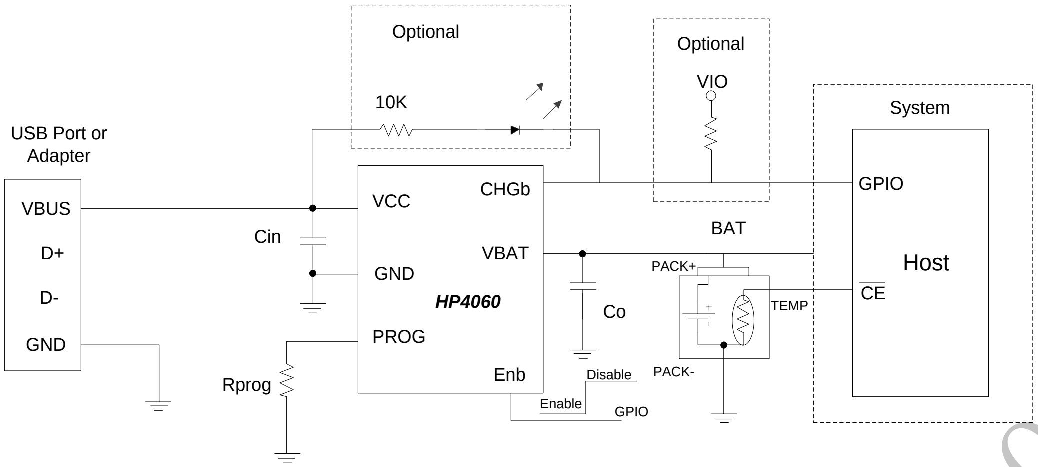
TOP VIEW

TDFN1X1-6L

PIN DESCRIPTION

Pin Number	Pin Name	I/O	Function
1	BAT	O	Charge Current Output. Provides charge current to the battery and regulates the final float voltage to 4.2V, 4.3V, 4.35V or 4.4V.
2	ENb	I	Enable control, internal pulled down. When Enb is low the charging is enabled and the otherwise is disabled.
3	CHGb	O	Open-Drain Charge Status Output. When the battery is charging, the CHGb pin is pulled low. When the charge cycle is completed or VCC is removed, the CHGb is forced high impedance.
4	GND	Ground	Ground
5	PROG	O	Charge current setting, charge current monitor. The charging current is given by IBAT= 100/R_{PROG}(A). Please choose 1% precision resistor for R_{PROG}.
6	VCC	Power	Power Supply

TYPICAL APPLICATION CIRCUIT



Note 1: *Cin=1uF, Co=1uF are recommended, not mandatory. Good layout and pure supply voltage can omit these capacitors. Rprog is not needed for fixed cc current part.*

ABSOLUTE MAXIMUM RATINGS (Note 2)

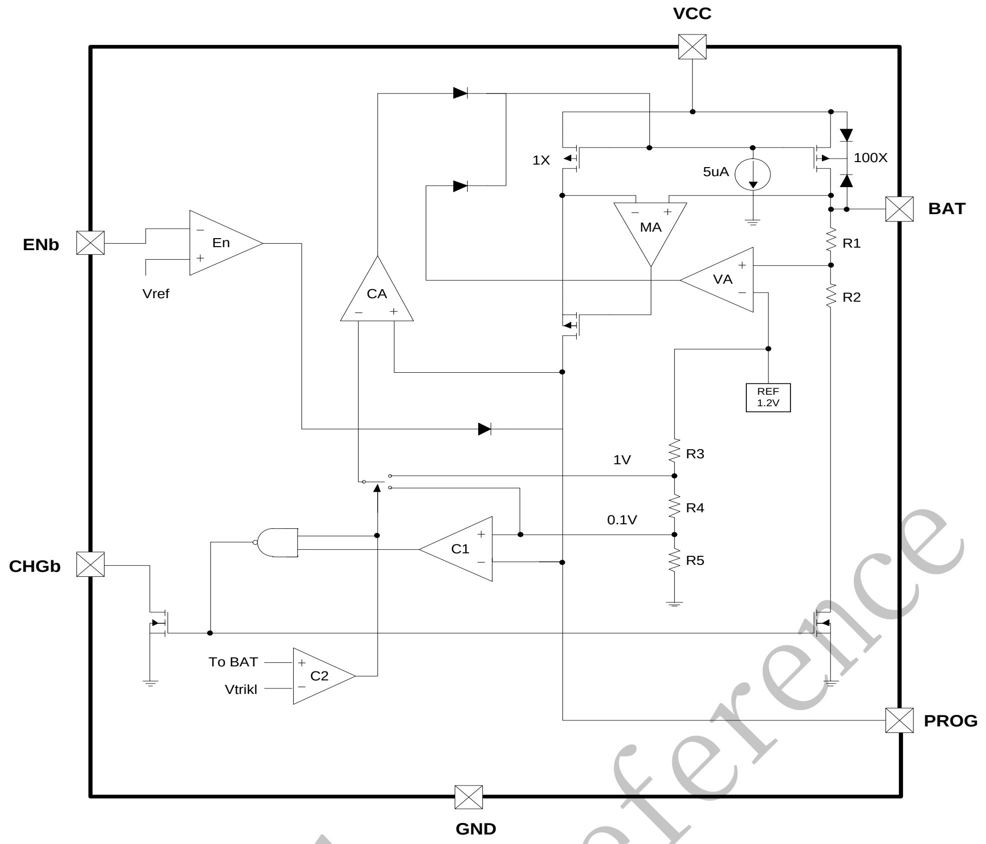
SYMBOL	ITEMS		VALUE	UNIT
V_{CC}	Input Voltage		-0.3~7	V
V_{PROG}	PROG Voltage		-0.3~7	V
V_{BAT}	BAT Voltage		-0.3~7	V
V_{CHGb}	CHGb Voltage		-0.3~7	V
V_{ENb}	ENb Voltage		-0.3~7	V
I_{BAT}	Battery Charge Current		300	mA
P_{DMAX}	Power Dissipation	TDFN1X1-6L	0.5	W
$R_{\theta JA}$	Thermal Resistance	TDFN1X1-6L	200	℃/ W
T_J	Junction Temperature		-40~150	℃
T_{STG}	Storage Temperature		-55 to 150	℃
T_{SOLDER}	Package Lead Soldering Temperature		260℃, 10s	

Note 2: *Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.*

RECOMMENDED OPERATING CONDITION

		MIN	NOM	MAX	UNIT
V_{CC}	Input operating voltage range	4.5	5	5.5	V
I_{BAT}	Battery charge current range	1	50	250	mA
R_{PROG}	CC mode charge current programming resistor	1	2	100	KΩ

SIMPLIFIED BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS

The following specifications apply for V_{CC}=5V T_A=25℃, unless specified otherwise.

SYMBOL	ITEMS	CONDITIONS	MIN	TYP	MAX	UNIT
$I_{SPLYCHRG}$	Charge Mode GND Current	R_{PROG}=1kΩ,V_{CC}=5V		94		µA
$I_{BATCHRG}$	Charge Mode Battery Current	R_{PROG}=1kΩ	95	100	105	mA
		R_{PROG}=2kΩ	47.5	50	52.5	
		R_{PROG}=4kΩ	23.7	25	26.3	
$I_{SPLYSTBY}$	Standby Mode Supply Current	Charge Terminated ENb=0V or floating	65	70	85	µA
		VCC=5V, ENb=High	0		1	µA
$I_{BATSTBY}$	Standby Mode Battery Current	Charge Terminated		2.5		µA
$I_{SPLYASD}$	Shutdown Mode Supply Current	Vcc<V_{BAT}<V_{CC}+0.3V / V_{CC}<UVLO, no battery		27		µA
I_{BATASD}	Shutdown Mode BAT Pin Current	V_{BAT} - V_{CC}>0.3V		0.52		µA
$I_{BATSLEEP1}$	Sleep Mode BAT Pin Current	V_{CC}=0 or V_{CC} Floating T_A=25℃		0		µA

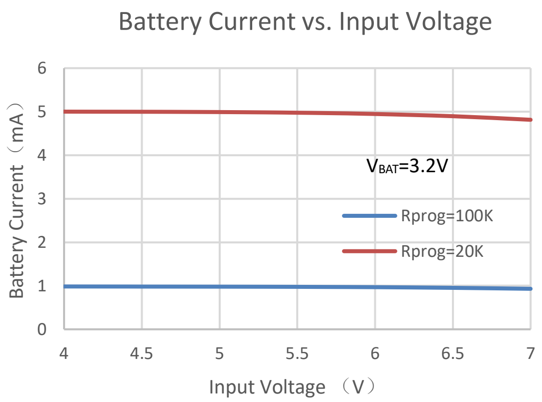
ELECTRICAL CHARACTERISTICS (Continued)

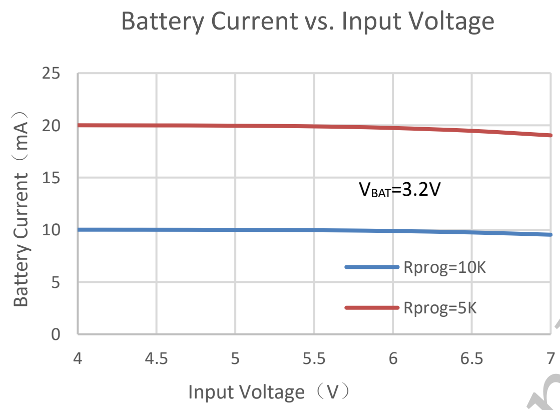
The following specifications apply for V_{CC}=5V T_A=25℃, unless specified otherwise.

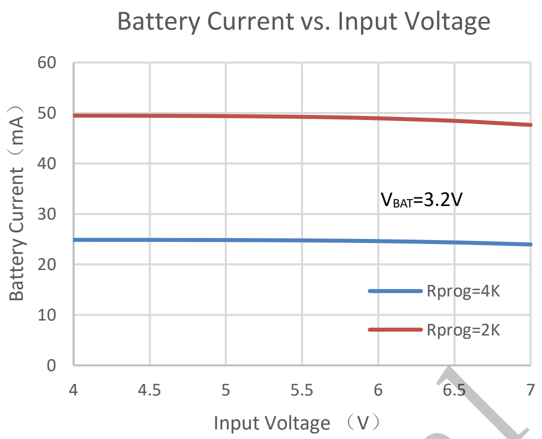
SYMBOL	ITEMS	CONDITIONS		MIN	TYP	MAX	UNIT
$I_{BATSLEEP2}$	Sleep Mode BAT Pin Current	V_{CC}=0 or V_{CC} Floating T_A=0℃~85℃, Note 3			40	45	nA
V_{FLOAT}	Float Voltage			4.158	4.2	4.242	V
				4.257	4.3	4.343	V
				4.306	4.35	4.394	V
				4.356	4.4	4.444	V
I_{TRIKL}	Trickle and Terminal Charge Current	R_{PROG}=1kΩ		8	10	12	mA
		R_{PROG}=2kΩ		4	5	6.5	mA
		R_{PROG}=4kΩ		2	2.5	3.5	mA
V_{TRIKL}	Trickle Charge Voltage Threshold	V_{BAT} from low to high	V_{FLOAT}= 4.2V	2.40	2.54	2.65	V
			V_{FLOAT}= 4.3V	2.45	2.6	2.7	V
			V_{FLOAT}= 4.35V	2.50	2.63	2.75	V
			V_{FLOAT}= 4.4V	2.55	2.7	2.8	V
$V_{TRIKL, HYS}$	Trickle Charge Voltage Hysteresis	V_{BAT} from high to low	V_{FLOAT}= 4.2V/4.3V		110		mV
			V_{FLOAT}= 4.35V/4.4V		130		mV
V_{UVLO}	UVLO Threshold	V_{CC} from Low to High		3.6	3.8	4.0	V
$V_{UVLO, HYS}$	UVLO Hysteresis	V_{CC} from high to low			270		mV
V_{ASD}	V_{CC}-V_{BAT} Lockout Threshold Voltage	V_{CC} from High to Low	V_{FLOAT}= 4.2V/4.3V		70		mV
			V_{FLOAT}= 4.35V/4.4V		60		mV
		V_{CC} from Low to High	V_{FLOAT}= 4.2V/4.3V		220		mV
			V_{FLOAT}= 4.35V/4.4V		210		mV
ΔV_{RECHRG}	Auto Recharge Battery Voltage	V_{BAT} from high to low		100	150	200	mV
V_{CHGb}	CHGb Pin Output Low Voltage	I_{CHGb}=1mA			0.035	0.1	V
VENb	ENb high threshold	ENb from low to high		1.5		VCC	V
	ENb Low threshold	ENb from high to low		0		0.4	V
T_{SS}	Soft-Start Time	Note 3			170		us
T_{RECHRG}	Recharge Comparator Filter Time	Note 3			2		ms
T_{TERM}	Charge Terminated Filter Time	Note 3			1		ms

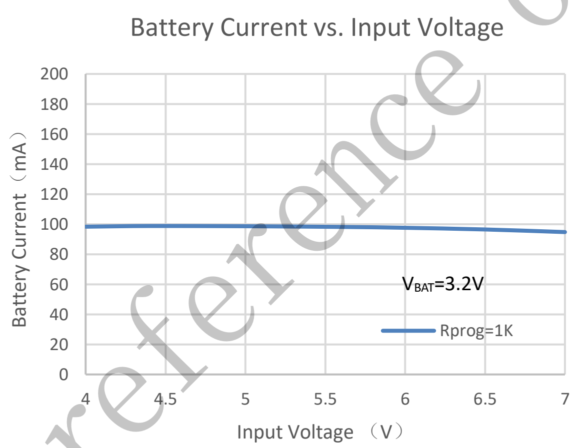
Note 3: *Guaranteed by Design.*

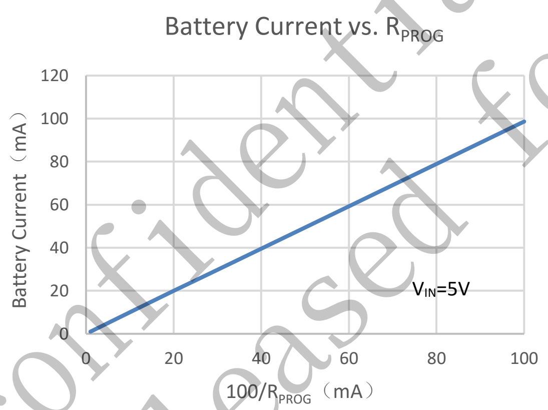
TYPICAL CHARACTERISTICS

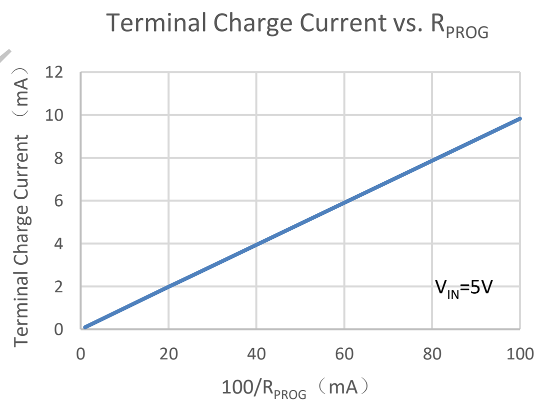


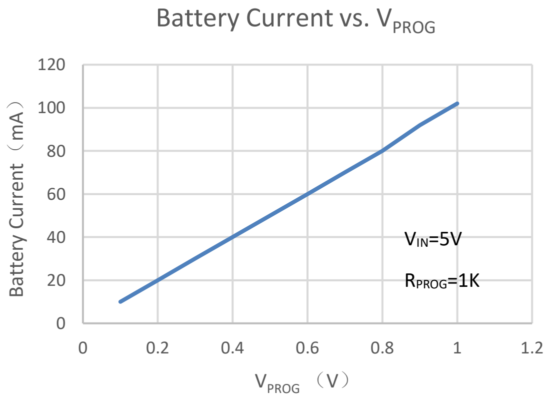


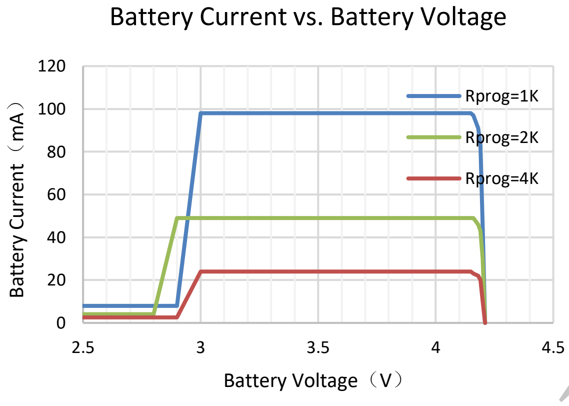


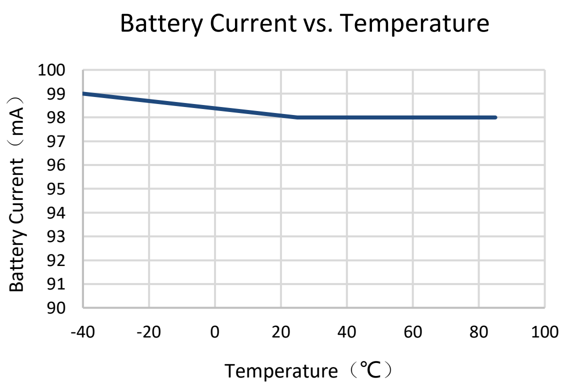


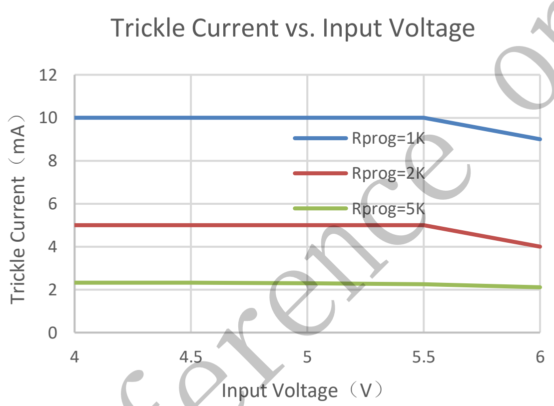


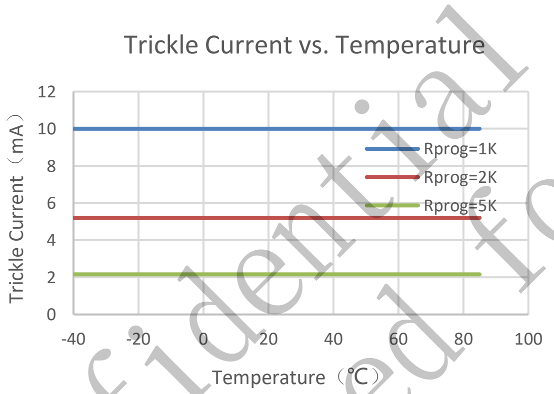


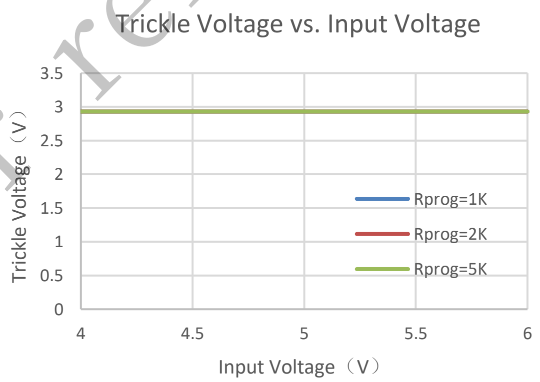

Battery Current vs. V_{PROG}
Battery Current（mA）
V_{PROG}（V）
V_{IN}=5V
R_{PROG}=1K

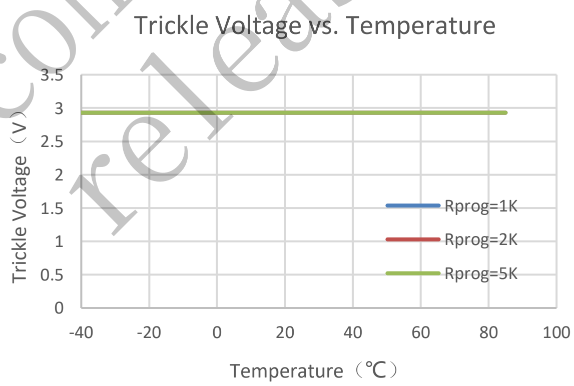

Battery Current vs. Battery Voltage
Battery Current（mA）
Battery Voltage（V）
Rprog=1K
Rprog=2K
Rprog=4K

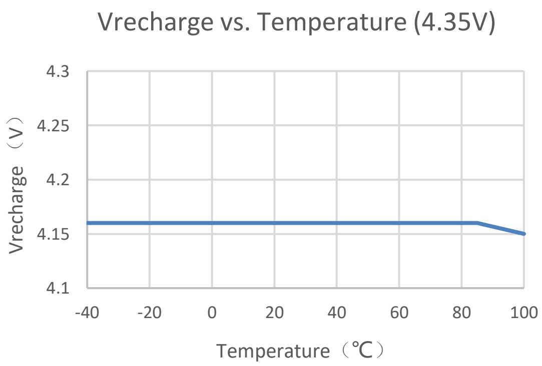

Battery Current vs. Temperature
Battery Current（mA）
Temperature（℃）


Trickle Current vs. Input Voltage
Trickle Current（mA）
Input Voltage（V）
Rprog=1K
Rprog=2K
Rprog=5K

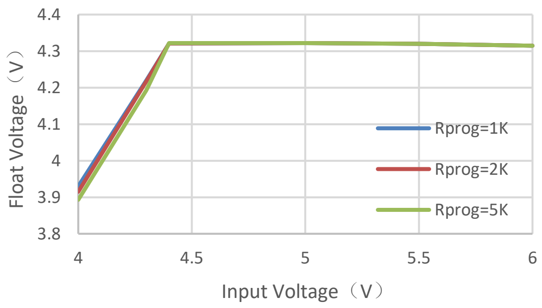

Trickle Current vs. Temperature
Trickle Current（mA）
Temperature（℃）
Rprog=1K
Rprog=2K
Rprog=5K


Trickle Voltage vs. Input Voltage
Trickle Voltage（V）
Input Voltage（V）
Rprog=1K
Rprog=2K
Rprog=5K

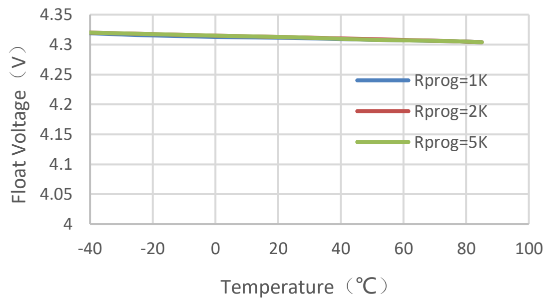

Trickle Voltage vs. Temperature
Trickle Voltage（V）
Temperature（℃）
Rprog=1K
Rprog=2K
Rprog=5K


Vrecharge vs. Temperature (4.35V)
Vrecharge（V）
Temperature（℃）

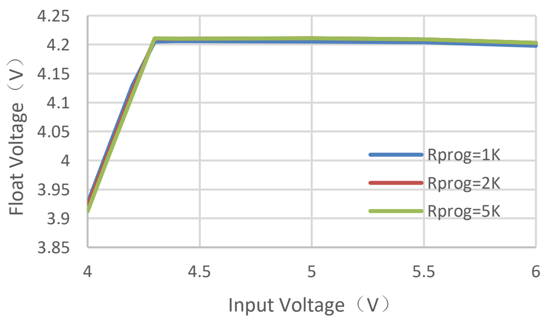
Float Voltage vs. Input Voltage (4.35V)



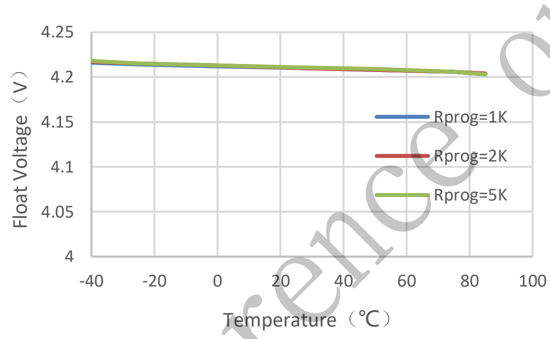
Float Voltage vs. Temperature (4.35V)



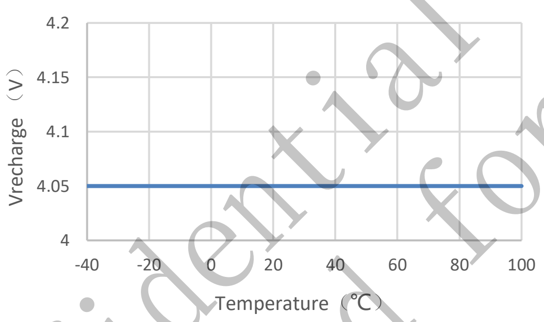
Float Voltage vs. Input Voltage (4.2V)



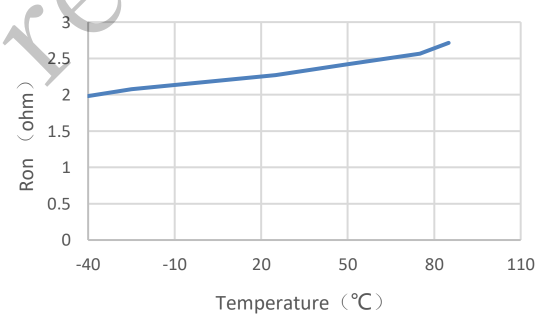
Float Voltage vs. Temperature (4.2V)



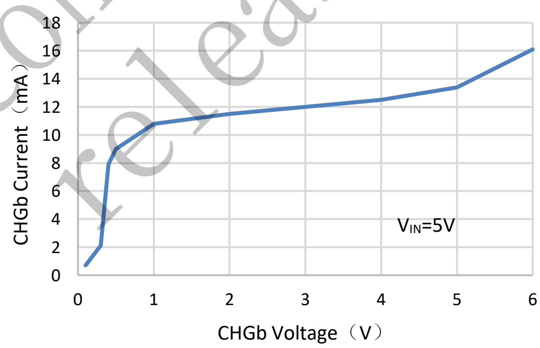
Vrecharge vs. Temperature (4.2V)



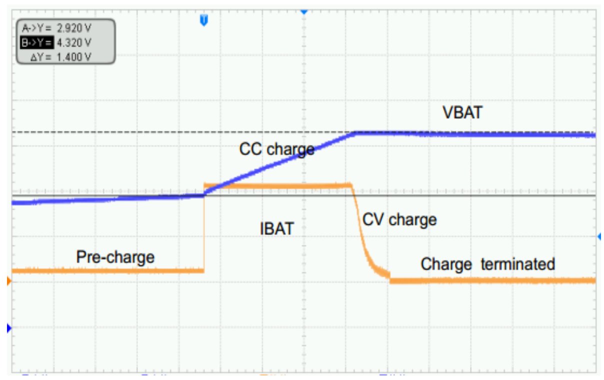
Ron vs. Temperature



CHGb Current vs. CHGb Voltage



Charge Curve



OPERATION INFORMATION

The HP4060 is a single cell Li-Ion and Li-Pol battery linear charger using a constant-current / constant-voltage algorithm. It is designed especially for small capacity battery that is used in handheld devices, such as GPS tracker, Smart wrist and U-Key. It can deliver up to 300mA of charge current (using a good thermal PCB layout) with a final float voltage accuracy of ±1%. The HP4060 includes an internal P-channel power MOSFET and current regulation circuitry. No blocking diode or external current sense resistor is required, thus the basic charger circuit requires only two external components. Furthermore, the HP4060 is capable of operating from a USB power source.

Normal charge cycle

A charge cycle begins when the voltage at the VCC pin rises above the UVLO threshold level and a 1% program resistor is connected from the PROG pin to ground or when a battery is connected to the charger output. If the BAT pin is less than Vtrikl, the charger enters trickle charge mode. In this mode, the HP4060 supplies approximately 1/10 the programmed charge current to bring the battery voltage up to a safe level for full current charging.

When the BAT pin voltage rises above Vtrikl, the charger enters constant-current mode, where the programmed charge current is supplied to the battery. When the BAT pin approaches the final float voltage, the HP4060 enters constant-voltage mode and the charge current begins to decrease. The charge cycle ends when the PROG voltage is less than 100mV.

Programming charge current

The charge current is programmed using a single resistor from the PROG pin to ground. The battery charge current of constant current mode is 100 times the current out of the PROG pin. The program resistor and the charge current of constant current are calculated using the following equations:

$$I_{BAT} = 100 / R_{PROG} \text{ (A)}$$

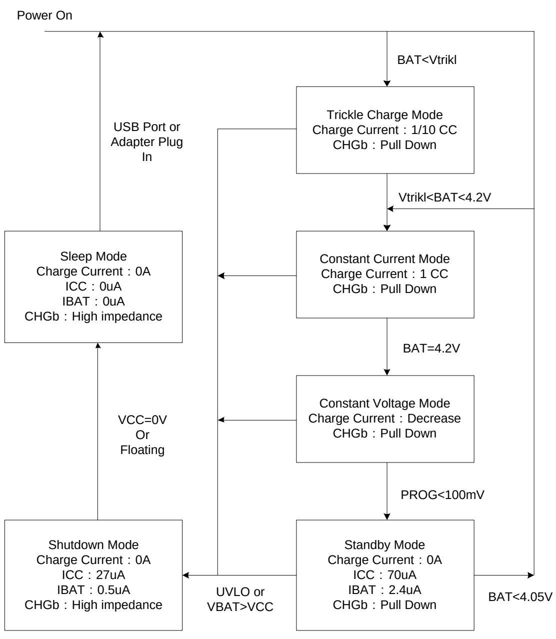
For example, I_{BAT}=0.1A, R_{PROG}=1KΩ, I_{BAT}=0.02A, R_{PROG}=5KΩ. Please choose 1% precision resistor for R_{PROG}, this will affect the accuracy of CC charge current and termination current.

Charge termination

A charge cycle is terminated when the charge current falls to 1/10 of the programmed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the PROG pin. When the PROG pin voltage falls below 100mV for longer than T_{TERM} (typically 1ms), charging is terminated. The charge current is latched off and the HP4060 enters standby mode, where the input supply current drops to 70uA. (Note: 1/10 CC termination is disabled in trickle charging mode).

When charging, transient loads on the BAT pin can cause the PROG pin to fall below 100mV for short periods of time before the DC charge current has dropped to 1/10 of the programmed value. The 1ms filter time (T_{TERM}) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the average charge current drops below 1/10 of the programmed value, the HP4060 terminates the charge cycle and ceases to provide any current through the BAT pin, the chip will be put into standby mode. In this state, all loads on the BAT pin must be supplied by the battery.

The HP4060 constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the Vfloat-0.15V (typically) recharge threshold (V_{RECHRG}), another charge cycle begins and current is once again supplied to the battery. The state diagram of a typical charge cycle is as below:



Charge status indicator (CHGb)

The charge status output indicator is an open drain circuit. The indicator has two different states: pull-down (~16mA), and high impedance. The pull-down state indicates that the HP4060 is in a charge cycle. High impedance indicates that the charge cycle is complete. The CHGb also can be used to detect the charge states by a microprocessor with a pull-up resistor.

Shutdown mode

The HP4060 will be put into shutdown mode when the battery voltage is higher than the V_{CC} voltage or V_{CC}-V_{BAT} is less than V_{ASD}. This reduces the battery drain current to less than 0.5uA and the supply current to less than 27uA. A new charge cycle can be initiated when the V_{CC}-V_{BAT} is high than V_{ASD}.

The HP4060 also be put into shutdown mode when V_{CC} voltage down to UVLO threshold. In this state, the CHGb pin is high impedance state. The CHGb pin is also in a high impedance state if the charge cycle is completed.

Automatic recharge

Once the charge cycle is terminated, the HP4060 continuously monitors the voltage on the BAT pin using a comparator with a 2ms filter time (T_{RECHRG}). A charge cycle restarts when the battery voltage falls below delta V_{RECHRG} (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations. CHGb output enters a pull-down state during recharge cycles.

ENb Control

The ENb is a low effective control logical pin, when it is blow low threshold voltage, the HP4060 is enabled to charge battery. The typical low threshold value is 0.75V when Enb is from high to low. HP4060 is disabled to charge when it is higher than 1V, and the VCC's current consumption is lower than 1uA in this condition.

APPLICATION INFORMATIONS

Stability considerations

The constant-voltage mode feedback loop is stable without an output capacitor provided a battery is connected to the charge output. With no battery present, an output capacitor is recommended to reduce ripple voltage.

In constant-current mode, the PROG pin is in the feedback loop, not the battery. The constant-current mode stability is affected by the impedance at the PROG pin. With no additional capacitance on the PROG pin, the charger is stable with program resistor values as high as 100KΩ. However, additional capacitance on this node reduces the maximum allowed program resistor thus it should be avoided.

Power dissipation

HP4060 has low temperature coefficient, at higher temperatures, the charging current will decrease slightly. To -40℃~125℃ temperature range the change of the charging current is very small. Nearly all of this power dissipation is generated by the internal MOSFET. This is calculated to be approximately:

$$P_D = (V_{CC} - V_{BAT})*I_{BAT}$$

Maximum allowable power dissipation limited by the packaging format and cooling conditions in actual applications. For TDFN1X1-6L package, P_D is not allowed to exceed 0.3W. For example, the worse case application of HP4060 is V_{CC}=5.5V, V_{BAT}=3V, I_{BAT}=0.1A, so P_D=0.25W, it is safe. At charge cycle, the battery voltage is rising gradually, so the power dissipation is reduce accordingly. The power dissipation turn into heat, please taken into consideration when design system.

VCC bypass capacitor

Many types of capacitors can be used for input bypass, however, caution must be exercised when using multilayer ceramic capacitors. Because of the self-resonant and high Q characteristics of some types of ceramic capacitors, a 10uF/16V ceramic capacitor is recommended for this bypass capacitor. Due to a high voltage transient will be generated under some start-up conditions, such as connecting the charger input to a live power source.

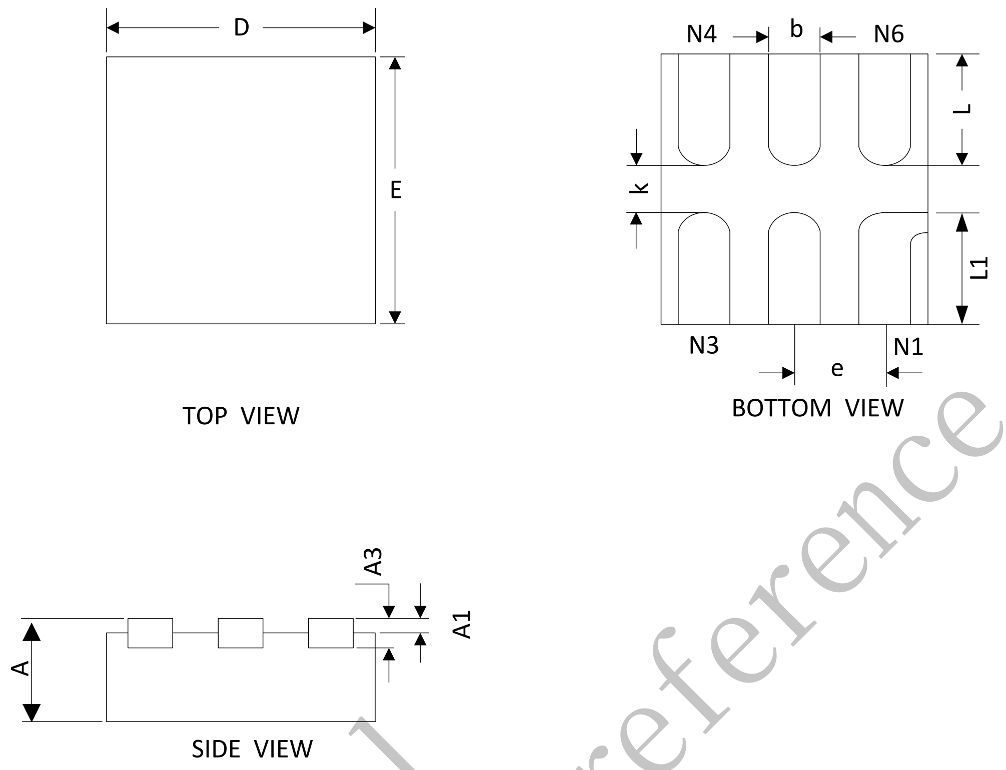
Charge current soft-start

The HP4060 includes a soft-start circuit to minimize the inrush current at the start of a charge cycle. When a charge cycle is initiated, the charge current ramps from zero to the full-scale current over a period of approximately 190us. This has the effect of minimizing the transient current load on the power supply during start-up.

PACKAGE OUTLINE

Package	TDFN1X1-6L	Devices per reel	10000Pcs	Unit	mm

Package Dimension：



Symbol	Millimeter			Symbol	Inches		
	MIN	NOM	MAX		MIN	NOM	MAX
A	0.320	-	0.400	A	0.013	-	0.016
A1	0.000	0.020	0.050	A1	0.000	0.001	0.002
A3	0.100REF			A3	0.004REF		
D	0.950	1.000	1.050	D	0.037	0.039	0.041
E	0.950	1.000	1.050	E	0.037	0.039	0.041
K	0.150MIN			K	0.006MIN		
b	0.120	0.175	0.230	b	0.005	0.007	0.009
e	0.350TYP			e	0.014TYP		
L	0.350	0.400	0.450	L	0.014	0.016	0.018
L1	0.350	0.400	0.450	L1	0.014	0.016	0.018