

N Channel MOSFET (Deep Trench Superjunction)



Lead Free Package and Finish

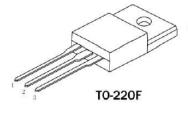
Applications:

- •Adapter & Charger
- •SMPS Standby Power
- •AC-DC Switching Power Supply
- •LED driving power

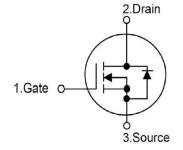
Features:

- •Low On Resistance
- •Low Gate Charge
- •Peak Current vs Pulse Width Curve
- •Proprietary New Super Junction Technology
- •RoHS Compliant

ID	RDS (ON) (Typ.)	Vdss
4. 0A	0. 84 Ω	600V



Not to Scale



Ordering Information

Part Number	Package	Marking
RSU4N60F	T0-220F	RSU4N60F

Absolute Maximun Ratings Tc=25℃ unless otherwise specified

Symbol	Parameter	RSU4N60F	Units
VDSS	Drain-to-Source Voltage (Note*1)	600	V
ID	Continuous Drain Current	4.0	Δ.
IDM	Pulsed Drain Current (Note*2)	12.0	A
PD	Power Dissipation	32	W
VGS	Gate-to-Source Voltage	± 30	V
EAS	Single Pulse Avalanche Engergy L=10mH VDD=50V TJ=25℃	20	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	$^{\circ}\! \mathbb{C}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

^{*}Drain Current Limited by Maximum Junction Temperature

Caution:Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.

Thermal Resistance

Symbol	Parameter	RSU4N60F	Units	Test Conditions
Rejc	Junction-to-Case	3. 9		Drain lead soldered to water cooled heatsink,PD adjusted for a peak junction temperature of +150℃.
Rө ja	Junction-to-Ambient	100		1 cubic foot chamber, free air.



OFF Characteristics $TJ=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BVDSS	Drain-to-source Breakdown Voltage	600		1	٧	V _{GS} =0V, I _D =250μA
IDSS	Drain-to-Source Leakage Current			1.0	μД	VDS=600V, VGS=0V
т	Gate-to-Source Forward Leakage			100	Δ.	VGS=+30V VDS=0V
Igss	Gate-to-Source Reverse Leakage			-100	nA	$V_{GS}=-30V$ $V_{DS}=0V$

ON Characteristics $TJ=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Rds (on)	Static Drain-to-Source On-Resistance		0.84	0. 90	Ω	Vgs=10V, Id=2A
VGS (TH)	Gate Threshold Voltage	2.0		4. 0	V	Vgs=Vds, Id=250μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn-on Delay Time		8.0		nS	V _{DS} =480V I _D =4. 0A R _G =25 Ω
trise	Rise Time		6.0			
td(OFF)	Turn-OFF Delay Time		35			
tfall	Fall Time		20	-		(Note:3,4)

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		350			V _{GS} =0V
Coss	Output Capacitance		40		рF	$V_{DS}=50V$
Crss	Reverse Transfer Capacitance		4		1	f=1.0MHz
Qg	Total Gate Charge		7			$V_{DS}=480V$
Q_{gs}	Gate-to-Source Charge		1.5		nC	In=4.0A VGS=10V (Note:3,4)
Q_{gd}	Gate-to-Drain("Miller") Charge		2.5			



Source-Drain Diode Characteristics

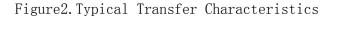
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Is	Continuous Source Current			4. 0	A	Integral pn-diode
Ism	Maximum Pulsed Current			12.0	A	in MOSFET
Vsd	Diode Forward Voltage		0.9	1.2	V	$I_S=4.0A, V_{GS}=0V$
trr	Reverse Recovery Time		200		nS	$V_{GS}=0V$ $V_{R}=480V$
Q_{rr}	Reverse Recovery Charge		0.8		μС	Is=4.0A, di/dt=100A/μs

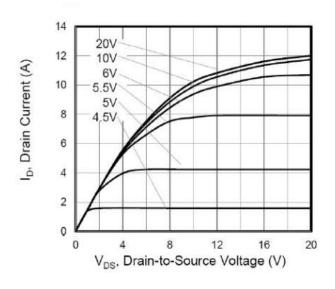
Notes:

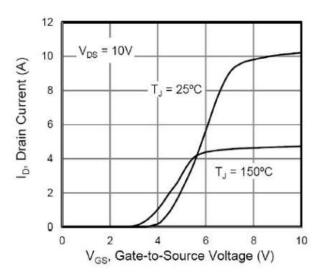
- *1. $TJ=\pm 25^{\circ}C$ to $+150^{\circ}C$.
- *2. Repetitive rating; pulse width limited by maximum junction temperature.
- *3. Pulse width≤380µs; duty cycle ≤2%.
- *4. Basically not affected by temperature.

Typical Feature curve

Figure 1. Typical Output Characteristics

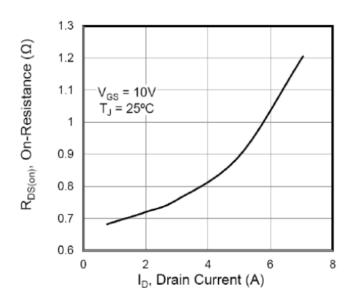








Figuer3. Typical ON Resistance vs Drain Current



Figuer4. Typical Body Diode Transfer Characteristics

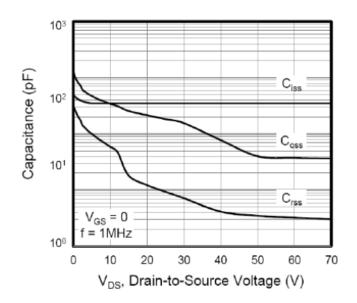


Figure 5. Body Diode Forward Voltage

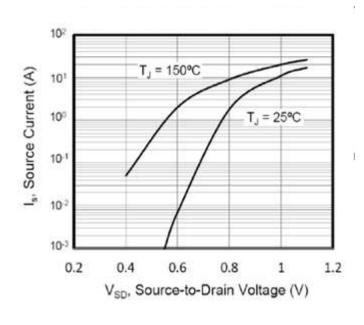


Figure 6. Typical Gate Charge vs Gate-to-Source Voltage

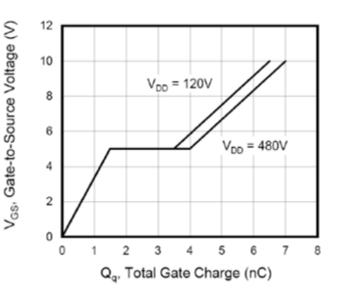
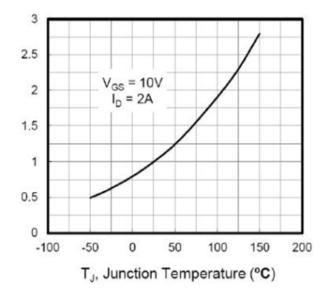




Figure 7. Threshold Voltage vs Junation Temperature

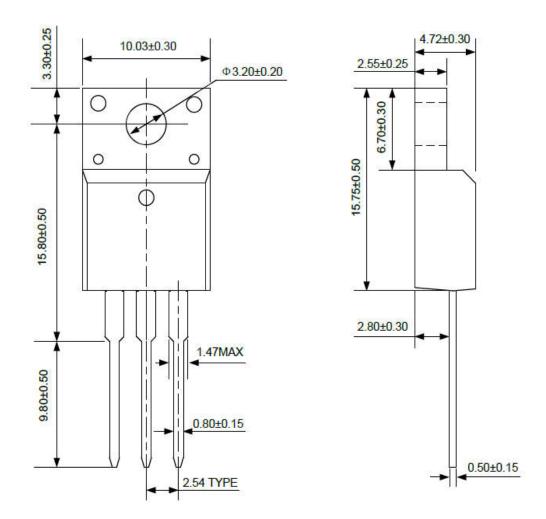
0.6 0.4 V_{GS(th)}, (Variance) 0.2 0 $I_D = 250 \mu A$ -0.2 -0.4-0.6 -0.8 -1 -1.2 -100 -50 0 50 100 150 200 T_J , Junction Temperature (°C)

Figure 8. Typical Drain-to-Source ON Resistance vs Junction Temperature





Package outline drawing



T0-220F



Disclaimers:

GuangDong Reasunos Semiconductor Technology CO.,LTD(Reasunos) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information in current and complete. All products are sold subject to Reasunos's terms and conditions supplied at the time of order acknowledgement.

GuangDong Reasunos Semiconductor Technology CO.,LTD warrants performance of its hardware products to the speciffications at the time of sale. Testing, reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

GuangDong Reasunos Semiconductor Technology CO., LTD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using Reasunos's components. To minimize risk, customers must provide adequate design and operating safeguards.

GuangDong Reasunos Semiconductor Technology CO.,LTD does not warrant or convey any license either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in Reasunos's data sheets or data books is permissible only if reproduction is without modification oralteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. GuangDong Reasunos Semiconductor Technology CO.,LTD is not responsible or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by GuangDong Reasunos Semiconductor Technology CO.,LTD for that product or service voids all express or implied warrantees for the associated Reasunos's product or service and is unfair and deceptive business practice. GuangDong Reasunos Semiconductor Technology CO.,LTD is not responsible or liable for such statements.

Life Support Policy:

GuangDong Reasunos Semiconductor Technology CO., LTD's Products are not authorized for use as critical components in life support devices or systems without the expressed written approval of GuangDong Reasunos Semiconductor Technology CO., LTD.

As used herein:

- 1. Life support devices or systems are devices or systems which:
 - a.are intended for surgical implant into the human body,
 - b. support or sustain life,
 - c. whose failure to when properly used in accordance with instructions for used provided in the laeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.