SPECIFICATIONS

Product Name			Multi-layer Chip Power Inductor					
Sunlord Part Number				MPL	1608S3R3I	ИНТ		
Customer Part	Number							
[⊠New Released, ⊡Revised]					SPEC	No.: M	PL0819000	40
【This SPEC is total 【ROHS Compliant		ncludin	g specifi	cations a	nd appendix.	1		
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【Version change history】

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	Jul.16,2019	New release	/	Hai Guo

Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships,etc.
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Data-processing equipment
- 12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

1. Scope

This specification applies to MPL1608S3R3MHT of multi-layer chip power inductors

2. Product Description and Identification (Part Number)

1) Description

MPL1608S3R3MHT of multi-layer chip power inductors.

2) Product Identification (Part Number)

MPL	<u>1608</u>	<u>s</u>	<u>3R3</u>	M	<u>H</u>	<u>T</u>
1	2	3	4	(5)	6	7

①Туре	
MPL	Chip Power Inductor

③Feature Type	
S	Standard

⑤Inductance Tolerance	
M	±20%

⑥Thickness	
Н	0.9 mm

②External Dimensions (L x W) (mm)		
1608 [0603]	1.6 × 0.8	

Nominal Inductance	
Example	Nominal Value
3R3	3.3µH

⑦Packing	
Т	Tape Carrier Package

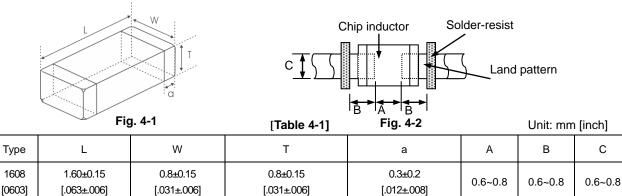
3. Electrical Characteristics

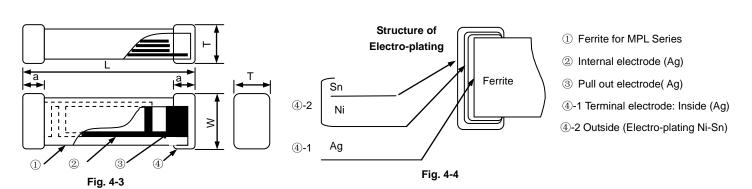
Part Number	L (µH)	L Test Freq. (MHz)	S.R.F Min. (MHz)	DCR (Ω)	emperature Rise Current I _{DC} 1 (mA.) (max.)
MPL1608S3R3MHT	3.3	1	40	0.35±25%	700

- 2) Storage temperature range (packaging conditions): -10°C ~+40°C and RH 70% (Max.)

4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.4-1, Fig.4-2 and Table 4-1.
- 2) Structure: See Fig. 4-3 and Fig. 4-4.





Material Information: See Table 4-2.

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Code	Part Name	Material Name	
1	Ferrite Body	Ferrite Powder	
2	2 Inner Coils Silver Pas		
3	Pull-out Electrode (Ag)	Silver Paste	
4 -1	Terminal Electrode: Inside Ag Termination Silver Co		
4 -2	Electro-Plating: Ni/Sn plating	Plating Chemicals	

5. Test and Measurement Procedures

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- a. Ambient Temperature: 20±15℃b. Relative Humidity: 65±20%
- c. Air Pressure: 86 kPa to 106 kPa

If any doubt on the results, measurements/tests should be made within the following limits:

- a. Ambient Temperature: 20±2°C
- b. Relative Humidity: 65±5%
- c. Air Pressure: 86kPa to 106 kPa

5.2 Visual Examination

a. Inspection Equipment: 20x magnifier

5.3 Electrical Test

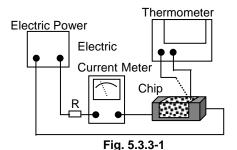
- 5.3.1 DC Resistance (DCR)
 - a. Refer to Item 3.
 - b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B or equivalent.

5.3.2 Inductance (L)

- a. Refer to Item 3.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
- c. Test signal: -20dBm or 50mV.
- d. Test frequency refers to Item 3.

5.3.3 Temperature Rise Current $(I_{DC}1)$

- a. Refer to Item 3.
- b. Test equipment (see Fig. 5.3.3-1): Electric Power, Electric current meter, Thermometer.
- c. Measurement method (see Fig. 5.3.3-1):
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of chip surface.
 - 3. Gradually increase voltage and measure chip temperature for corresponding current.
 - 4. Definition of Temperature Rise Current ($I_{DC}1$) : $I_{DC}1$ is direct electric current as chip surface temperature rose just 40° C against chip initial surface temperature (Ta) (see **Fig. 5.3.3-2**)



Temperature (°C)

+40
Ta

0

I_{DC}1

Fig. 5.3.3-2

5.3.4 Saturation Current (I_{DC}2)

- a. Refer to Item 3.
- b. Test equipment: HP6632B system DC power supply, HP4291B+HP16192A+HP16200A or equivalent.
- c. Measurement method:
 - 1. Measurement conditions of initial inductance L: Measuring Frequency: 1MHz.

Test Current: 1mA.

2. Raising the voltage of the DC power supply, measure the inductance at the various current.

The rated current is the value of DC current at which the inductance will be 30% down compared with the initial inductance value.

Note: In the period of raising voltage, voltage cannot be reduced.

3. Definition of Saturation Current ($I_{DC}2$): $I_{DC}2$ is the value of DC current as inductance L (μ H) decreased just 30% against initial value (see Fig. 5.3.4-1).

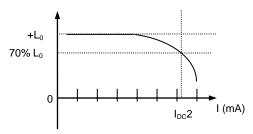


Fig. 5.3.4-1

5.3.5Self-Resonant Frequency (SRF)

- a. Refer to Item 3.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
- c. Test signal: -20dBm or 50 mV.

5.4 Reliability Test

ltems	Requirements	Test Methods and Remarks
5.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur. Chip Glass Epoxy Board Fig.5.4.1-1	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.1-1) using eutectic solder. Then apply a 10N force in the direction of the arrow. Keep time: 10±1s. Speed: 1.0mm/s.
5.4.2 Resistance to Flexure	Unit: mm [inch] Type a b c 1608[0603] 1.0 3.0 1.2	 Solder the inductor to the test jig (glass epoxy board shown in Fig.5.4.2-1) Using a eutectic solder. Then apply a force in the direction shown Fig. 5.4.2-2. Flexure: 2mm. Pressurizing Speed: 0.5mm/sec. Keep time: 30 sec.
5.4.3 Vibration	No visible mechanical damage. Inductance change: Within ±20%. Cu pad Solder mask Glass Epoxy Board Fig. 5.4.3-1	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.3-1) using eutectic solder. The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours).
5.4.4 Dropping 5.4.5 Temperature 5.4.6 Solderability	 No visible mechanical damage. Inductance change: Within ±20%. Inductance change should be within ±20% of initial value measuring at 20℃. No visible mechanical damage. Wetting shall exceed 95% coverage. 	Drop chip inductor 10 times on a concrete floor from a height of 100 cm. Temperature range: -40°C ~ +85°C Reference temperature: +20°C ① Solder temperature: 240±2°C ② Duration: 3 sec. ③ Solder: Sn/3.0Ag/0.5Cu. ④ Flux: 25% Resin and 75% ethanol in weight.
5.4.7 Resistance to Soldering Heat	 No visible mechanical damage. Wetting shall exceed 95% coverage. Inductance change: Within ±20%. 	 Solder temperature: 260±3°C. Duration: 5 sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight. The chip shall be stabilized at normal condition for 1~2 hours before measuring.

Items	Requirements	Test Methods and Remarks	
5.4.8 Thermal Shock	① No mechanical damage. ② Inductance change: Within ±20%. 30 min. 30 min. Ambient Temperature -40℃ 30 min. 20sec. (max.)	 Temperature, Time: (See Fig.5.4.8-1) -40°C for 30±3 min→ 85°C for 30±3min. Transforming interval: 20 sec.(max.). Tested cycle: 100 cycles. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 	
5.4.9 Resistance to Low Temperature	No mechanical damage. Inductance change: Within ±20%.	 Temperature: -40±2°C Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 	
5.4.10 Resistance to High Temperature	No mechanical damage. Inductance change: Within ±20%.	 Temperature: 85±2°C Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 	
5.4.11 Damp Heat (Steady States)	No visible mechanical damage. Inductance change: Within ±20%.	 Temperature: 60±2°C Humidity: 90% to 95% RH. Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 	

6. Packaging and Storage

6.1 Packaging

Tape Carrier Packaging:

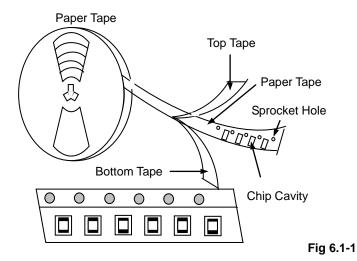
Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

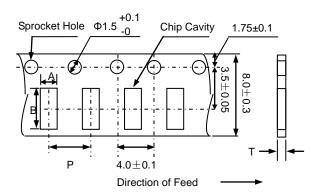
Type	1608[0603]	
T(mm)	0.8±0.15	
Tape	PaperTape	
Quantity	4K	

- c. Reel shall be packaged in vinyl bag.
- d. Maximum of 5 or 10 reels bags shall be packaged in an inner box.
- e. Maximum of 6 or 10 inner boxes shall be packaged in an outer case.

(1) Taping Drawings (Unit: mm)



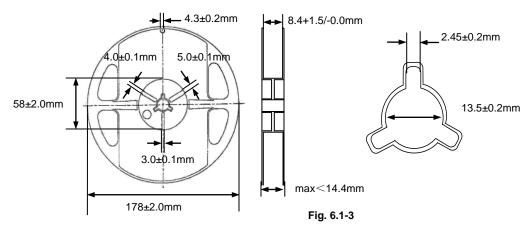
(2) Taping Dimensions (Unit: mm)



Туре	Α	В	Р	Tmax
MPL1608_H	1.0±0.2	1.8±0.2	4.0±0.1	1.1

Fig 6.1-2

(3) Reel Dimensions (Unit: mm)



6.2 Storage

- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCI, sulfurous gas of H_2S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.4.6** shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 12 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

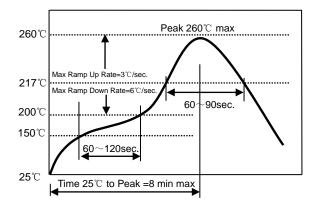
7.1 Reflowing Profile:

 \triangle Preheat condition: 150 ~200 °C/60~120sec.

 \triangle Allowed time above 217°C: 60~90sec.

△ Max temp: 260°C

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]

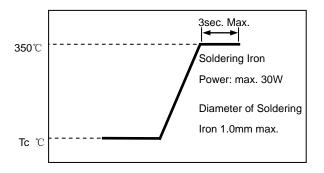


7.2 Iron Soldering Profile.

△ Iron soldering power: Max.30W

 \triangle Pre-heating: 150 $^{\circ}$ C / 60sec.

△ Soldering Tip temperature: 350 °C Max.



8. Supplier Information

a) Supplier:

Shenzhen Sunlord Electronics Co., Ltd.

b) Manufacturer:

Shenzhen Sunlord Electronics Co., Ltd.

c) Manufacturing Address:

Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China 518110