

Features

- Low Quiescent Current: 50µA
- Low Output Noise: 40µVRMS(10Hz~100kHz)
- Operating Voltage Range: 1.8V ~ 6.0V
- Low Dropout Voltage: 50mV@100mA
- High Accuracy: ±2%(Typ.)
- Output Voltage:1.05~ 5.0V
- TTL-Logic-Controlled Shutdown Input
- Excellent Line and Load Transient Response
- Built-in Current Limiter, Short-Circuit Protection
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)

Applications

- Cellular and Smart Phones
- Radio control systems
- Laptop, Palmtops and PDAs
- Digital Still and Video Cameras
- MP3, MP4 Player
- Battery-Powered Equipment

Description

The MC6225K3 series are a group of positive voltage regulators manufactured by CMOS technologies with high ripple rejection, ultra-low noise, low power consumption and low dropout voltage, which can prolong battery life in portable electronics. The MC6225K3 series work with low-ESR ceramic capacitors, reducing the amount of board space necessary for power applications. The MC6225K3 series consume less than 0.1µA in shutdown mode and have fast turn-on time less than 50µS. The series are very suitable for the battery-powered equipments, such as RF applications and other systems requiring a quiet voltage source.

MCC Part Number	Device Marking
MC6225K3-1.2	ACdXX ⁽¹⁾
MC6225K3-1.8	ACjXX ⁽¹⁾
MC6225K3-2.5	ACqXX ⁽¹⁾
MC6225K3-3.3	ACyXX ⁽¹⁾

Note:

1. "XX" indicate DateCode.

Low Noise CMOS Voltage Regulators



DIMENSIONS					
DIM	INCHES		MM		NOTE
DIN	MIN	MAX	MIN	MAX	NOTE
Α	0.113	0.117	2.87	2.97	
В	0.108	0.112	2.75	2.85	
С	0.061	0.065	1.55	1.65	
D	0.036	0.038	0.914	0.965	
E	0.073	0.077	1.85	1.95	
G	0.0016	0.0039	0.04	0.100	
Н	0.041	0.045	1.05	1.15	
J	0.006	0.007	0.14	0.17	
K	0.012	0.020	0.30	0.50	

Suggested Solder Pad Layout





Pin Configuration and Functions (Top View)



Number	Name	Function
1	V _{ss}	Ground
2	V _{out}	Output Pin
3	V _{in}	Power Input Pin

Typical Application Circuit





Absolute Maximum Ratings

- Operating Free Air Temperature Range: -40~+85°C
- Operating Junction Temperature Range: -40~+125°C
- Storage Temperature Range: -40~+125°C
- Thermal Resistance: 400°C/W Junction to Ambient

Parameter	Symbol	Ratings	Units
Input Voltage	V _{IN}	V _{SS} -0.3 ~ V _{SS} +7	V
Output Voltage	V _{OUT}	V _{SS} -0.3 ~ V _{IN} +0.3	V
Output Current	I _{OUT}	500	mA
Power Dissipation	P _D	0.38	W

Electrical Characteristics(V_{IN}=V_{OUT}+1V, C_{IN}=C_{OUT}=1µF, T_A=25°C, unless otherwise specified)

Paramete	er	Symbol	Conditions	Min.	Тур.	Max.	Units
Output Voltage		V _{OUT(E)} ⁽²⁾	I _{OUT} =1mA	Vout*0.98	Vout	Vout*1.02	V
Supply Curr	rent	lss	l _{ouτ} =0		50	100	μA
Standby Cur	rent	ISTBY	CE = V _{SS}		0.1	1	μA
Output Curr	rent	l _{оит}	—	500			mA
Dropout Volta	age	Vdif ⁽³⁾	l _{oυ⊺} =100mA V _{oυτ} ≥3.3V		50		mV
Load Regula	ation	ΔVουτ	V _{IN} = V _{OUT} +1V, 1mA≤I _{OUT} ≤100mA		1		mV
Line Regula	tion		I _{OUT} =10mA V _{OUT} +1V≤V _{IN} ≤6V		0.01	0.2	%/V
Output Volta Temperatu Characteris	ire		l _{ou⊤} =10mA -40≤T≤+85		50		ppm
Short Curre	ent	I _{Short}	V _{OUT} =V _{SS}		50		mA
Input Voltage		VIN	—	1.8		6.0	V
	100Hz				75		
Power Supply	1kHz	PSRR	Ι _{ουτ} =50mA		80		dB
Rejection Rate	10kHz				80		
CE "High" Voltage		Vce"H"		1.5		Vin	V
CE "Low" Voltage		Vce"l"				0.3	V
C _{OUT} Auto-Discharge Resistance		R _{DISCHRG}	V _{IN} =5V,V _{OUT} =3.0V, V _{CE} =V _{SS}		60		Ω

Note:

2. $V_{OUT(E)}$: Effective Output Voltage (Ie. The output voltage when $V_{IN} = (V_{OUT} + 1.0V)$ and maintain a certain I_{OUT} Value).

3. Vdif : The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output Voltage Equals To 98% Of V_{OUT(E)}.



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Curve Characteristics



V_{OUT}=1.8V I_{LOAD}=150mA 60 80 100 120







Ordering Information

Device		Packing	
Part Number-TP		Tape&Reel: 3Kpcs/Reel	

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