

DIO5322

40V/24V/18V Step-Up LED Driver with PWM to Constant Current Dimming Mode

Features

- Drive up to 10 serial LEDs for DIO5322A
- PWM to constant current dimming mode
- Integrated 40V high current switch (1.8A limit)
- Wide V_{IN} input range: 2.7V~5.5V
- Up to 90% PWM converter efficiency
- Low 300mV feedback voltage
- LED open-circuit (OVP) protection
DIO5322A @ 40V
DIO5322B @ 24V
DIO5322C @ 18V
- High switching frequency@1.1MHz
- For compact solution size
- Integrated soft start
- < 1 μ A shutdown current
- Compact SOT23-6 package green compliant
- -40 to +85°C Temperature range

Applications

- LED backlighting
- Mobile Phones
- Handheld Devices
- Digital Photo Frames
- Automotive Navigation

Descriptions

DIO5322 is a serial white LED driver, featuring architecture of 90% high-efficiency current mode boost converter, driving up to 10 serial LEDs or a 3x13 (3LEDs in rows) LED matrix (DIO5322A). And it adopts PWM to Constant Current dimming control mode, with wide frequency range from 200Hz to 200kHz (The dimming frequency above 10kHz is recommended). The serial configuration assures the very most brightness consistency of the whole LED array.

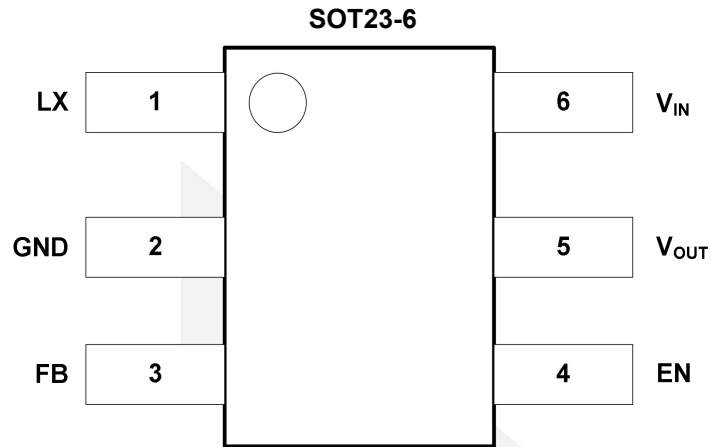
DIO5322 works on 1.1MHz switching frequency, which can maximize current output of 1.8A limit and achieve high current conversion efficiency and result in external compact component size. Additionally, the total external component number is minimized due to the integrated low-side power MOSFET.

DIO5322 integrates multiple protection features, such as LED open-circuit protection, thermal shutdown protection and cycle-by-cycle input current limit protection. And the built-in soft start circuit limits inrush current when the circuit starts.

Ordering Information

Order Part Number	Top Marking		T_A	Package	
DIO5322ACST6	YWGA	Green	-40 to +85°C	SOT23-6	Tape & Reel, 3000
DIO5322BCST6	YWGB	Green	-40 to +85°C	SOT23-6	Tape & Reel, 3000
DIO5322CCST6	YWGC	Green	-40 to +85°C	SOT23-6	Tape & Reel, 3000

Pin Assignment



Pin Descriptions

Name	Description
LX	Converter switching node
GND	Converter/IC ground
FB	Output feedback pin regulated at 0.3V
EN/SHDN	IC enable and PWM dimming control pin
V _{OUT}	Converter output and over-voltage protection sensing pin
V _{IN}	IC supply voltage



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Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Rating” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameter		Rating	Unit
Supply Voltage / V_{IN}		-0.3 to 6.0	V
High Voltage Nodes / LX, V_{OUT}		-0.3 to 40	V
Other pins / FB, EN		-0.3 to $V_{IN} + 0.3$	V
Operating Temperature Range / T_J		-40 to 150	°C
Storage Temperature Range / T_S		-65 to 150	°C
Lead Temperature Range / T_{LEAD}		300	°C
Thermal Resistance / θ_{JA} SOT23-6		190	°C/W
Maximum Power Dissipation at $T_A < 25^\circ\text{C}$		0.526	W
ESD	CDM, JEDEC: JESD22-C101	2	kV

Recommend Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

Parameter	Rating	Unit
Supply Voltage	2.7 to 5.5	V
Operating Temperature Range	-40 to 85	°C



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Electrical Characteristics

Typical value: $V_{CC}=3.6V$, $T_A = 25^{\circ}C$, unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
IC Supply						
V _{IN}	Input operating range		2.7		5.5	V
UVLO	Input under voltage lockout	Rising edge		2.6		V
UVLO _{HYST}	UVLO hysteresis			0.2		V
I _Q	IC quiescent current(non switching)	FB=0.4V		0.2		mA
	IC quiescent current(switching)	FB=0V		0.7		mA
I _{SHDN}	V _{IN} pin shutdown current	EN=GND			0.1	μA
Boost Converter						
FB	FB pin accuracy		0.288	0.3	0.312	V
f _{ref(3dB)}	Vref filter 3dB Frequency			420		Hz
I _{FB}	FB pin bias current			0.1		μA
R _{DS(ON)}	NMOS on-resistance			0.3		Ω
I _{LX}	LX pin leakage current			0.1		μA
I _{LIM}	Peak NMOS current limit			1.8		A
F _{SW}	Oscillator frequency			1.1		MHz
D _{MAX}	Maximum duty cycle		92	95		%
OVP	Over voltage threshold, Measured at V _{OUT} pin	DIO5322A	36	40		V
		DIO5322B	21.5	24		
		DIO5322C	16	18		
T _S	Start-up time	DIO5322A		2.5		ms
		DIO5322B/C		1.5		
Logic Signal						
V _{TH-L}	Logic low threshold				0.4	V
V _{TH-H}	Logic high threshold		1.4			V
T _{OFF}	EN low to shutdown time			2.4		ms
F _{EN}	Dimming frequency		0.2		200	kHz
T _{J-TH}	IC junction thermal shutdown threshold			145		°C
	IC junction thermal shutdown hysteresis			15		°C

Specifications subject to change without notice.

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Application Circuit

In typical application, DIO5322A is competent in the below two configurations: 10 series LED-Array and 3(row)x13 LED Matrix. As depicted in following figures:

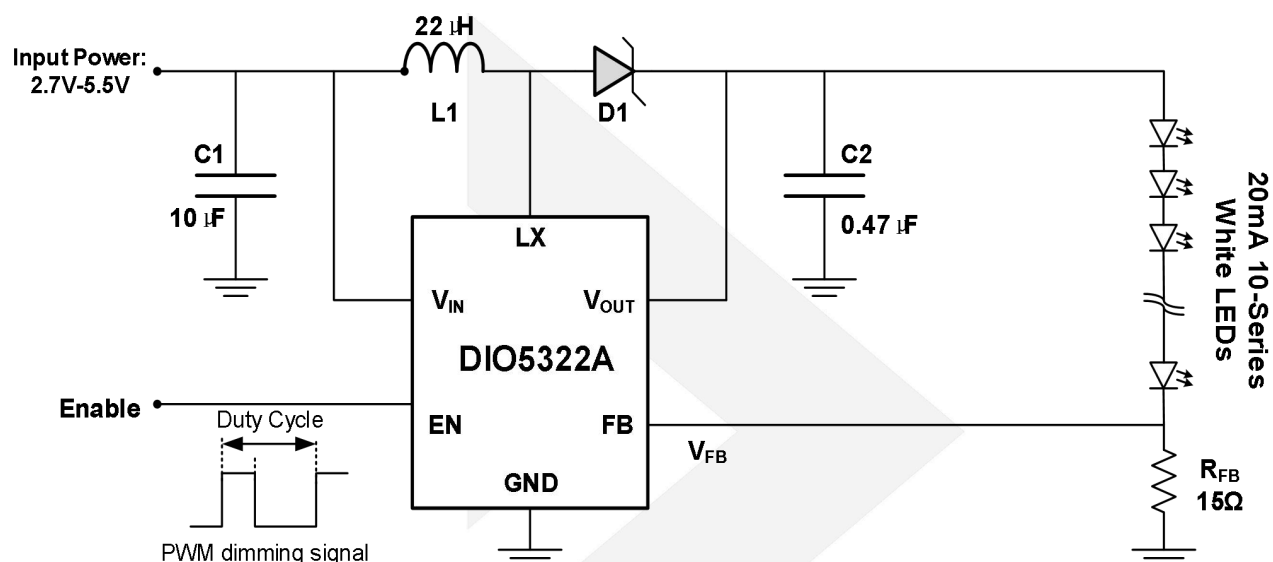


Figure 2. 10 Series LED Array Application Circuit Configuration

Note: A 22µH inductor is recommended for DIO5322A, a 10µH inductor is recommended for DIO5322B/C.

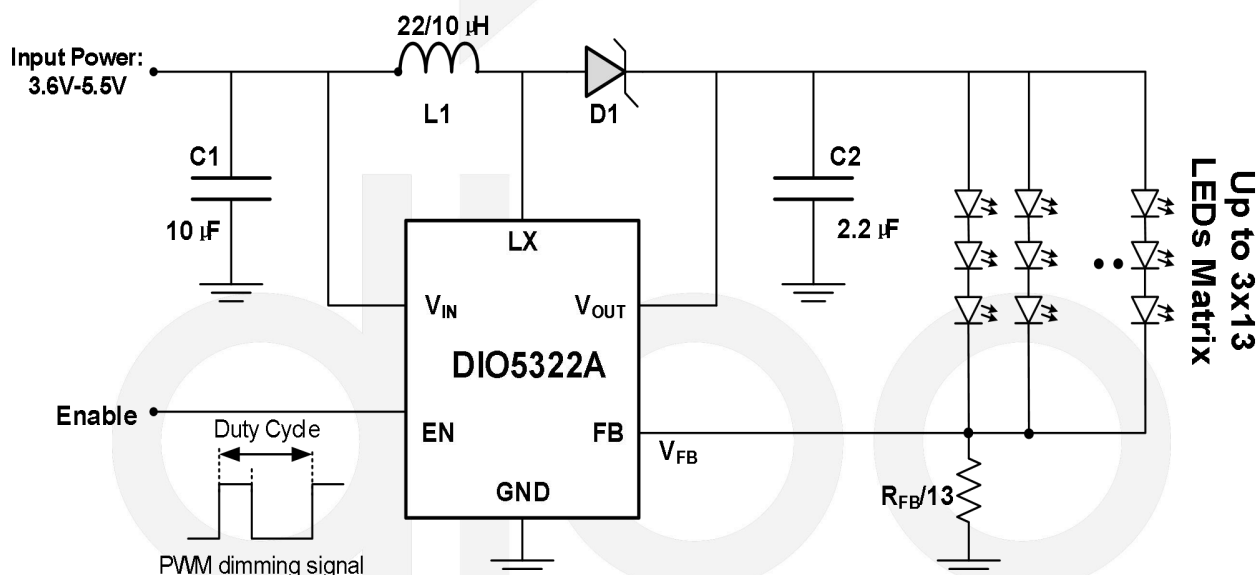


Figure 3. 3x13 (3 LEDs in row) LED Matrix Application Circuit Configuration

Functional Block Diagram

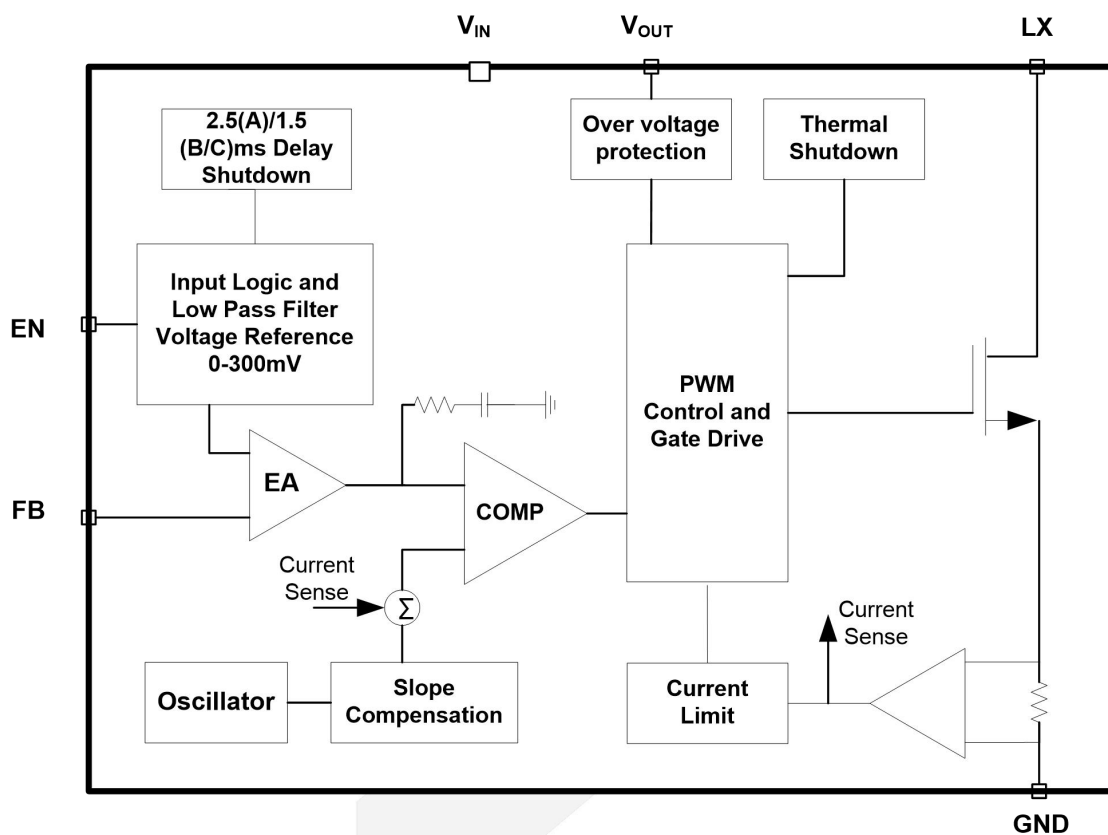


Figure 4. Functional Block Diagram

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Operation Principle

DIO5322 utilizes a constant frequency current-mode boost converter architecture to power white LED strings or arrays by pumping current precisely regulated by feedback voltage V_{FB} and feedback resistor R_{FB} , illustrated in Figure 6, Page 8.

As illuminated in the Functional Block Diagram above, PWM Control and Gate Drive Block periodically opens and closes the Power MOSFET synchronized with Oscillator. At the rising edge of Oscillator pulse, Power MOSFET is turned on, while closed when the comparator COMP tells the Current Sense slope ramp goes above the output "difference" of error amplifier EA. The Current Sense slope ramp is generated by Current Sense from sampling inductor charging current and compensated by Slope Compensation. And the output "difference" comes from comparing feedback voltage V_{FB} with internal reference voltage V_{REF} by Error Amplifier EA. Both the two close loops assure the output current stabilization and make feedback voltage V_{FB} in consistency with reference voltage V_{REF} . and we will see in the next section that the IC dims LEDs by regulating the reference voltage.

DIO5322 integrates soft-start to limit the inrush current and the overshoot on the output. And DIO5322 also features internal protection circuits such as over-voltage protection (OVP), cycle-by-cycle current limit protection and thermal shutdown.

DIO5322 adopts PWM dimming control by regulating the reference voltage. The PWM signals through a low-pass filter, then transfer to an analog reference voltage (PWM to Constant Current Mode). Pin EN listens PWM signals from 200Hz to 200kHz. Once powered on, the DIO5322 gradually soft starts to output LED current proportional to the PWM signal duty cycle. A 100% duty cycle PWM signal leads the IC to maximum current output. And a 0% duty cycle PWM signal or a 2.5(A)/1.5(B/C) ms long low pulse will shutdown the IC. Any non-0% duty cycle PWM signal to Pin EN will arouse the IC again.

All the dimming process could be explained in detail that, the output LED current is controlled by the internal feedback voltage V_{FB} , as illustrated in the following Application Notes- LED current Setting Section. And the value of V_{FB} (from 0 to 300mV) is absolutely proportional to the duty cycle of PWM signal (from 0% to 100%). More is depicted in the below figure.

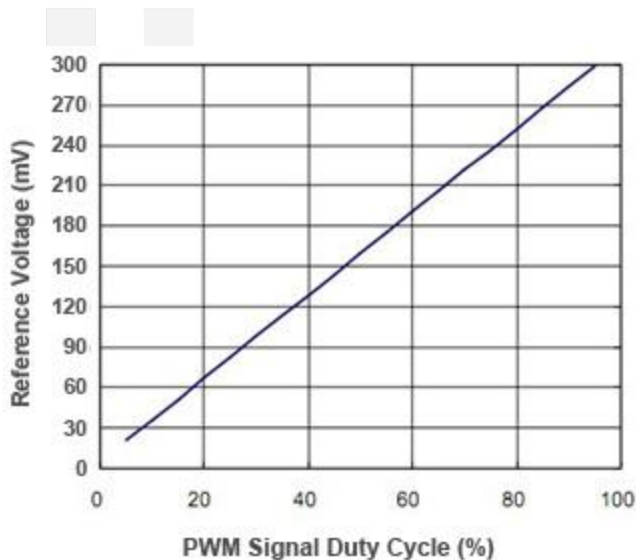


Figure 5. Duty cycle of PWM signal vs. Reference Voltage V_{REF}

Application Notes:

MAX LED Current Settings

LED Current is determined by the current through the feedback resistor R_{FB} , as depicted in the right-hand figure. V_{FB} is a high-impedance state input feedback voltage, so no current goes through Pin FB and the built-in "boost" DC/DC has to pump current to feed I_{FB} .

$$I_{LED} = I_{FB} = V_{FB} / R_{FB}$$

V_{FB} equals to V_{REF} which is internally set to a maximum value of 300mV. So

$$I_{LED(MAX)} = 300mV / R_{FB}$$

For LED current accuracy, 1% precision resistor is recommended.

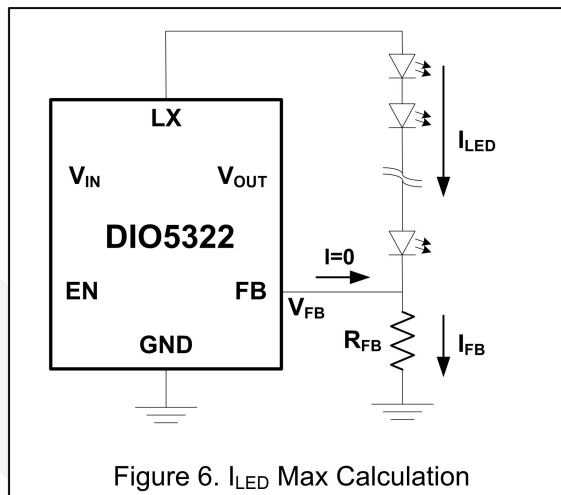


Figure 6. I_{LED} Max Calculation

Feedback Voltage V_{FB} Calculation

The feedback voltage V_{FB} is regulated by the internal reference voltage V_{REF} , which is in detail described in Figure 5. So the V_{FB} is approximately same as V_{REF} in the timing, except for that V_{FB} always has a delay with respect to V_{REF} since both in the start-up or the dimming process, the system takes time to adjust V_{FB} equal to V_{REF} . if we neglect these delays, V_{FB} changes with Pin EN signal just as the V_{REF} .

Recommended LED Dimming Method for 10-LEDs series Application

EN pin PWM signal dimming

The principle of PWM dimming control is explained in detail above. A certain duty cycle PWM signal to EN pin will reset the reference voltage V_{REF} , which will in turn determine the LED current. If we set $R_{FB} = 15.0\Omega$, so $I_{LED(MAX)} = 20mA$, then we have I_{LED} changes with the PWM duty cycle as the following:

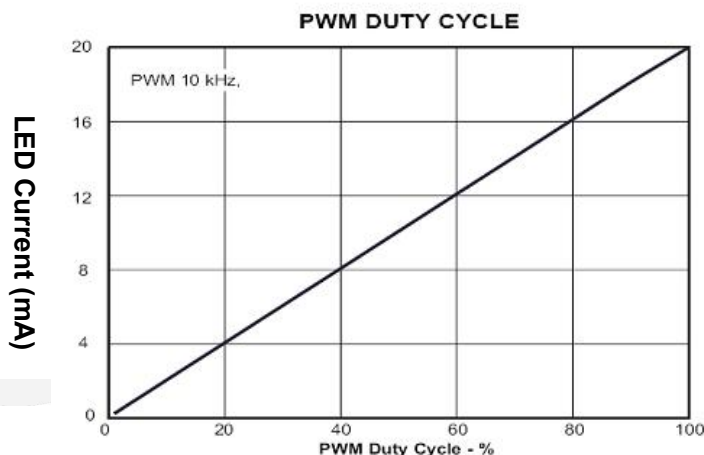


Figure 7. Duty cycle of PWM signal vs. LED Current



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Inductor Selection

A 10μH~22μH inductor is recommended for both 10-LED serial string and 3x13 LED matrix application. For DIO5322A, a 22μH inductor is recommended, and a 10μH inductor is ok for DIO5322B/C. A low DCR inductor could be suggested if a high efficiency is critical. The inductor's saturation current rating should also exceed the peak input current, especially for high load current application such as 3x13 matrix.

Table 1. Inductor Selector

Part Number	Inductor(μH) @100KHz,1V	DCR(Ω) +/-30%	Min. Self-resonant Frequency(MHz)	Saturation Current(A)	Heat Rating Current(A)	Manufacturer
SWPA8040S100MT	10+/-20%	0.029	15	3.60	3.30	Sunlord
SWPA8040S220MT	22+/-20%	0.069	9.5	2.40	2.10	Sunlord

The Calculation Formula of Inductor Peak to Peak (I_{PP}):

$$I_{PP} = \frac{V_{in} \times D}{L \times F_s}$$

The Calculation Formula of The Cycle Duty :

$$D = \frac{t_{on}}{T_s} = \frac{V_{out} + V_f - V_{in}}{V_{out} + V_f}$$

V_{in} : The voltage of Input

I_{PP} : Inductor Peak to Peak

L : The Inductor Value

V_f : The forward bias voltage of Schottky diode

F_s : Switching frequency (1/ T_s)

V_{out} : The voltage of Output

The Calculation Formula of The Inductor Peak current :

$$I_{peak} = I_{in} + \frac{I_{PP}}{2} + \frac{V_{out} * I_{out}}{V_{in} * \eta} + \frac{V_{in} \times D}{2 \times L \times F_s}$$

Capacitor Selection

Small size ceramic capacitors are recommended for DIO5322 application. A 10μF input capacitor and a 0.47μF output capacitor are recommended for 10/8/6-Series LED applications. Larger value output capacitors like 2.2μF are recommended in higher output current applications to minimize output ripple. Ceramic capacitor Vendors such as Sunlord, Murata, AVX, Taiyo Yuden are recommended.

Diode Selection

Since DIO5322's low forward voltage drop and fast reverse recovery time, a schottky diode is recommended. The current rating of the schottky diode should exceed the peak current of the boost converter. The voltage rating should also exceed the target output voltage

Table 2. Diode Selector

Applications	Schottky Diode Part Number	Forward Voltage/V	Forward Current/mA	Reverse Voltage/V	Manufacturer
20mA, 8/10 Serial LEDs 40V OVP	PMEG6010CEJ	0.57	1000	60	NXP
20mA, 6 Serial LEDs 24V OVP	B150	0.75	1000	50	Vishay

PCB Layout Design Recommendation

As for all switching power supplies, especially those high frequency and high current ones, layout is an important design step. If layout is not carefully done, the regulator could suffer from instability as well as noise problems. To reduce switching losses, the LX pin rise and fall times are made as short as possible. To prevent radiation of high frequency resonance problems, proper layout of the high frequency switching path is essential. Minimize the length and area of all traces connected to the LX pin and always use a ground plane under the switching regulator to minimize inter-plane coupling. The loop including the PWM switch, Schottky diode, and output capacitor, contains high current rising and falling in nanosecond and should be kept as short as possible. The input capacitor needs not only to be close to the V_{IN} pin, but also to the GND pin in order to reduce the IC supply ripple. Figure 8 shows a sample layout.

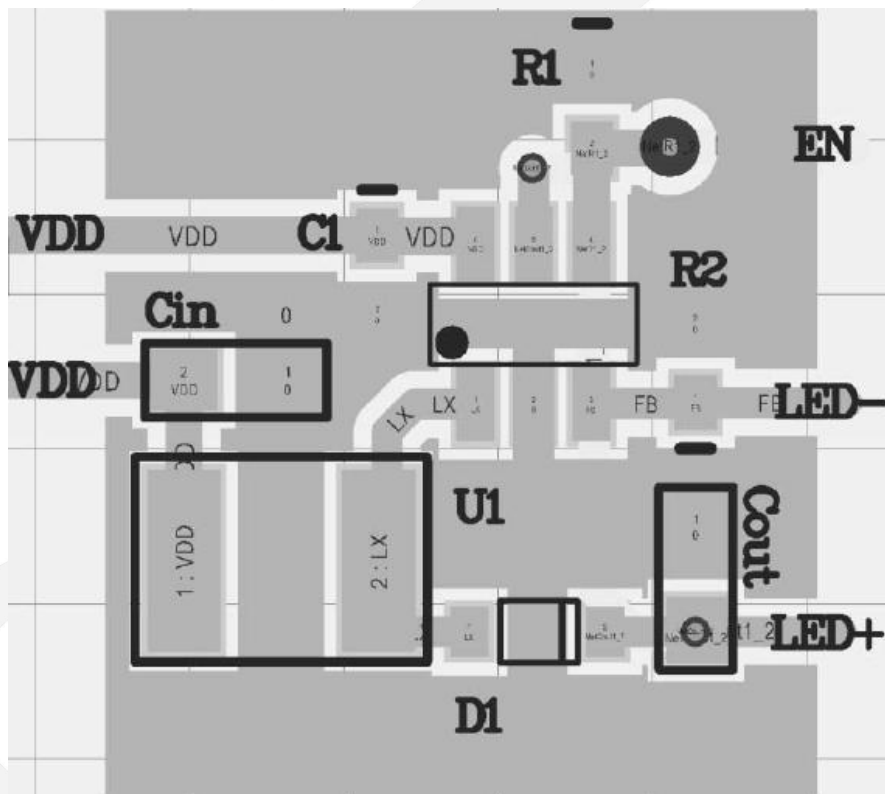
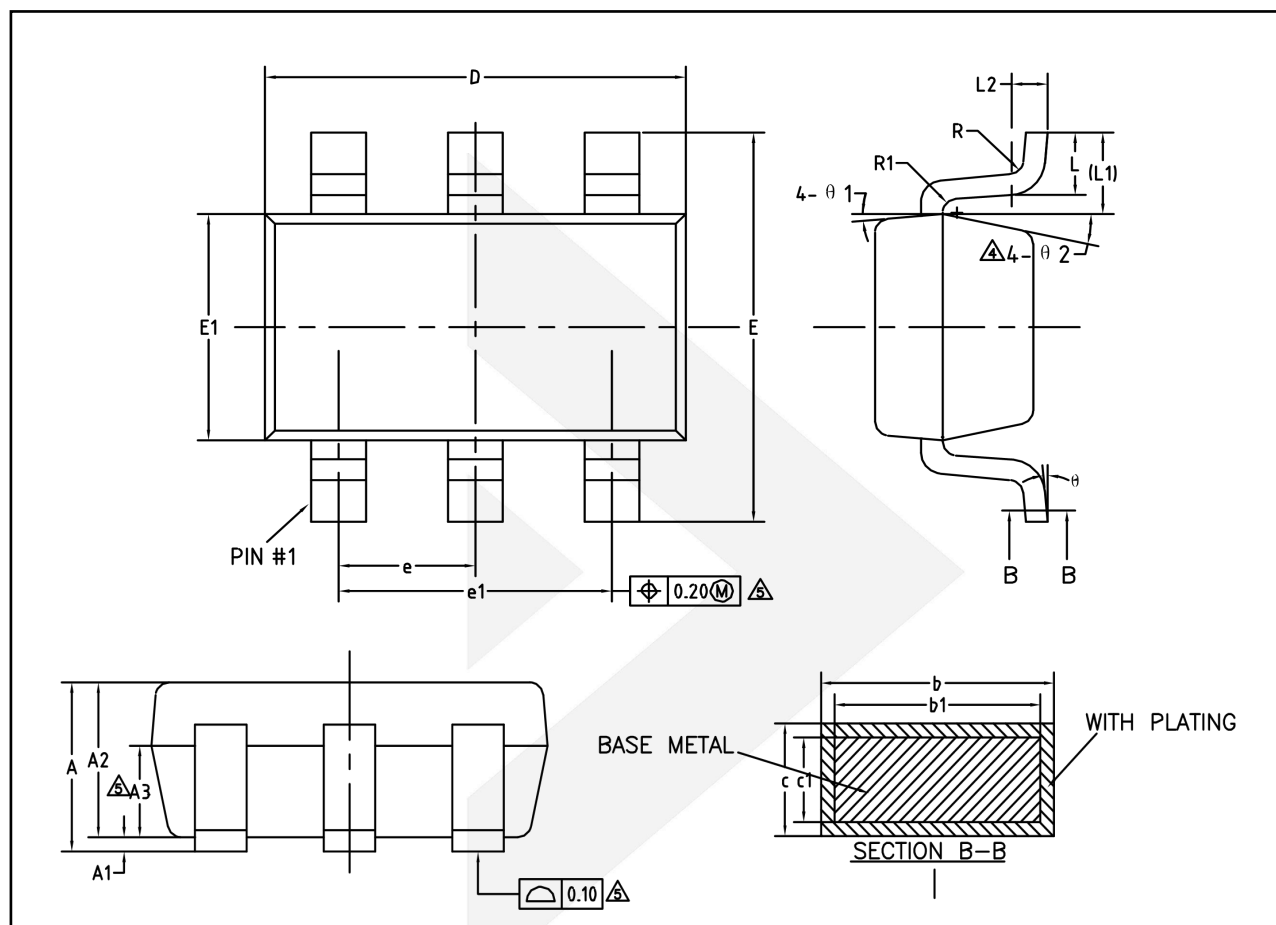


Figure 8. PCB Layout recommended

Physical Dimensions: SOT-23-6



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)			
Symbol	MIN	NOM	MAX
A	-	-	1.25
A1	0	-	0.15
A2	1.00	1.10	1.20
A3	0.60	0.65	0.70
b	0.36	-	0.50
b1	0.36	0.38	0.45
c	0.14	-	0.20
c1	0.14	0.15	0.16
D	2.826	2.926	3.026
E	2.60	2.80	3.00
E1	1.526	1.626	1.726
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
L	0.35	0.45	0.60
L1	0.59REF		
L2	0.25BSC		
R	0.10	-	-
R1	0.10	-	0.25
θ	0°	-	8°
θ1	3°	5°	7°
θ2	6°	-	14°



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