

# TLC696x2/4/8 16-Channel, 2/4/8 Time-Multiplexing, Local Dimming Backlight LED Driver

## 1 Features

- Operating voltage  $V_{CC}$  range: 3 V to 5.5 V
- 16 constant current sinks with high precision:
  - Maximum output current / voltage:
    - 30 mA / 20 V: TLC69602/4/8
    - 60 mA / 20 V: TLC69612/4/8
    - 30 mA / 50 V: TLC69652/4/8
    - 60 mA / 50 V: TLC69662/4/8
  - Device-to-device error:  $\pm 2\%$  (typ.)
  - Channel-to-channel error:  $\pm 2\%$  (typ.)
- Flexible dimming control:
  - Global 8-bit Maximum Current (MC) setting
  - Brightness resolution: up to 15-bit
  - PWM / Hybrid control mode
- High speed daisy chain interface:
  - I/O voltage compatible with: 1.8 V / 3.3 V
  - Data transfer rate: up to 20 MHz
- High system efficiency:
  - Adaptive headroom voltage control (AHVC)
  - Ultra-low device power consumption:
    - Standby mode:  $I_{CC} \leq 200 \mu\text{A}$
    - Normal mode:  $I_{CC} \leq 3.5 \text{ mA}$
- EMI enhancement:
  - Programmable interface driving capability
  - Integrated 4 phase-shifting schemes
- Display quality improvement:
  - Minimum brightness update latency
  - Programmable black insertion
  - Variable refresh rate (VRR) without flickering
- Diagnostics:
  - LED open / short detection for each zone
  - Device thermal shutdown detection
  - Report interface option:
    - UART and interrupt pin (INT)
    - Two-wire output: CLK\_O and SOUT

## 2 Applications

- LCD local dimming backlight:
  - TV
  - Monitor
  - Notebook
  - Tablet

## 3 Description

TLC696x2/4/8 devices are a family of LCD local dimming back-light driver compatible with TLC696x0 which is a scan MOSFET controller to achieve 2/4/8 time-multiplexing control. Each device integrates 16 constant current sinks with corresponding size of SRAM for brightness storage. The device connects to each other by two-wire serial interface in daisy chain topology and supports up to 1024 devices for more than 32,000 local dimming zones.

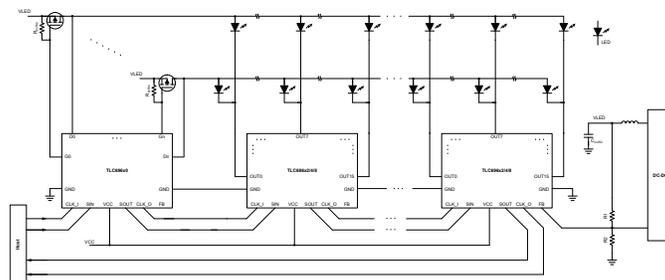
To optimize system efficiency, the device equips with adaptive headroom voltage control scheme to directly control DC/DC. Only the FB pin from last device in serial chain should be connected to DC/DC to achieve simplified system layout. The device also integrates minimum brightness update latency, black insertion and VRR features to improve display quality.

TLC696x2/4/8 has three error flags: LED open detection (LOD), LED short detection (LSD) and thermal shutdown detection (TSD) for diagnostic. The device implements two options for readback including UART/INT and SOUT/CLK\_O which is programmable by register.

### Device Information

PART NUMBER	PACKAGE <sup>(1)</sup>	BODY SIZE (NOM)
TLC696x2/4/8	DSBGA (24)	1.58 mm × 2.43 mm
	WQFN (24)	4 mm × 4 mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.



Simplified Schematic



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## 4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

DATE	REVISION	NOTES
April 2023	*	Initial Release

## 5 Device and Documentation Support

TI offers an extensive line of development tools. Tools and software to evaluate the performance of the device, generate code, and develop solutions are listed below.

### 5.1 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on [ti.com](#). Click on *Subscribe to updates* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 5.2 Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

### 5.3 Trademarks

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### 5.4 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 5.5 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

## 6 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLC69602RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69602	<a href="#">Samples</a>
TLC69602YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69602	<a href="#">Samples</a>
TLC69604RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69604	<a href="#">Samples</a>
TLC69604YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69604	<a href="#">Samples</a>
TLC69608RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69608	<a href="#">Samples</a>
TLC69608YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69608	<a href="#">Samples</a>
TLC69612RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69612	<a href="#">Samples</a>
TLC69612YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69612	<a href="#">Samples</a>
TLC69614RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69614	<a href="#">Samples</a>
TLC69614YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69614	<a href="#">Samples</a>
TLC69618RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69618	<a href="#">Samples</a>
TLC69618YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69618	<a href="#">Samples</a>
TLC69652RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69652	<a href="#">Samples</a>
TLC69652YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69652	<a href="#">Samples</a>
TLC69654RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69654	<a href="#">Samples</a>
TLC69654YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69654	<a href="#">Samples</a>
TLC69658RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69658	<a href="#">Samples</a>
TLC69658YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69658	<a href="#">Samples</a>
TLC69662RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69662	<a href="#">Samples</a>
TLC69662YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69662	<a href="#">Samples</a>

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLC69664RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69664	<a href="#">Samples</a>
TLC69664YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69664	<a href="#">Samples</a>
TLC69668RTWR	ACTIVE	WQFN	RTW	24	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69668	<a href="#">Samples</a>
TLC69668YBHR	ACTIVE	DSBGA	YBH	24	3000	RoHS & Green	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69668	<a href="#">Samples</a>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSELETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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**OTHER QUALIFIED VERSIONS OF TLC69602, TLC69604, TLC69608, TLC69612, TLC69614, TLC69618, TLC69652, TLC69654, TLC69658, TLC69662, TLC69664, TLC69668 :**

- Automotive : [TLC69602-Q1](#), [TLC69604-Q1](#), [TLC69608-Q1](#), [TLC69612-Q1](#), [TLC69614-Q1](#), [TLC69618-Q1](#), [TLC69652-Q1](#), [TLC69654-Q1](#), [TLC69658-Q1](#), [TLC69662-Q1](#), [TLC69664-Q1](#), [TLC69668-Q1](#)

NOTE: Qualified Version Definitions:

- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

## GENERIC PACKAGE VIEW

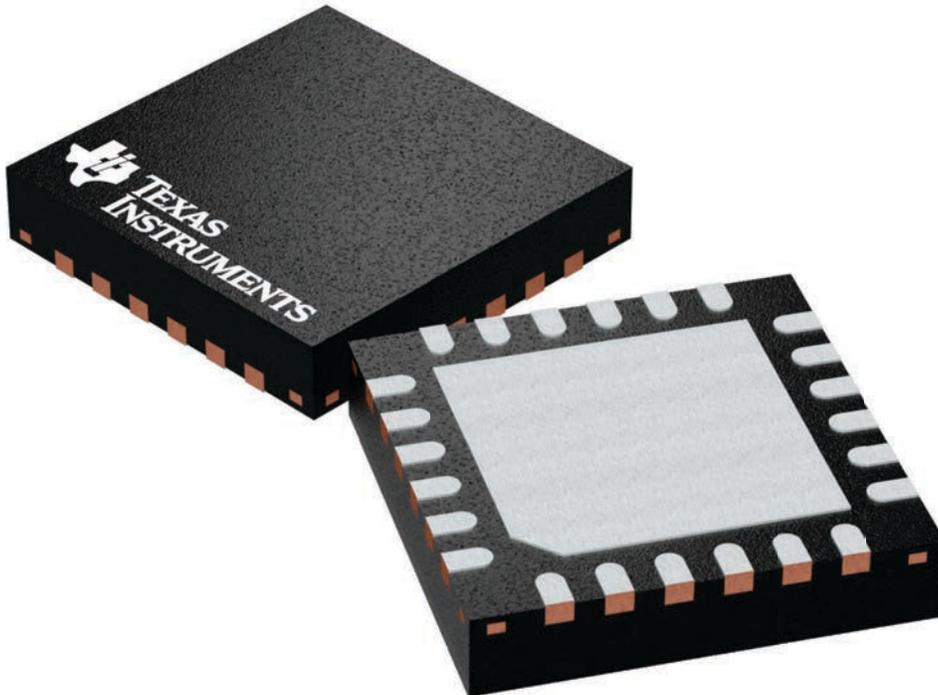
**RTW 24**

**WQFN - 0.8 mm max height**

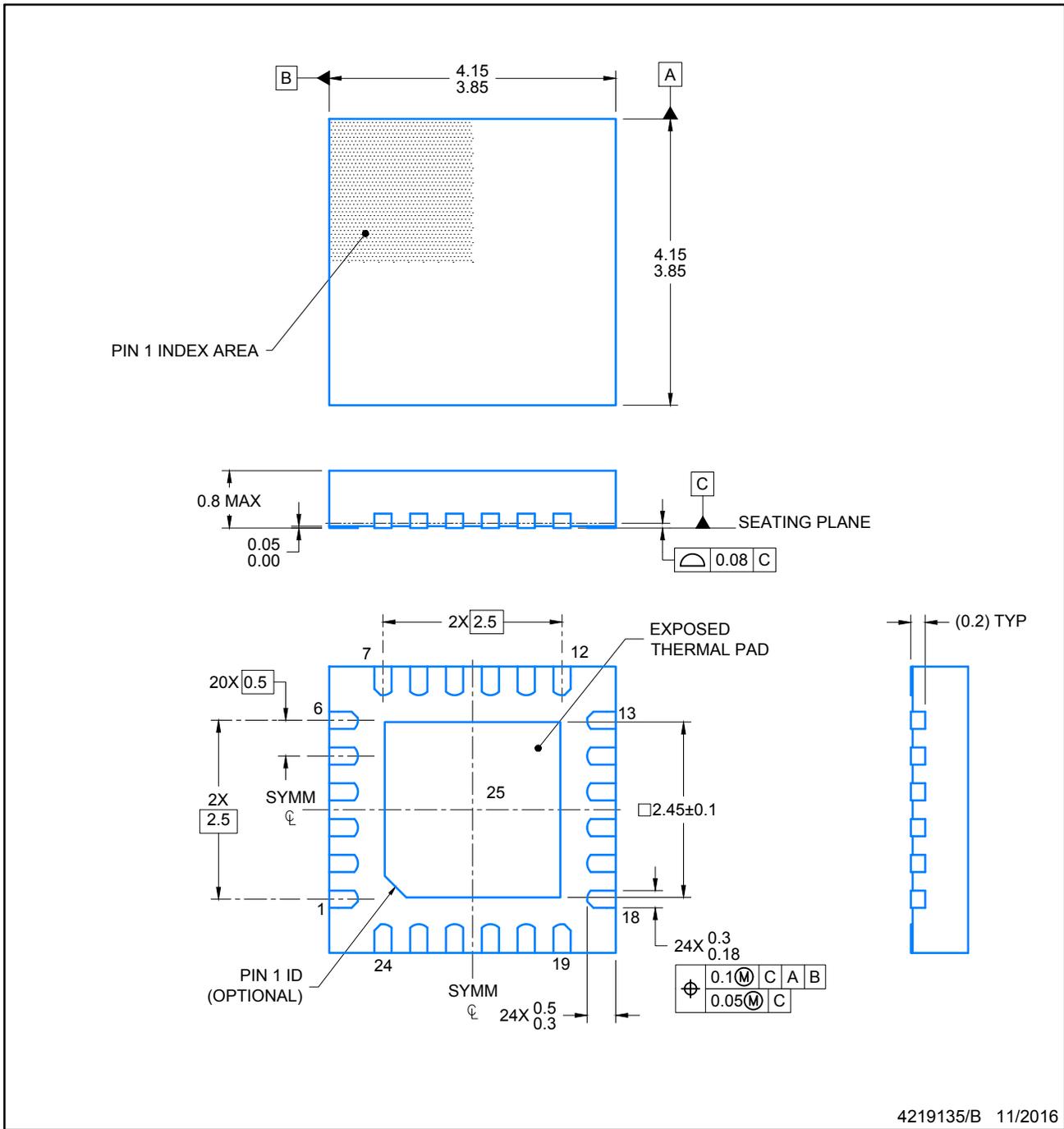
4 x 4, 0.5 mm pitch

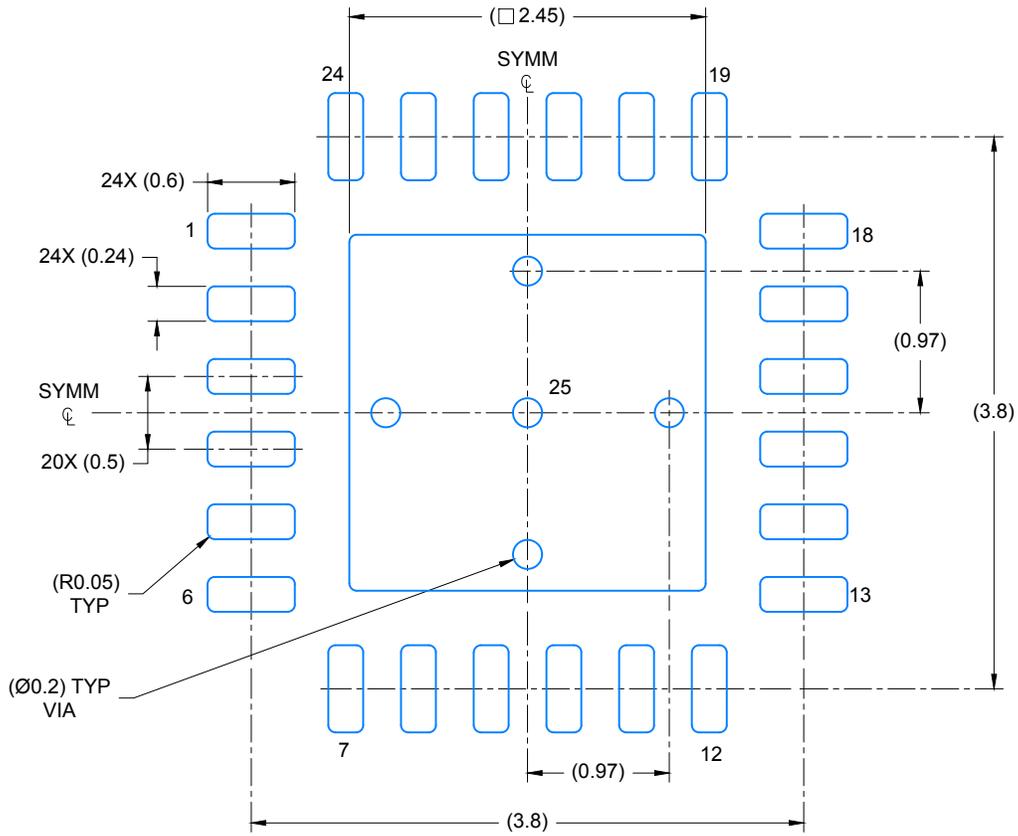
PLASTIC QUAD FLATPACK - NO LEAD

This image is a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

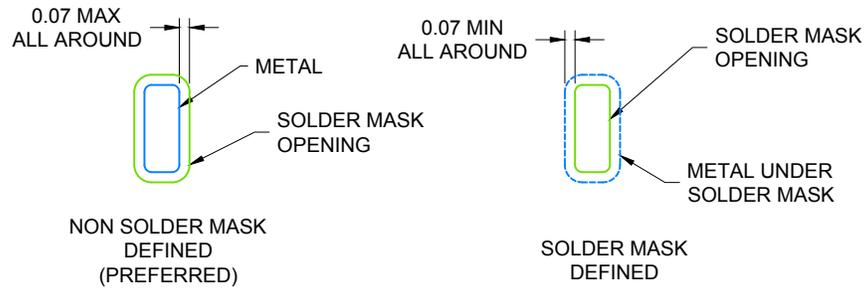


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LAND PATTERN EXAMPLE  
SCALE: 20X

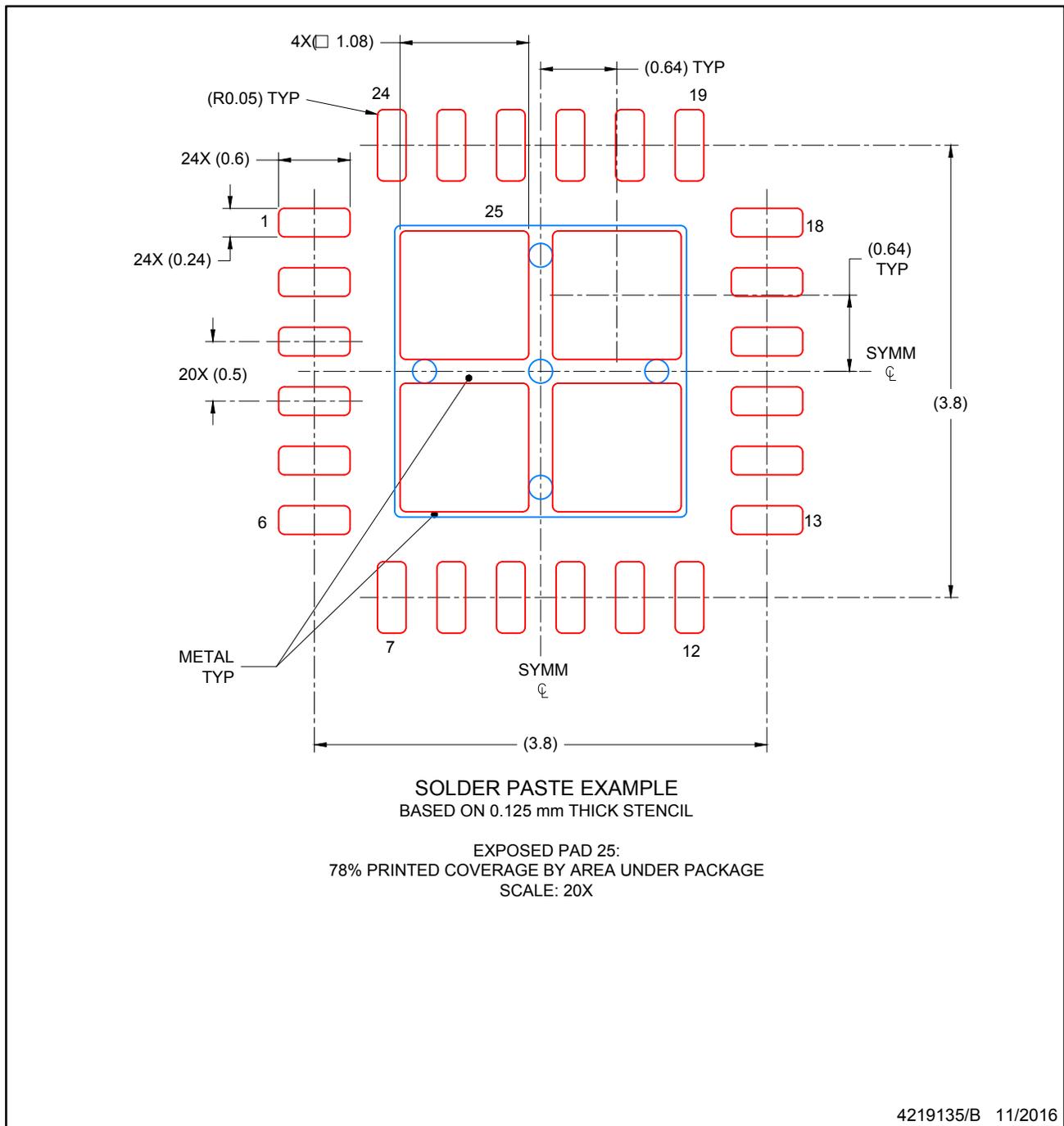


SOLDER MASK DETAILS

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NOTES: (continued)

- For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/sluea271](http://www.ti.com/lit/sluea271)).



NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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