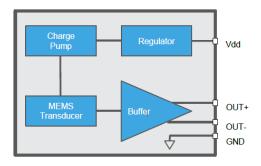
# ANALOG BOTTOM PORT SISONIC™ MICROPHONE



The SPW0878LR5H-1 is a miniature, high-performance, single end to differential mode, matched sensitivity bottom port silicon microphone. Using Knowles' proven high performance SiSonic™ MEMS technology, the SPW0878LR5H-1 consists of an acoustic sensor, a low noise input buffer, and an output amplifier. These devices are suitable for applications such as cellphone, smart phones, laptop computers and other portable electronic device. The microphone has a flat frequency response with low phase distortion for superior noise cancelling algorithm performance. Its high AOP provides large, distortion free dynamic range.

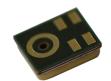


## ABSOLUTE MAXIMUM RATINGS

Table 1: Absolute Maximum Ratings

Parameter	Absolute Maximum Rating	Units
Vdd to Ground	-0.5, +5.0	V
OUT+, OUT- to Ground	-0.3, Vdd+0.3	V
Input Current	±5	mA
Storage Temperature	-40 to +125	°C
Operating Temperature	-40 to +85	°C

Stresses exceeding these "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation at these or any other conditions beyond those indicated under "Acoustic & Electrical Specifications" is not implied. Exposure beyond those indicated under "Acoustic & Electrical Specifications" for extended periods may affect device reliability.





### **PRODUCT FEATURES**

- Extended Operating Temperature Range
- Matched Sensitivity
- LGA Package
- Flat Frequency Response
- Low phase disortion
- High AOP
- Bottom Port
- Ultra-Stable Performance
- Omnidirectional
- · Standard SMD Reflow

## TYPICAL APPLICATIONS

- Headsets
- Portable electronics
- Cellphones
- Laptop Computers
- Tablets
- Portable Music Recorders





## **ACOUSTIC & ELECTRICAL SPECIFICATIONS**

#### Table 2: Normal Mode (NM) Microphone Specifications

Test Conditions: 23 ±2°C, 55±20% R.H., Vdd=2.75V, no load, unless otherwise indicated

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Supply Voltage	Vdd		2.3	2.75	3.6	V
Supply Current	Idd	Vdd = 2.75 V	-	220	-	μΑ
Sensitivity	S	94 dB SPL @ 1kHz, Single-Ended	-45	-44	-43	- dBV/Pa
Sensitivity	3	94 dB SPL @ 1kHz, Differential	-39	-38	-37	UDV/Fa
Signal to Noise Ratio	SNR	94 dB SPL @ 1kHz, A-weighted, Single-Ended Mode	_	65	-	- dB(A)
Signal to Noise Natio	SINK	94 dB SPL @ 1kHz, A-weighted, Differential Mode	_	65	-	UB(A)
Near-Ultrasonic SNR		94 dB SPL, @ 19 kHz , BW = 18.5 - 20.0 kHz	-	76	-	dB
Tatal Hamanania Diatamian	TUD	94 dB SPL @ 1 kHz	-	0.05	-	%
Total Harmonic Distortion	THD	115 dB SPL @ 1 kHz	-	0.15	-	%
		1% THD @ 1 kHz, S = typ	-	131	-	dB SPL
Acoustic Overload Point	AOP	10% THD @ 1 kHz, S = typ	-	134	-	dB SPL
Low Frequency Rolloff	LFRO	-3dB relative to 1 kHz	-	15	-	Hz
High Frequency Flatness		+3dB relative to 1 kHz	-	24	-	kHz
Resonant Frequency Peak	Fres		-	45	-	kHz
Power Supply Rejection	PSRR	200 mVpp sinewave @ 1 kHz, Single-Ended Mode	-	78	-	dB
Ratio	FSKK	200 mVpp sinewave @ 1 kHz, Differential Mode	-	88	-	UB
Davis Complex Dais ation	DOD : N	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, Single-Ended	-	-107	-	-ID)-//A)
Power Supply Rejection	PSR+N	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, Differential	-	-101	-	dBV(A)
DC Output		Vdd= 2.75V	-	0.69	-	V
DC Offset		OUT+ to OUT-	-	-	±20	mV
Output Impedance	Zout	@ 1 kHz	-	380	-	Ω
	Cload		-	-	150	pF
Output Load	Rload	AC-coupled	10	-	_	kΩ
Sensitivity Drop		$Vdd(min) \le Vdd \le Vdd(max)$	-	-	±0.25	dB
Directivity		Omnidirectional		ional		1
Polarity		Increasing sound pressure	Increasing Output Voltage			
Startup Time		S within TBD dB of final value, outputs AC coupled	7	-	15	ms

 $<sup>^{\</sup>rm 1}$  Sensitivity and Supply Current are 100% tested.





Table 3: Low Power Mode (LPM) Microphone Specifications

Test Conditions: 23 ±2°C, 55±20% R.H., Vdd=1.8V, no load, unless otherwise indicated

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Supply Voltage	Vdd		1.6	1.8	1.9	V
Supply Current	Idd	Vdd=1.8V	-	75	-	μΑ
Consitiuity	0	94 dB SPL @ 1kHz, Single-Ended	-45	-44	-43	dDV//Do
Sensitivity	S	94 dB SPL @ 1kHz, Differential	-39	-38	-37	dBV/Pa
Signal to Naise Datio	SNR	94 dB SPL @ 1kHz, A-weighted, Single-Ended Mode	-	64.5	-	dD(A)
Signal to Noise Ratio	SNR	94 dB SPL @ 1kHz, A-weighted, Differential Mode	-	63	-	dB(A)
Near-Ultrasonic SNR		94 dB SPL, @ 19 kHz , BW = 18.5 - 20.0 kHz	-	75	-	dB
T ( )	TUD	94 dB SPL @ 1 kHz	-	0.05	-	%
Total Harmonic Distortion	THD	115 dB SPL @ 1 kHz	-	0.15	-	%
		1% THD @ 1 kHz, S = typ	-	128	-	dB SPL
Acoustic Overload Point	AOP	10% THD @ 1 kHz, S = typ	-	132	-	dB SPL
Low Frequency Rolloff	LFRO	-3dB relative to 1 kHz	-	15	-	Hz
High Frequency Flatness		+3dB relative to 1 kHz	-	24	-	kHz
Resonant Frequency Peak	Fres		-	45	-	kHz
Power Supply Rejection	DODD	200 mVpp sinewave @ 1 kHz, Single-Ended Mode	-	75	-	ID.
Ratio	PSRR	200 mVpp sinewave @ 1 kHz, Differential Mode	-	72	-	- dB
	DOD. N	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, Single-Ended	-	-101	-	ID) ((A)
Power Supply Rejection	PSR+N	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, Differential	-	-95	-	- dBV(A)
DC Output		Vdd= 1.8V	-	0.69	-	V
DC Offset		OUT+ to OUT-	-	-	±20	mV
Output Impedance	Zout	@ 1 kHz	-	380	-	Ω
	Cload		-	-	150	pF
Output Load	Rload	AC-coupled	10	-	-	kΩ
Sensitivity Drop		Vdd(min) ≤ Vdd ≤ Vdd(max)	-	-	±0.25	dB
Directivity			Omnidirectional			
Polarity		Increasing sound pressure	Increasing Output Voltage			
Startup Time		S within TBD dB of final value, outputs AC coupled	-7 /	7 4	15	ms

<sup>&</sup>lt;sup>1</sup> Sensitivity and Supply Current are 100% tested.





Figure 1: Typical Single-Ended Application Circuit

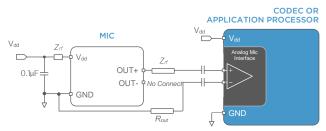
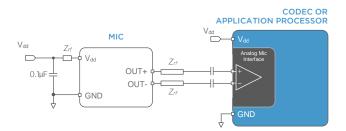


Figure 2: Typical Differential Mode Application Circuit



#### NOTES:

All Ground pins must be connected to ground.

 $If necessary \ to \ improve \ RF \ performance, optional \ series \ components \ (resistors, ferrites, etc.) \ should \ be \ placed \ closest \ to \ the \ microphone \ pads.$ 

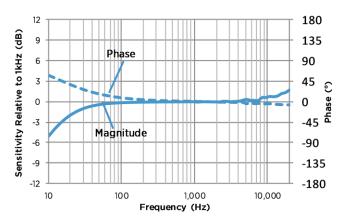
Bypass capacitors should be placed next to each Vdd pin for best performance.

Capacitors near the microphone should not contain Class 2 dielectrics due to their piezoelectric effect.

## PERFORMANCE CURVES

Test Conditions: 23 ±2°C, 55±20% R.H., Vdd=2.75V(NM) / 1.8V(LPM), no load, unless otherwise indicated

Figure 3: Typical Free Field Magnitude and Phase Response



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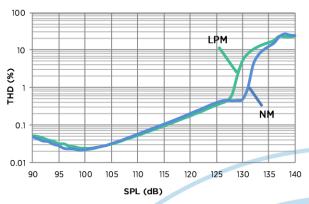


Figure 4: Typical Group Delay

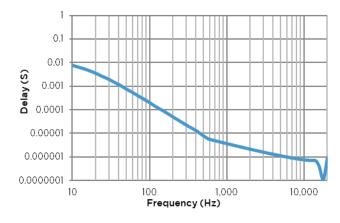


Figure 6: Typical THD vs Frequency

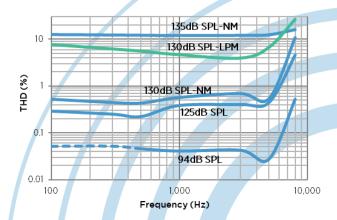






Figure 7: Typical Free Field Ultrasonic Response

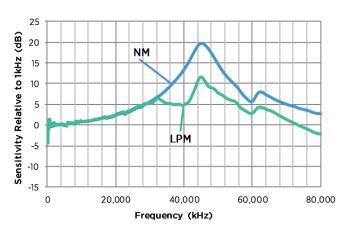


Figure 8: Noise Floor Power Spectral Density - LPM

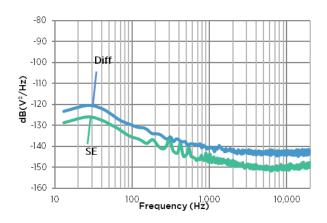


Figure 11: Typical PSRR - LPM

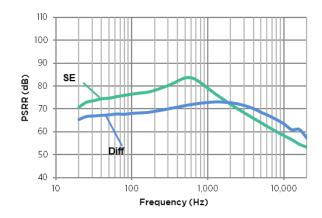


Figure 9: Typical Idd vs Vdd

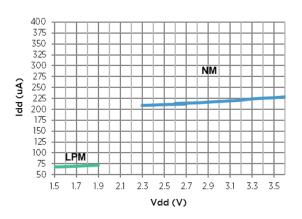


Figure 10: Noise Floor Power Spectral Density - NM

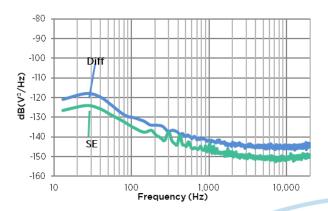
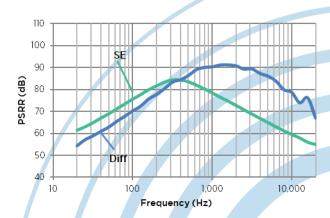


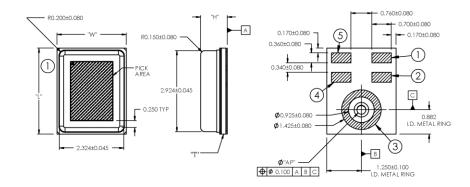
Figure 12: Typical PSRR - NM







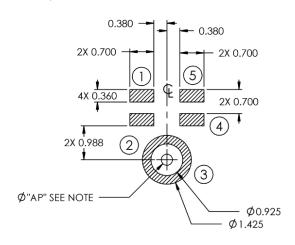
## **MECHANICAL SPECIFICATIONS**



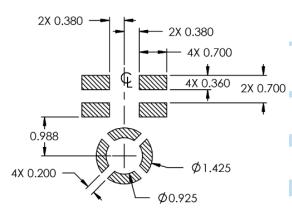
Item	Dimension	Tolerance	
Length (L)	3.1	±0.10	
Width (W)	2.5	±0.10	
Height (H)	0.960	±0.10	
Acoustic Port (AP)	Ø0.325	±0.05	
PCB Thickness (T)	0.290	±0.045	

Pin #	Pin Name	Туре	Description
1	OUT (+)	Signal	Output
2	OUT (-)	Signal	Output
3	GROUND	Power	Ground
4	GROUND	Power	Ground
5	Vdd	Power	Power Supply

## **Example Land Pattern**



# Example Solder Stencil Pattern



#### NOTES



Pick Area only extends to 0.25 mm of any edge or hole unless otherwise specified. Dimensions are in millimeters unless otherwise specified.

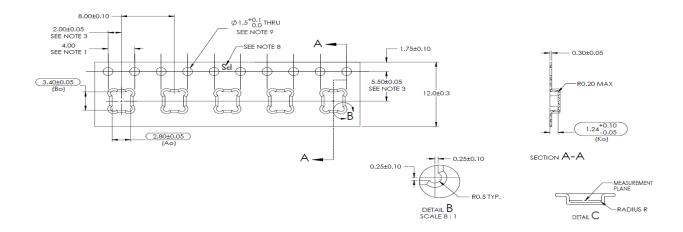
Tolerance is ±0.15mm unless otherwise specified.

In the acoustic path, and . Further optimizations based on application should be performed.



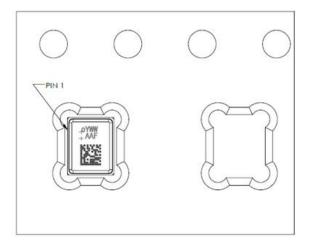


## **PACKAGING & MARKING DETAIL**



Model Number	Suffix	Reel Diameter	Quantity Per Reel
SPW0878LR5H-1	-7	13"	5700

Component	Surface Resistance (ohms)
Reel	10 <sup>5</sup> - 10 <sup>9</sup>
Carrier Tape	10 <sup>5</sup> - 10 <sup>9</sup>
Cover Tape	10 <sup>4</sup> - 10 <sup>10</sup>



Date Code YWW:

Y: Last digit of year

WW: Work week

AA = Project Name Designator:

AR: Ellen

F = Factory Location:

M: Knowles Factory KEM3

C: Knowles Factory KES2

P: Knowles KEI

2D barcode "ABCDEFGHIJKLMNOP":

Unique Job Identification Number for product traceability

#### NOTES:

Dimensions are in millimeters unless otherwise specified.

Vacuum pickup only in the pick area indicated in Mechanical Specifications.

Tape & reel per EIA-481.

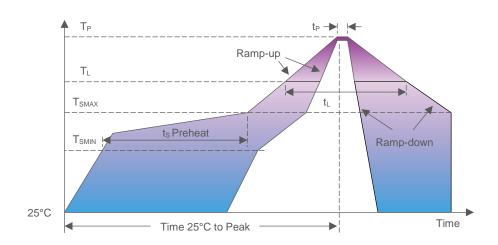
Labels applied directly to reel and external package.

Shelf life: Twelve (12) months when devices are stored in the factory-supplied, unopened ESD moisture sensitive bag under the maximum environmental conditions of 30°C, 70% R.H.





## RECOMMENDED REFLOW PROFILE



Profile Feature	Pb-Free
Average Ramp-up rate (T <sub>SMAX</sub> to T <sub>P</sub> )	3°C/second max.
Preheat  Temperature Min (T <sub>SMIN</sub> )  Temperature Max (T <sub>SMAX</sub> )  Time (T <sub>SMIN</sub> to T <sub>SMAX</sub> ) (t <sub>S</sub> )	150°C 200°C 60-180 seconds
Time maintained above:  Temperature (T <sub>L</sub> )  Time (t <sub>L</sub> )	217°C 60-150 seconds
Peak Temperature (T <sub>P</sub> )	260°C
Time within 5°C of actual Peak Temperature (t <sub>P</sub> )	20-40 seconds
Ramp-down rate (T <sub>P</sub> to T <sub>SMAX</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max

Based on IPC/JDEC J-STD-020 Revision C.

All temperatures refer to topside of the package, measured on the package body surface.

The actual reflow profile used should be optimized based on the reflow requirements of all components, board design, solder paste formulation and reflow equipment used. Details of recommended handling and manufacturing processes can be found in AN25 SMT Manufacturing Guidelines for SiSonic™ Microphones.

- (A) MSL (moisture sensitivity level) Class 1.
- (B) Maximum of 3 reflow cycles is recommended.
- In order to minimize device damage:
  - Do not board wash or clean after the reflow process.
  - Do not brush board with or without solvents after the reflow process.
  - Do not directly expose to ultrasonic processing, welding, or cleaning. Do not insert any object in port hole of device at any time.
  - Do not apply over 30 psi of air pressure into the port hole.
  - Do not pull a vacuum over port hole of the microphone.

  - Do not apply a vacuum when repacking into sealed bags at a rate faster than 0.5 atm/sec.
  - Do not directly expose to vapor phase soldering.





## **MATERIALS STATEMENT**

Meets the requirements of the European RoHS directive 2011/65/EC as amended.

Meets the requirements of the industry standard IEC 61249-2-21:2003 for halogenated substances and Knowles Green Materials Standards Policy section on Halogen-Free.

Product is Beryllium Free according to limits specified on the Knowles Hazardous Material List (HSL for Products).

Ozone depleting substances are not used in the product or the processes used to make the product, including compounds listed in Annex A, B, and C of the "Montreal Protocol on Substances That Deplete the Ozone Layer.

## **RELIABILITY SPECIFICATIONS**

Test	Description
Thermal Shock	100 cycles of air-air thermal shock from -40°C to +125°C with 15 minute soaks (IEC 68-2-4)
High Temperature Storage	+105°C environment for 1,000 hours (IEC 68-2-2 Test Ba)
Low Temperature Storage	-40°C environment for 1,000 hours (IEC 68-2-1 Test Aa)
High Temperature Bias	+105°C environment while under bias for 1,000 hours (IEC 68-2-2 Test Ba)
Low Temperature Bias	-40°C environment while under bias for 1,000 hours (IEC 68-2-1 Test Aa)
Temperature/Humidity Bias	+85°C/85% R.H. environment while under bias for 1,000 hours (JESD22-A101A-B)
Vibration	12 minutes in each X, Y, Z axis from 20 to 2,000 Hz with peak acceleration of 20 G (MIL 883E, Method 2007.2,A)
ESD-HBM	3 discharges at ±2kV pin to pin (ANSI/ESDA/JEDEC JS-001-2014)
ESD-HMM	10 discharges at ±8kV direct contact to lid when unit is grounded (ANSI/ESD SP56 -2009)
Reflow	5 reflow cycles with peak temperature of +260°C
Mechanical Shock	3 pulses of 10,000 G in each of the X, Y, and Z directions (IEC 68-2-27 Test Ea)

#### NOTES:

Microphones meet all acoustic and electrical specifications before and after reliability testing, except sensitivity which can deviate up to 3dB.





## ha SPECIFICATION REVISIONS

Revision	Specification Changes	Date
А	Initial Release (ECR 20-3550)	02/05/2020
В	Update photo and group delay curve	03/19/2020

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