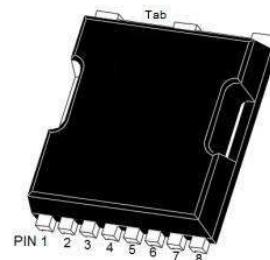


### ■ PRODUCT CHARACTERISTICS

VDSS	80V
R <sub>DS(on)</sub> Typ(V <sub>GS</sub> =10 V)	0.7mΩ
ID	500A

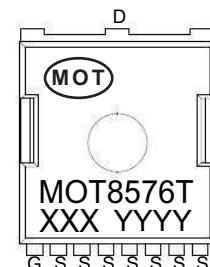

**TOLL-8L**

### ■ FEATURES

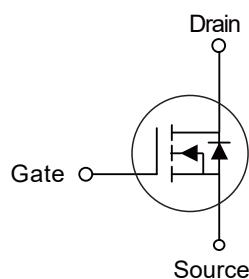
Surface-mounted package Advanced trench cell design Super trench

### ■ APPLICATIONS

High power system inverter  
Light electric vehicles  
BMS  
Drones

**Pin configuration (Top view)**


XXX = Lot Number  
YYYY = Year Week

**Marking**


### Order information

Device	Package	Shipping
MOT8576T/TR	TOLL-8L	2000/Tape&Reel

**■ ABSOLUTE MAXIMUM RATINGS (  $T_C = 25^\circ\text{C}$  unless otherwise noted )**

Parameter	Symbol	Conditions	Min	Max	Unit
Drain-Source Voltage	$V_{DS}$	$T_C = 25^\circ\text{C}$	80	-	V
Gate-Source Voltage	$V_{GS}$	$T_C = 25^\circ\text{C}$	-	$\pm 20$	V
Drain Current ( DC ) *	$I_D$	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	500	A
Drain Current ( Pulsed ) ***	$I_{DM}$	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	355	A
		$T_C = 100^\circ\text{C}, V_{GS} = 10\text{ V}$	-	1200	A
Drain power dissipation	$P_{tot}$	$T_C = 25^\circ\text{C}$	-	450	W
Storage Temperature	$T_{stg}$		-55	175	$^\circ\text{C}$
Junction Temperature	$T_J$		-	175	$^\circ\text{C}$
Continuous-Source Current	$I_S$	$T_C = 25^\circ\text{C}$	-	500	A
Single Pulsed Avalanche Energy	$E_{AS}$	$V_{DD} = 50\text{V}, L = 0.5\text{ mH}$	-	3042	mJ
Thermal Resistance- Junction to Ambient**	$R_{\theta JA}$		-	40	$^\circ\text{C}/\text{W}$
Thermal Resistance- Junction to Case**	$R_{\theta JC}$		-	0.33	

**■ ELECTRICAL CHARACTERISTICS (  $T_C=25^\circ\text{C}$ , unless otherwise specified )**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{ V}, I_{DS} = 250\text{ }\mu\text{A}$	80	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_{DS} = 250\text{ }\mu\text{A}$	2	-	4	V
Drain Leakage Current	$I_{DSS}$	$V_{DS} = 64\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	$\pm 100$	nA
On-State Resistance <sup>a</sup>	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{ V}, I_{DS} = 50\text{A}$	-	0.7	0.85	$\text{m}\Omega$
		$V_{GS} = 6\text{ V}, I_{DS} = 30\text{A}$	-	1.1	1.25	
<b>Diode Characteristics</b>						
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{SD} = 50\text{A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Reverse Recovery Time	$t_{rr}$	$I_{DS} = 50\text{A}, V_{GS} = 0\text{ V}$ $dI_{SD}/dt = 100\text{ A}/\mu\text{s}$	-	137	-	nS
Reverse Recovery Charge	$Q_{rr}$		-	369	-	nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 40\text{ V}$ Frequency = 1 MHz	-	8237	-	pF
Output Capacitance	$C_{oss}$		-	1549	-	
Reverse Transfer Capacitance <sup>b</sup>	$C_{rss}$		-	152	-	
Turn-on Delay Time	$t_d(\text{on})$	$V_{DS} = 40\text{ V}, V_{GEN} = 10\text{ V},$ $R_G = 4.5\text{ }\Omega, R_L = 1.3\text{ }\Omega,$ $I_{DS} = 30\text{A}$	-	32	-	nS
Turn-on Rise	$t_r$		-	115	-	
Turn-off Delay Time	$t_d(\text{off})$		-	93	-	
Turn-off Fall Time	$t_f$		-	140	-	
<b>Gate Charge Characteristics<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 40\text{ V}, V_{GS} = 10\text{ V},$ $I_{DS} = 30\text{A}$	-	138	-	nC
Gate-Source Charge	$Q_{gs}$		-	39	-	
Gate-Drain Charge	$Q_{gd}$		-	36	-	

Notes :

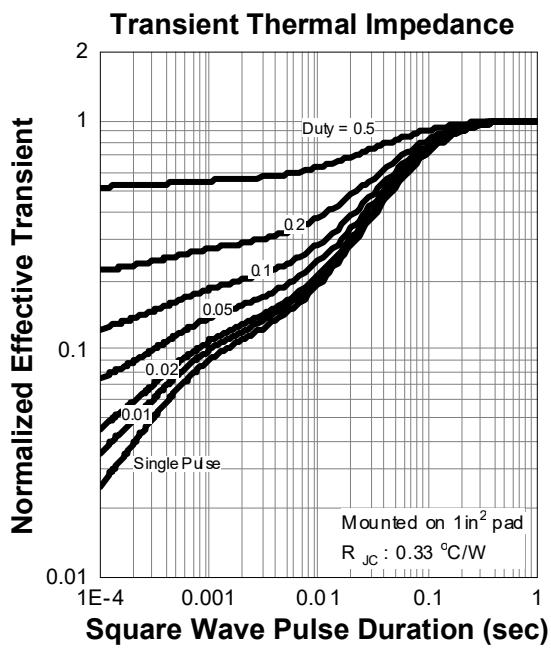
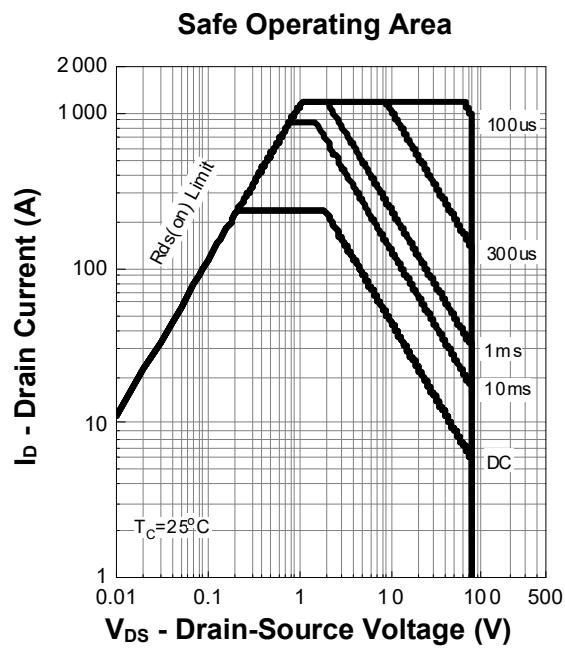
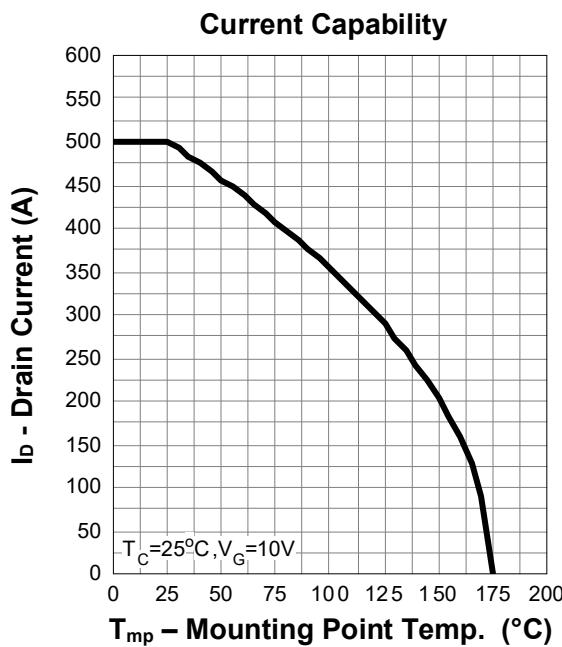
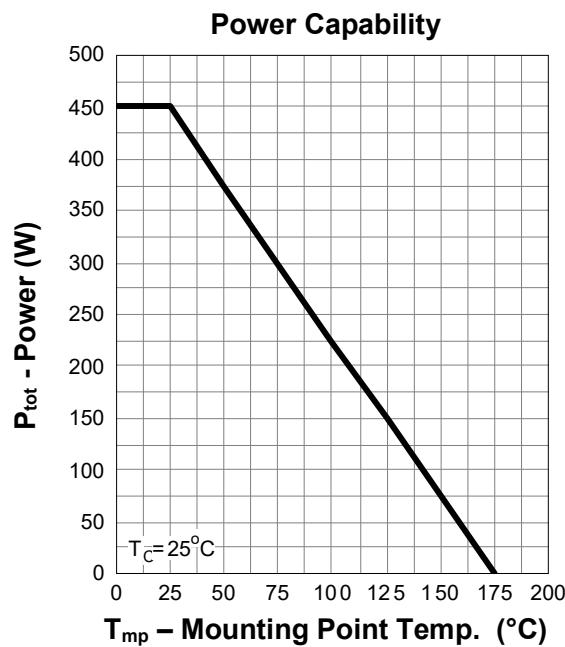
- \* Pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$
- \*\* Surface Mounted on minimum footprint pad area.

\*\*\* Limited by bonding wire

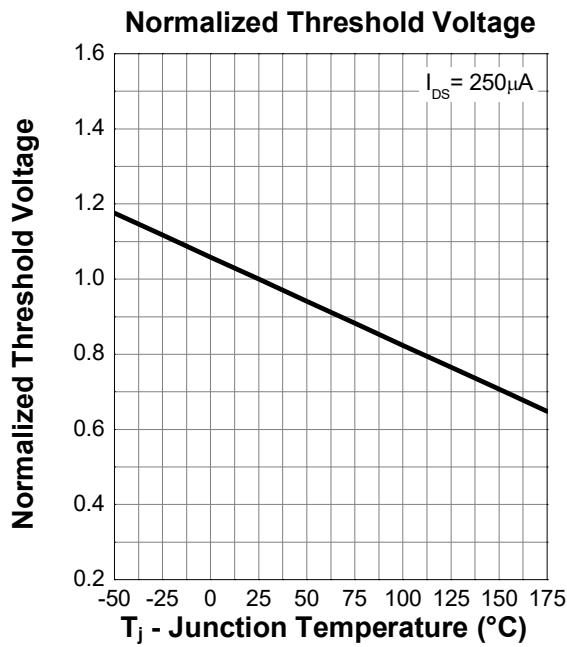
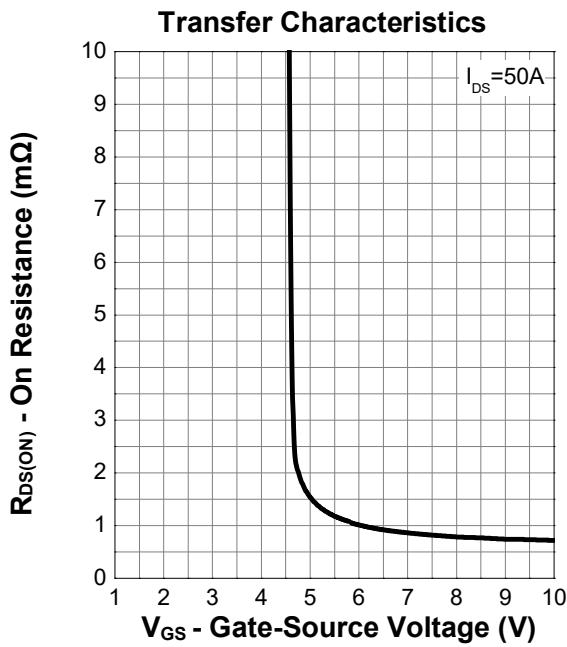
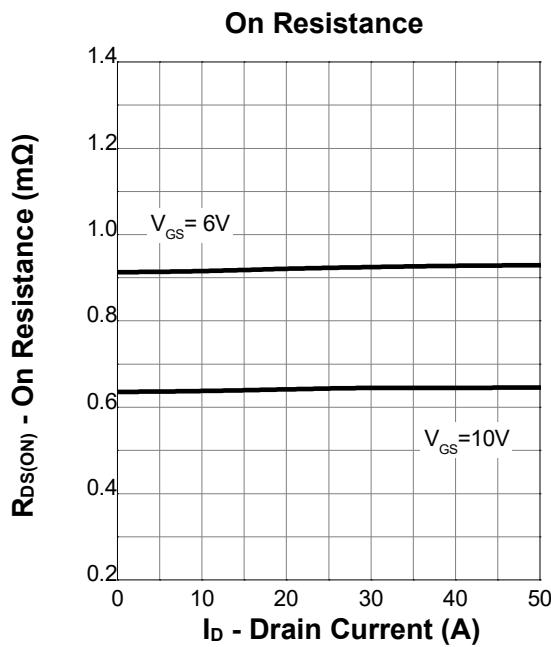
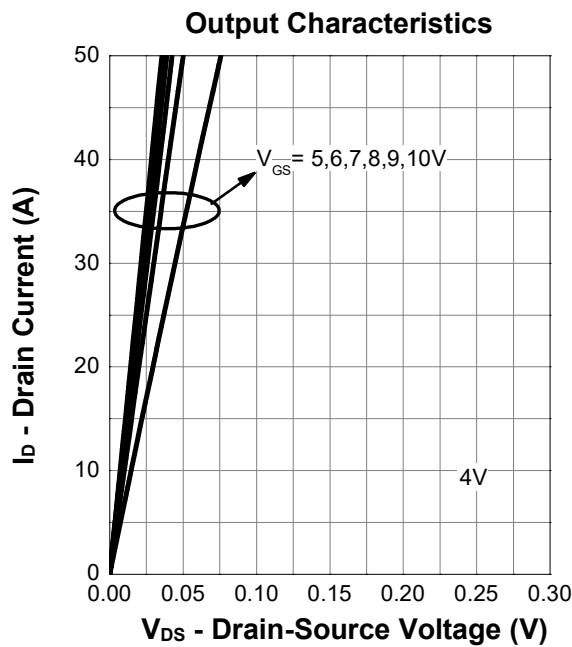
a : Pulse test ; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$

b : Guaranteed by design, not subject to production testing

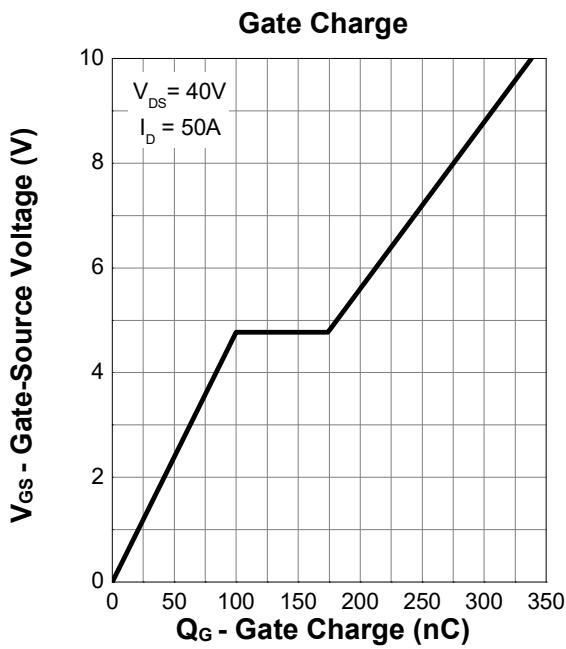
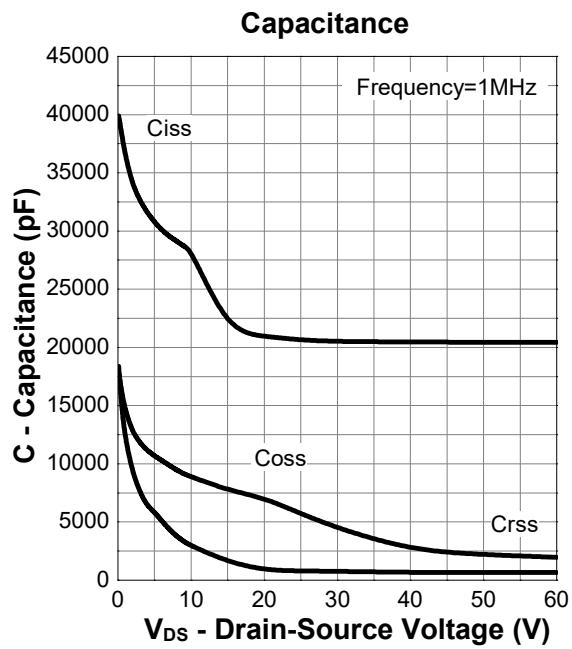
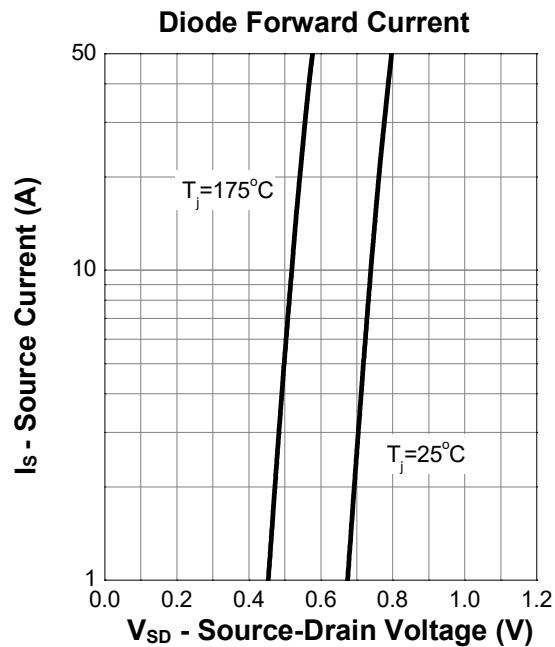
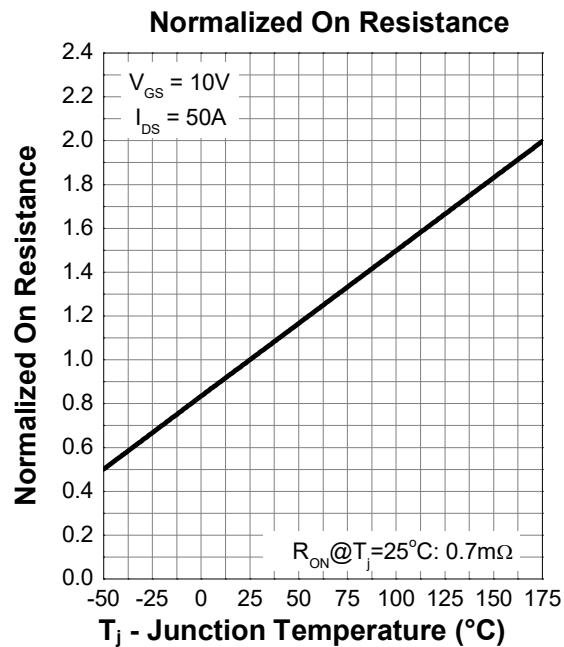
## ■ TYPICAL CHARACTERISTICS

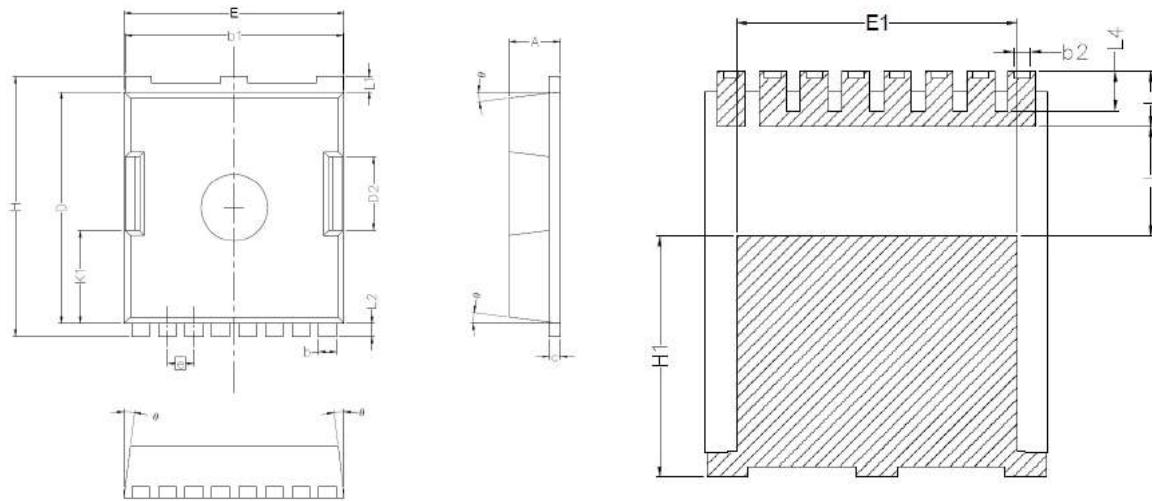


## ■ TYPICAL CHARACTERISTICS(Cont.)



## ■ TYPICAL CHARACTERISTICS(Cont.)



**■TOLL-8L PACKAGE OUTLINE DIMENSIONS**


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
theta	4°	10°

- The information contained hSurface-mounted package Advnced terch cell design Super trencherein is subject to change without notice.
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19th Floor, Shencheng Investment Center Building, Guiyuan Street, Luohu District, Shenzhen  
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Shenzhen Base:

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