

PRODUCT CHARACTERISTICS

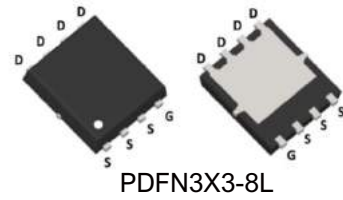
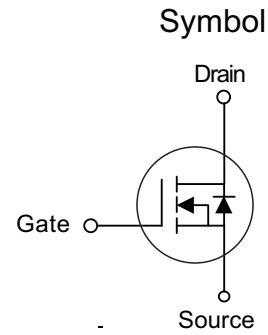
VDSS	30V
$R_{DS(on)typ}@V_{GS}=10V$	3.6mΩ
$R_{DS(on)typ}@V_{GS}=4.5V$	5.3mΩ
ID	85A

APPLICATIONS

- Portable Equipment and Battery Powered systems.
- Power Management in Notebook Computer

FEATURES

- Lower $R_{DS(ON)}$ to Minimize Conduction Losses
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested



ORDER INFORMATION

Order codes		Package	
Halogen-Free	Halogen		
N/A	MOT3145J	PDFN3X3	5000pieces/Reel

ABSOLUTE MAXIMUM RATINGS (T_J=25°C Unless Otherwise Noted)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DSS}	30	V
Gate-Source Voltage		V _{GSS}	±20	V
Maximum Junction Temperature		T _J	150	°C
Storage Temperature Range		T _{STG}	-55 to 150	°C
Pulse Drain Current Tested	T _C =25°C	I _{DM} ^①	85	A
Continuous Drain Current	T _C =25°C	I _D	76	A
	T _C =100°C	I _D	48	A
Maximum Power Dissipation	T _C =25°C	P _D	31	W
	T _C =100°C	P _D	12.5	W
Avalanche Current, Single pulse	L=0.1mH	I _{AS} ^②	28	A
Avalanche Energy, Single pulse	L=0.1mH	E _{AS} ^②	39	mJ

■ ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Static electrical characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250uA	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V	-	-	1	uA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =250uA	1.1	1.6	2.1	V
Gate Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Drain-Source On-state Resistance	R _{DS(ON)} ④	V _{GS} =10V, I _{DS} =20A	-	3.6	4.5	mΩ
		V _{GS} =4.5V, I _{DS} =18A	-	5.3	7.2	mΩ
Forward Transconductance	g _{fs}	V _{DS} =5V, I _{DS} =20A	-	22	-	S
Dynamic characteristics⑤						
Gate Resistance	R _G	V _{GS} =0V,V _{DS} =0V,Freq.=1MHz	-	2.2	-	Ω
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =15V, Freq.=1MHz	-	1859	-	pF
Output Capacitance	C _{OSS}		-	260	-	pF
Reverse Transfer Capacitance	C _{rSS}		-	212	-	pF
Turn-on Delay Time	td _(on)	V _{GS} =10V,V _{DS} =15V, I _D =1A,R _{GEN} =6Ω	-	9.6	-	nS
Turn-on Rise Time	tr		-	23.4	-	nS
Turn-off Delay Time	td _(off)		-	62.8	-	nS
Turn-off Fall Time	tf		-	23	-	nS
Total Gate Charge	Qg	V _{GS} =4.5V,V _{DS} =25V I _D =14A	-	26	-	nC
Total Gate Charge	Qg	V _{GS} =10V,V _{DS} =25V, I _D =14A	-	48	-	nC
Gate-Source Charge	Qgs		-	3.4	-	nC
Gate-Drain Charge	Qgd		-	14	-	nC
Source-drain characteristics						
Diode Forward Voltage	V _{SD} ④	I _{SD} =1A, V _{GS} =0V	-	0.75	1.1	V
Reverse Recovery Time	T _{rr}	I _F =2A, V _R =0V dI _F /dt=100A/μs	-	18.2	-	nS
Reverse Recovery Charge	Q _{rr}		-	9.2	-	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

■ TYPICAL CHARACTERISTICS

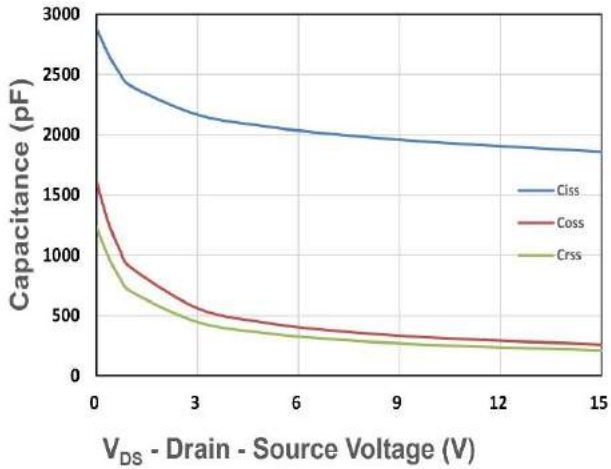


Figure 1. Capacitance

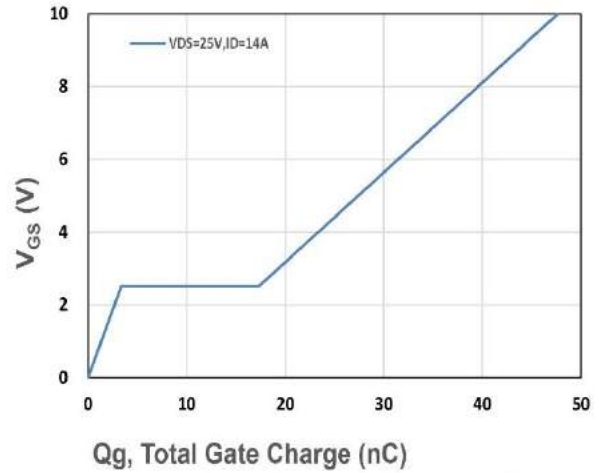


Figure 2. Gate Charge Characteristics

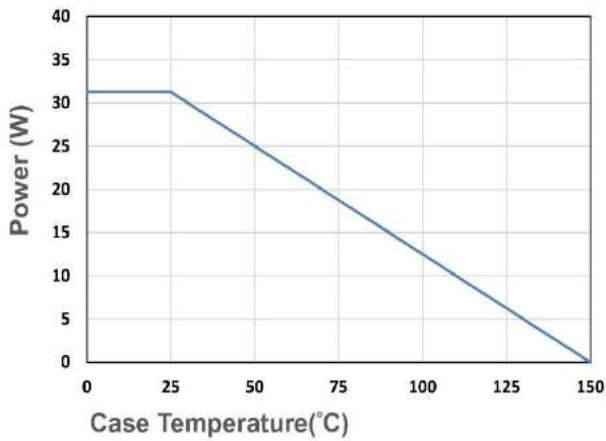


Figure 3. Power Dissipation

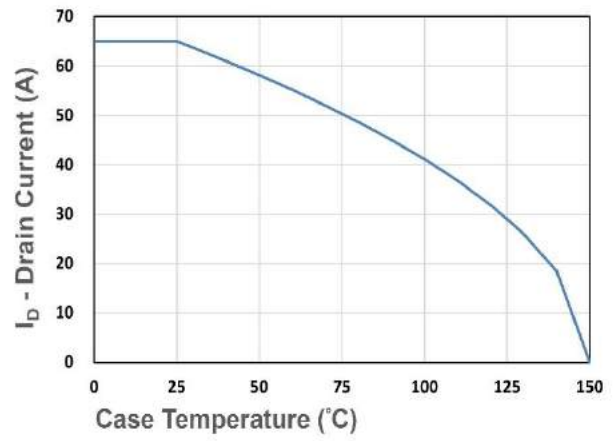


Figure 4. Drain Current

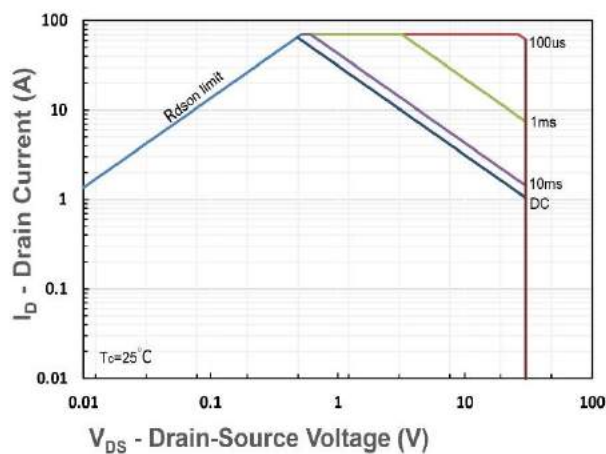


Figure 5. Safe Operating Area

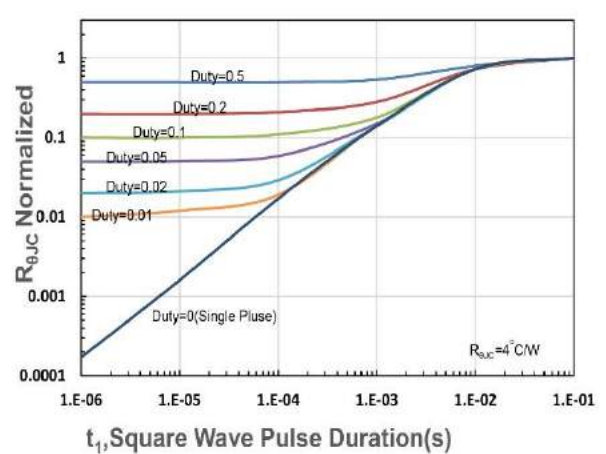
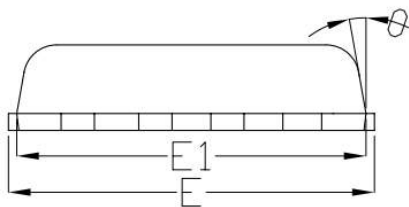
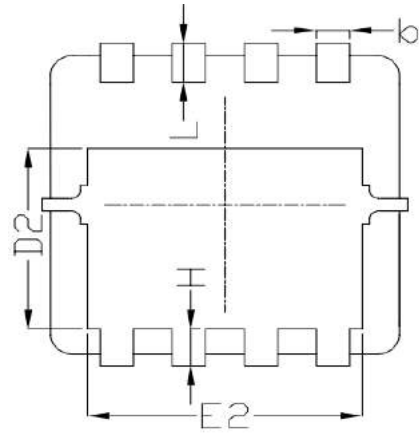
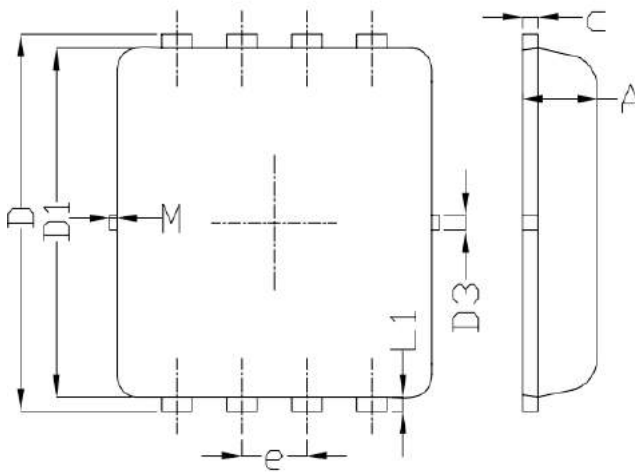
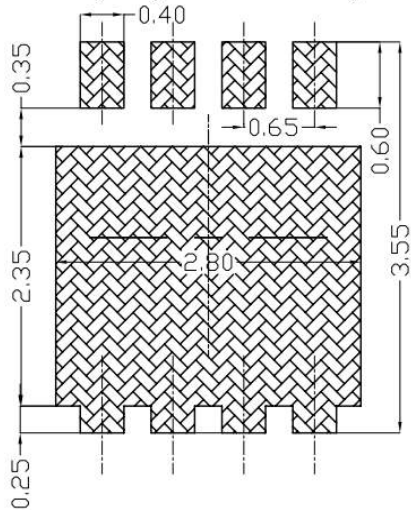


Figure 6. $R_{\theta JC}$ Transient Thermal Impedance

■ PDFN3X3-8L Package Mechanical Data



Land Pattern
(Only for Reference)



SYMBOL	DIMENSIONAL REQOMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
θ	---	10°	12°
M	*	*	0.15
* Not specified			