

General Description

The 120N12 uses advanced trench technology and design to provide excellent RDS(ON) . This device is suitable for PWM, load switching and general purpose applications.

Product Summary

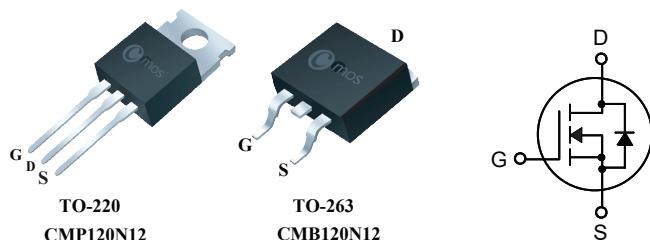
BVDSS	RDS(ON)	ID
120V	7mΩ	120A

Applications

- Synchronous Rectification
- Power Management in Inverter Systems
- Motor Driver

Features

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

TO-220/263 Pin Configuration**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	120	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _c =25°C	Continuous Drain Current	120	A
I _D @T _c =100°C	Continuous Drain Current	80	A
I _{DM}	Pulsed Drain Current	480	A
EAS	Single Pulse Avalanche Energy ¹	2722	mJ
P _D	Total Power Dissipation	170	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient(Steady State)	---	62.5	°C/W
R _{θJC}	Thermal Resistance Junction -Case(Steady State)	---	0.74	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	120	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=30\text{A}$	---	6	7	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=40\text{A}$	---	53	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.5	---	Ω
Q_g	Total Gate Charge	$I_{\text{D}}=25\text{A}$	---	167	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=60\text{V}$	---	37	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	51	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=60\text{V}$	---	46	---	ns
T_r	Rise Time	$R_{\text{G_ext}}=5\Omega$	---	55	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$V_{\text{GS}}=10\text{V}$	---	152	---	
T_f	Fall Time	$R_L=2.4\Omega$	---	61	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	7800	---	pF
C_{oss}	Output Capacitance		---	800	---	
C_{rss}	Reverse Transfer Capacitance		---	550	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	120	A
I_{SM}	Pulsed Source Current		---	---	480	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=30\text{A}$, $T_J=25^\circ\text{C}$	---	0.77	1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=5\text{mH}$, $I_{\text{AS}}=33\text{A}$.

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Cmos reserves the right to improve product design ,functions and reliability without notice.

CMP120N12/CMB120N12



Typical Characteristics

120V N-Channel MOSFET

