

## N-Channel Enhancement Mode Field Effect Transistor

## General Description

The 2810 uses advanced trench technology and design to provide excellent RDS(ON). This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

## Features

- N-channel-Enhancement mode
- Lower On-resistance
- 100% Avalanche Tested
- RoHS Compliant

## Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ\text{C}$	Continuous Drain Current	45	A
$I_D @ T_c = 100^\circ\text{C}$	Continuous Drain Current	35	A
$I_{DM}$	Pulsed Drain Current	180	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	264	mJ
$P_D @ T_c = 25^\circ\text{C}$	Total Power Dissipation	100	W
$T_{STG}$	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range	-55 to 175	°C

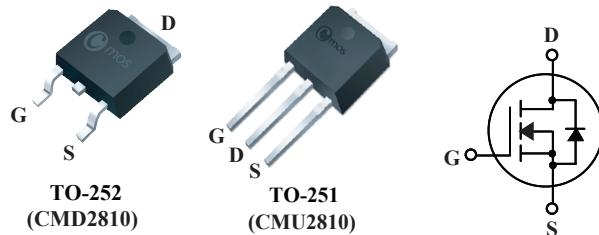
## Product Summary

BVDSS	RDS(on)	ID
80V	8.5mΩ	45A

## Applications

- DC-DC converters
- Power switching application
- Ideal for high-frequency switching and synchronous rectification

## TO-252/251 Pin Configuration



## Thermal Data

Symbol	Parameter	Value	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	1.5	°C/W

## N-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	80	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	6.2	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=6\text{V}$ , $I_D=20\text{A}$	---	10	13.5	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D =250\mu\text{A}$	2	---	3.5	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=20\text{A}$	---	25	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1	---	$\Omega$
$Q_g$	Total Gate Charge	$I_D=20\text{A}$	---	27	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=40\text{V}$	---	8.5	---	
$Q_{\text{gd}}$	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	5	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=40\text{V}$	---	12	---	$\text{ns}$
$T_r$	Rise Time	$R_{\text{GEN}}=3\Omega$	---	8.5	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$V_{\text{GS}}=10\text{V}$	---	22	---	
$T_f$	Fall Time	$R_L=2\Omega$	---	5.5	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1500	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	600	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	60	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	45	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	180	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=20\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=40\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=1\text{mH}$ , $I_{\text{AS}}=23\text{A}$ .

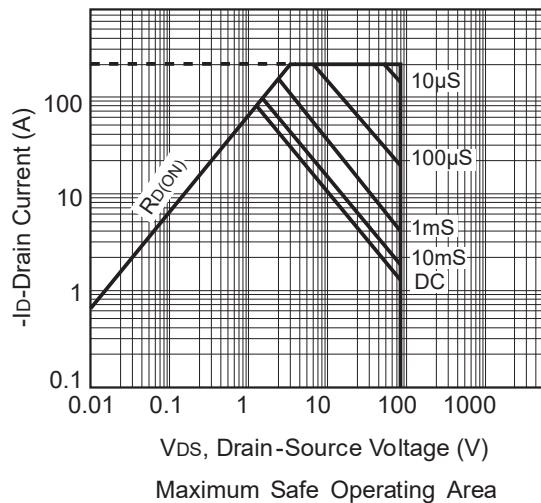
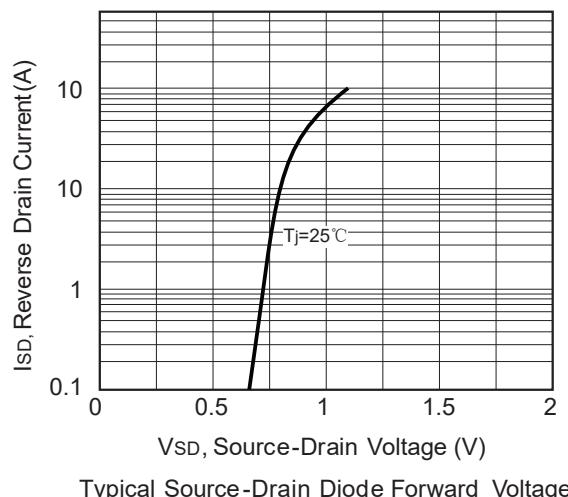
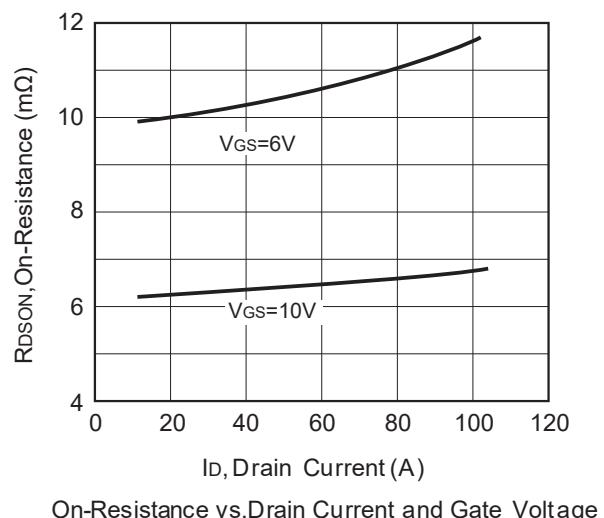
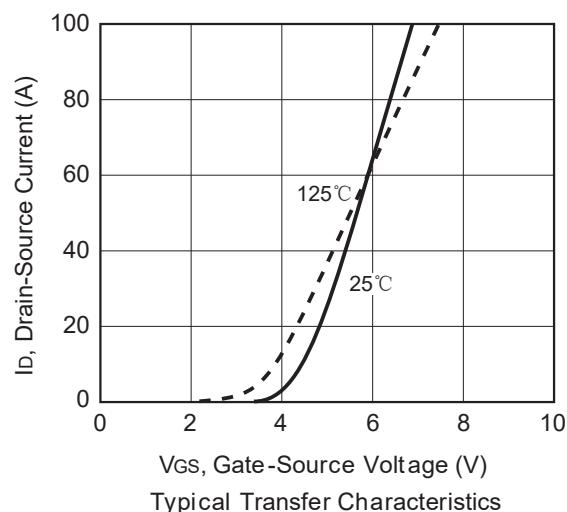
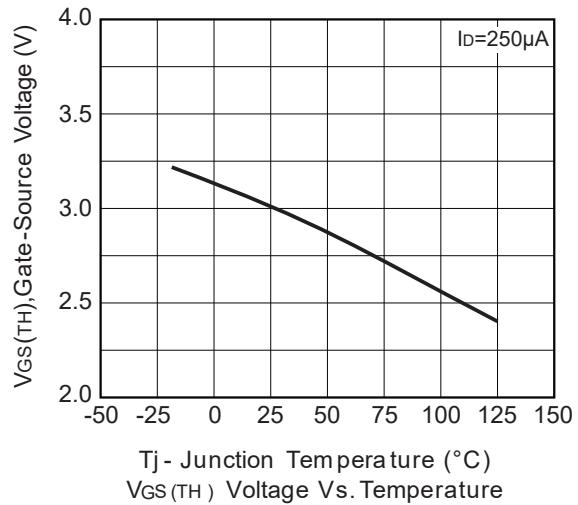
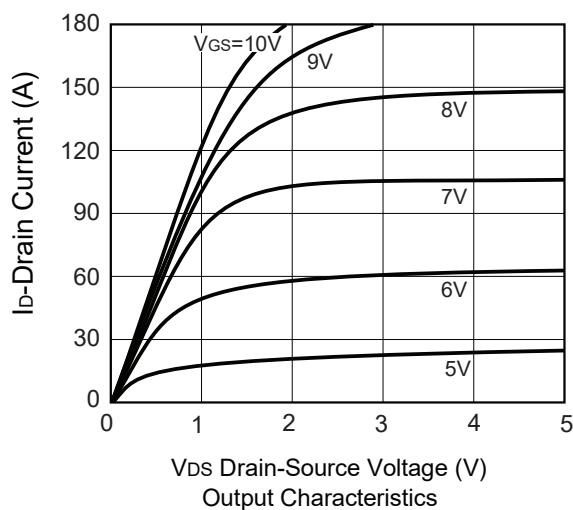
This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

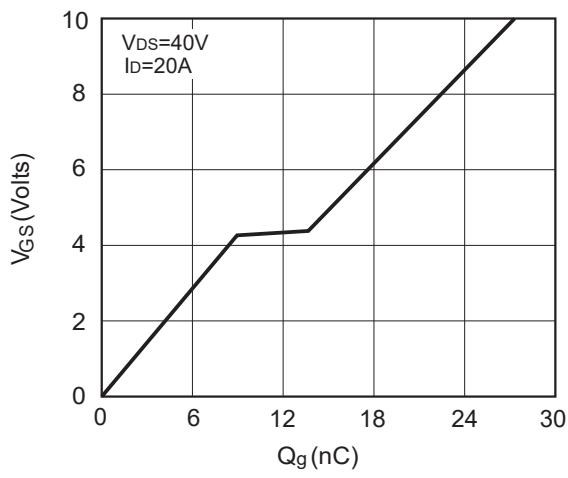
## N-Channel Enhancement Mode Field Effect Transistor

## Typical Characteristics

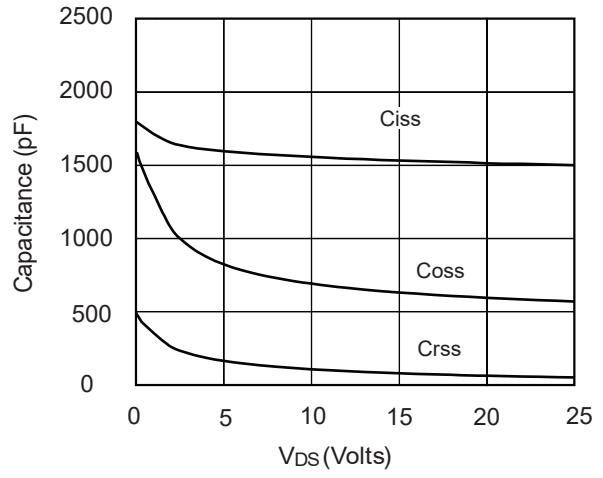


## N-Channel Enhancement Mode Field Effect Transistor

## Typical Characteristics



Gate-Charge Waveforms



Capacitance Characteristics

