

General Description

The 35N06B combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

Features

- N-channel-Enhancement mode
- Lower On-resistance
- 100% Avalanche Tested
- RoHS Compliant

Product Summary

BVDSS	RDSON	ID
60V	36mΩ	30A

Applications

- DC-DC & DC-AC Converters
- DC Motor Drivers
- LED Lighting and LED Backlight Drivers

TO-252/251 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	30	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	20	A
I_{DM}	Pulsed Drain Current	120	A
EAS	Single Pulse Avalanche Energy ¹	24	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	40	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction -Case ¹	---	3.1	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=10\text{A}$	---	32	36	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=5\text{A}$	---	37	42	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	10	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=10\text{A}$	---	13	---	S
R_g	Gate Resistance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.4	---	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	15	---	nC
Q_{gs}	Gate-Source Charge		---	2.2	---	
Q_{gd}	Gate-Drain Charge		---	4.3	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=48\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=2.5\Omega$ $I_D=20\text{A}$	---	7	---	ns
T_r	Rise Time		---	13	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	18	---	
T_f	Fall Time		---	2.5	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	670	---	pF
C_{oss}	Output Capacitance		---	40	---	
C_{rss}	Reverse Transfer Capacitance		---	30	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	30	A
I_{SM}	Pulsed Source Current		---	---	120	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=10\text{A}$, $T_J=25^\circ\text{C}$	---	0.88	1.3	V

Note :

1.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=7\text{A}$.

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Cmos reserves the right to improve product design ,functions and reliability without notice.

Typical Characteristics

