

## FEATURES

- Supply Voltage: 4.75V~40V
- Output Range: 1.8V,3.3V,3.6V,5.0V~10V
- Output Accuracy: <+/-2%
- Output Current: 100mA (Up to 500mA Typ.)
- PSRR : 45dB @ 100Hz
- Dropout Voltage : 300mV @  $I_{OUT}=100mA$
- Quiescent Current : 6 $\mu A$ @ $V_{IN}=7V$ (Typ.)
- Recommend Capacitor : 10uF

## DESCRIPTION

The BL9153 series is a high accuracy, high input voltage low quiescent current, high speed, and low dropout Liner regulator with high ripple rejection. The device is manufactured with Bi-CMOS process. The BL9153 offers over-current limit and over temperature protection to ensure the device working in well conditions. The BL9153 regulators are available in standard SOT-89-3L packages. Standard products are Pb-free and Halogen-free.

## APPLICATIONS

- Smart Meter
- Instrumentation
- Home appliances
- Industrial control
- .....

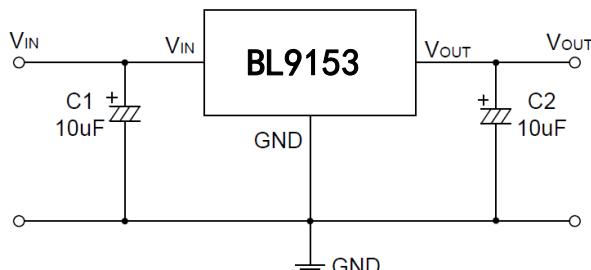
## ORDERING INFORMATION

Part No.	BL9153-xxCC3TR
Package Type	SOT-89-3L
Tape & Reel	1000pcs/Reel

xx: Output voltage, e.g.

18=1.8V,33=3.3V,36=3.6V,50=5.0V, etc

## TYPICAL APPLICATION

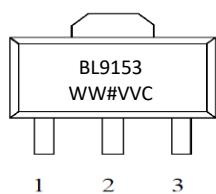


**ABSOLUTE MAXIMUM RATING (Ta=25°C)**

Parameter Name	Rating	Unit
Power Dissipation	Internal limited	mW
V <sub>IN</sub> Range	-0.3~45	V
V <sub>OUT</sub> Range	-0.3~12	V
Lead Temperature Range	260	°C
Storage Temperature Range	-55~150	°C
Operating Junction Temperature Range	125	°C
ESD MM	400	V
ESD HBM	4K	V

**RECOMMENDED OPERATING CONDITIONS (Ta=25°C)**

Parameter Name	Rating	Unit
Operating Supply voltage	4.75~40	V
Operating Temperature Range	-40~85	°C
Thermal Resistance(On PCB), R <sub>θJA</sub>	43.5	°C/W
Power Dissipation	1000	mW

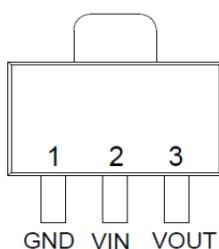
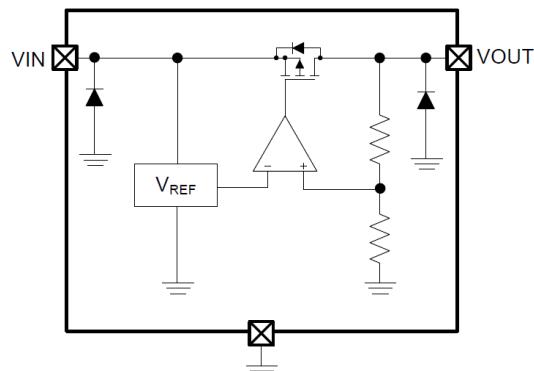
**BODYING MARKING (SOT89-3L/TOP VIEW)**


"#": represents a space;

"WW": represents the packaging week;

"VV": represents voltage code,

VV	33	36	50	...
Voltage	3.3V	3.6V	5.0V	...

**PIN CONFIGURATION**

**FUNCTIONAL BLOCK DIAGRAM**

**PIN DESCRIPTION**

Pin Number	Pin Name	Function Description
1	GND	Ground
2	V <sub>IN</sub>	Voltage Input
3	V <sub>OUT</sub>	Voltage Output

**ELECTRICAL CHARACTERISTICS** (Ta=25°C, VIN=12V, CIN=COUT=10uF, unless otherwise noted)

Parameter Name	Symbol	Test Conditions		Min	Typ	Max	Unit	
Input Range	V <sub>IN</sub>	I <sub>OUT</sub> =10mA		4.75		40	V	
Output Voltage	V <sub>OUT</sub>	V <sub>IN</sub> =12V, I <sub>OUT</sub> =10mA		3.234	3.3	3.366	V	
				3.528	3.6	3.672		
				4.9	5.0	5.1		
Maximum Output Current	I <sub>OUT_PK</sub>	V <sub>IN</sub> =12V, R <sub>L</sub> =1Ω			500		mA	
Quiescent Current	I <sub>Q</sub>	V <sub>IN</sub> =3.6V, No load			5.8	7.8	uA	
		V <sub>IN</sub> =5.5V, No load			5.9	7.9		
		V <sub>IN</sub> =7V, No load			6	8		
		V <sub>IN</sub> =24V, No load			7.5	10		
		V <sub>IN</sub> =40V, No load			10	15		
Dropout Voltage	V <sub>DROP</sub>	I <sub>OUT</sub> =1mA			2	12	mV	
		I <sub>OUT</sub> =100mA			300	400		
Line Regulation	LNR	V <sub>I</sub> =7~24V, V <sub>OUT</sub> =5V, I <sub>OUT</sub> =1mA			0.02		%/V	
		V <sub>IN</sub> =7~45V, V <sub>OUT</sub> =5V, I <sub>OUT</sub> =1mA			0.1			
Load Regulation	LDR	V <sub>IN</sub> =12V, I <sub>OUT</sub> =1~100mA			0.6		%	
Output Noise	e <sub>NO</sub>	I <sub>OUT</sub> =10mA		-100		100	μV	
Ripple Rejection	PSRR	V <sub>IN</sub> =10V V <sub>PP</sub> =0.5V I <sub>OUT</sub> =1mA	f=100Hz		50		dB	
			f=1KHz		40			
			f=10KHz		30			
Thermal Protection	T <sub>SD</sub>	V <sub>IN</sub> =12V, I <sub>OUT</sub> =1mA			155		°C	
Thermal Protection Hys	T <sub>SD_HYS</sub>	V <sub>IN</sub> =12V, I <sub>OUT</sub> =1mA			30		°C	
Temperature Cofficient	ΔVo/ΔT	V <sub>IN</sub> =12V, I <sub>OUT</sub> =1mA			±0.4		mV/°C	

**OUTLINE DIMENSIONS**

SOT-89-3L		Unit:mm			
Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
$\Delta$	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.197	
b1	0.400	0.580	0.016	0.023	
c	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550 REF		0.061 REF		
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
e	1.500 TYP		0.060TYP		
e1	3.000 TYP		0.118TYP		
L	0.900	1.200	0.035	0.047	

## Storage conditions and packaging

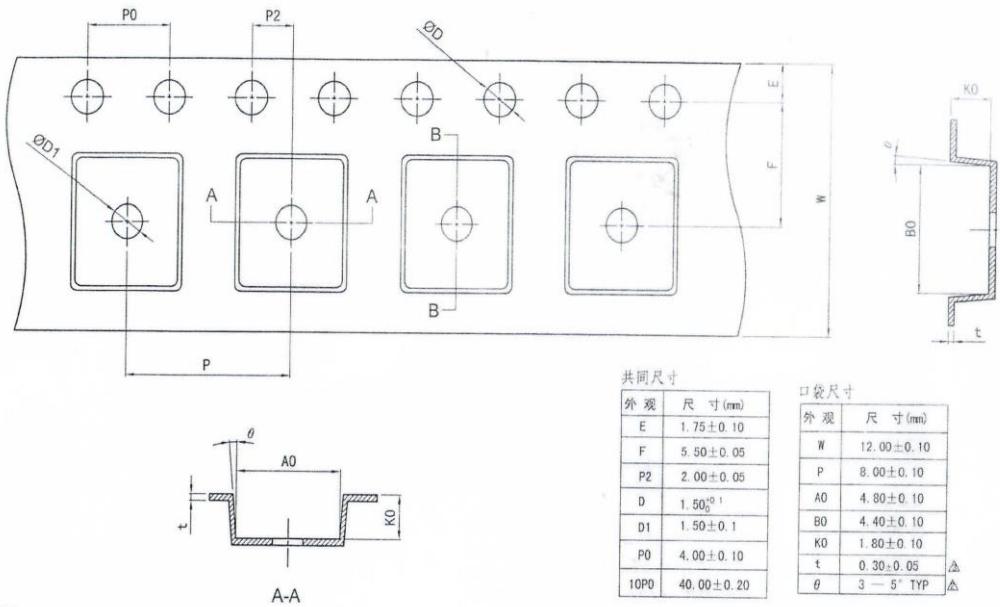
**Humidity sensitivity level: MSL 3**

**Warranty period: two years**

**Packing method: Tape**

**Minimum packaging: 1000**

## Tray information



**技术要求:**

- 任意10个传输孔间距的累积误差≤0.2mm;
- 载带沿长度方向的侧弯≤1mm/100mm;
- 从口袋底部上方为0.3mm处测定A0及B0;
- K0是从口袋的内部底面到载带的顶部表面测量的尺寸;
- 表面电阻率:  $10^5\sim10^{10}\Omega/\square$ ;
- 粗糙度:  $R_a<0.8\mu m$ ;
- 颜色: 黑色 (参考色号: CO MO Y35 K100)。

拟 制	王永忠 2005.05.23	测 试 部	天水华天 12-5-10
审 核	张雅迪 2005.5.10	制 造 部	
复 审	刘立波 2005.5.10	采 购 部	
设 备 部		销 售 部	
质 量 部	张加富 2005.5.10	标 准 化	张雅迪 12-5-10
制 图		批 准	张加富 12-5-10
参 考 图 号		幅 面:	A4
		比 例:	1 : 1

△ 名称由SOT-8%改为SOT89-3L	张雅迪 20120510
△ 检查技术要求第6条10°改为10°	张雅迪 2009.12.08
△ D 2.4±0.05改为0.30±0.05	张雅迪 2006.07.25
△ 测量方法标注	张雅迪 2006.07.25
△ PT CZN0891798改为PTCZG402HTTY01	王永忠 2005.05.23
更改标记	更改内容
	签名 日期

天水华天科技股份有限公司  
Tian Shui Hua Tian Technology Co., Ltd

图号: PT CZN0891798 △  
载带  
单位: 版次: 页数:  
mm 4版 1共 1张