FREE

Hyperfast Rectifier, 30 A FRED Pt<sup>®</sup> G5



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### LINKS TO ADDITIONAL RESOURCES

3		
<u>3D M</u>	odels	

PRIMARY CHARACTERISTICS									
I <sub>F(AV)</sub> 30 A									
V <sub>R</sub>	1200 V								
V <sub>F</sub> at I <sub>F</sub> at 125 °C	1.7 V								
t <sub>rr</sub>	32 ns								
T <sub>J</sub> max.	175 °C								
Package	D <sup>2</sup> PAK 2L (TO-263AB 2L)								
Circuit configuration	Single								

### **FEATURES**

- Hyperfast and optimized Q<sub>rr</sub>
- Best in class forward voltage drop and switching RoHS losses trade off COMPLIANT HALOGEN
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Polyimide passivation
- Designed and qualified according JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant.

Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Repetitive peak reverse voltage	V <sub>RRM</sub>		1200	V						
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 96 °C, D = 0.50	30							
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_C = 96 \text{ °C}, t_p = 10 \text{ ms}, \text{ sine wave}$	240	A						
Repetitive peak forward current	I <sub>FRM</sub>	T <sub>C</sub> = 45 °C, D = 0.50, f = 20 kHz	60							
Operating junction and storage temperature	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C						

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER         SYMBOL         TEST CONDITIONS         MIN.         TYP.         MAX.									
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	1200	-	-				
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	1.9	2.5	V			
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 125 °C	-	1.7	-				
Deverse leakers eurrent	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	50				
Reverse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA			
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	17	-	pF			
Series inductance	L <sub>S</sub>	Measured to lead 5 mm from package body	-	8	-	nH			

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# Vishay Semiconductors

DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TE	ST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}I_F$	: = 100 A/μs, V <sub>R</sub> = 30 V	-	32	-				
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	113	-	ns			
		T <sub>J</sub> = 125 °C		-	175	-				
Peak recovery current	I	$T_J = 25 \ ^\circ C$	I <sub>F</sub> = 20 A dI <sub>F</sub> /dt = 600 A/μs V <sub>R</sub> = 400 V	-	17	-	А			
	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	24	-				
Powerse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	850	-	nC			
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	2150	-				
Powerse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	85	-	ns			
Reverse recovery time		T <sub>J</sub> = 125 °C		-	132	-				
Deels receiver a surrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_{\rm F} = 30  {\rm A}$	-	30	-	٨			
Peak recovery current		T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 1000 A/µs V <sub>B</sub> = 800 V	-	43	-	A			
D	0	T <sub>J</sub> = 25 °C	] ``	-	1350	-	nC			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	3215	-				

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	-	1.1	°C/W			
Weight			-	2.0	-	g			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C			
Marking device		Case style D <sup>2</sup> PAK 2L (TO-263AB 2L)	E5TH3012S						



#### 100 I<sub>F</sub> - Instantaneous Forward Current (A) T<sub>J</sub> = 175 °C 10 T<sub>J</sub> = 125 °C 1 T<sub>J</sub> = 25 °C T<sub>.1</sub> = -40 °C 0.1 0 0.5 1.0 1.5 2.0 2.5 3.0 3.5 V<sub>F</sub> - Forward Voltage Drop (V)

Fig. 1 - Typical Forward Voltage Drop Characteristics

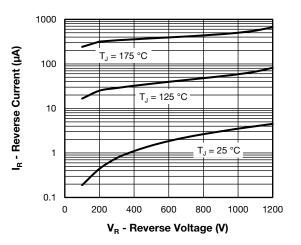


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

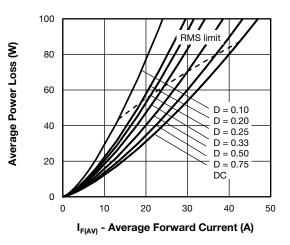


Fig. 5 - Forward Power Loss Characteristics

# VS-E5TH3012S2L-M3

## **Vishay Semiconductors**

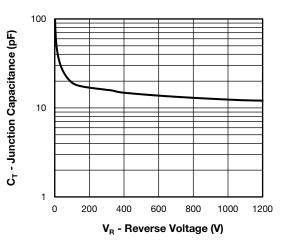


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

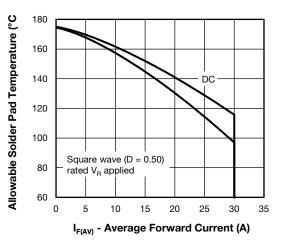


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

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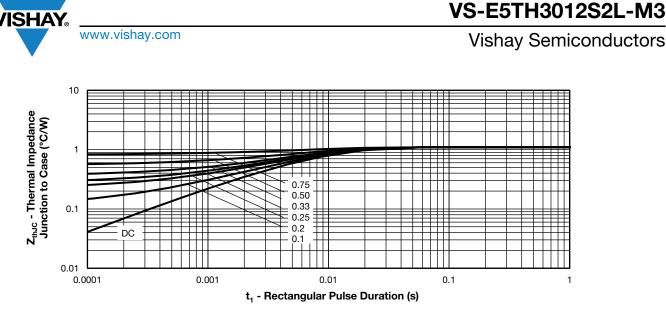


Fig. 6 - Thermal Impedance ZthJC Characteristics

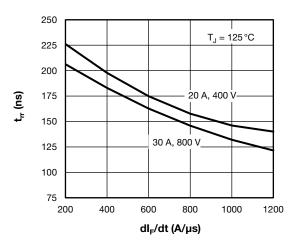


Fig. 7 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

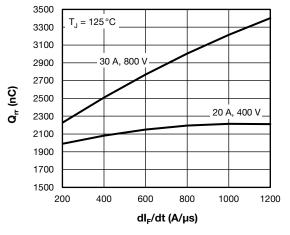


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

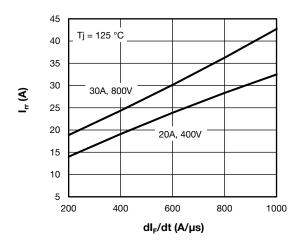


Fig. 9 - Typical Recovery Current vs. dI<sub>F</sub>/dt





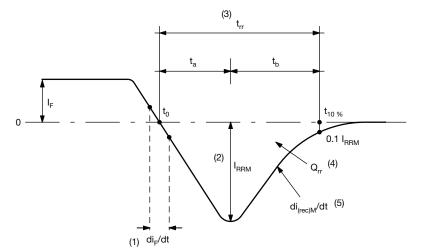


Fig. 10 - Reverse Recovery Waveform and Definitions

#### Notes

- $^{(1)}$  di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current (3)  $t_{rr}$  reverse recovery time measured from  $t_0$ , crossing point of negative going  $I_F$ , to point  $t_{10\%}$ , 0.1  $I_{RRM}$ (4)  $Q_{rr}$  area under curve defined by  $t_0$  and  $t_{10\%}$

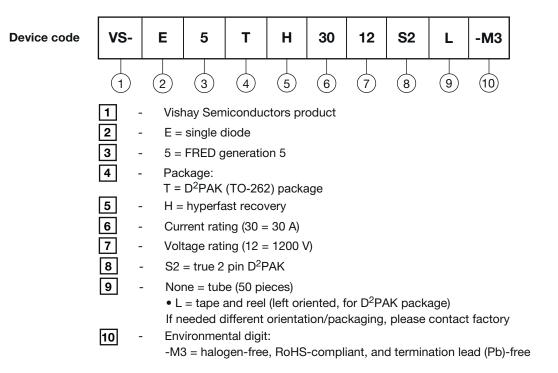
$$Q_{rr} = \int_{t_0}^{t_{10\%}} I(t) dt$$

 $^{(5)}$  di<sub>(rec)</sub>M/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>



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### **ORDERING INFORMATION TABLE**



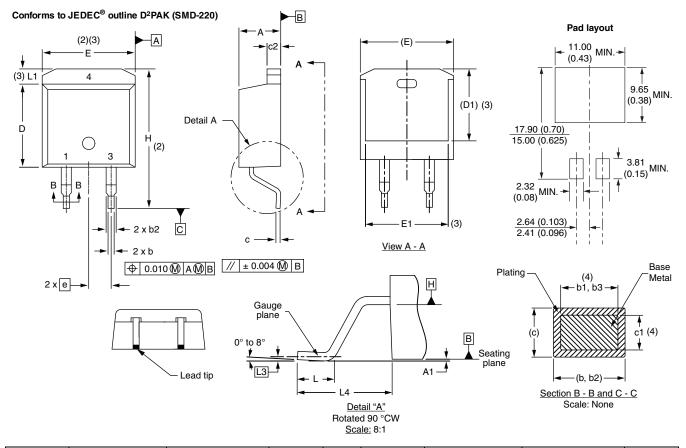
ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-E5TH3012S2L-M3	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96683					
Part marking information	www.vishay.com/doc?96693					
Packaging information	www.vishay.com/doc?95032					
SPICE model	www.vishay.com/doc?96926					



D<sup>2</sup>PAK 2L (TO-263AB 2L)

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	NOTES	NOTES SYMBOL	MILLIMETERS		INCHES		NOTES	
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L3	0.25	BSC	0.010	BSC	
c2	1.14	1.65	0.045	0.065			L4	4.78	5.28	0.188	0.208	
D	8.51	9.65	0.335	0.380	2							

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
 (3) Thermal and contain antional within dimension E 1.1, D1 and E1.

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

(7) Outline conforms to JEDEC® outline TO-263AB

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