

## Features

- Split Gate Trench MOSFET Technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low  $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device <sup>(Note 1)</sup>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## N-CHANNEL MOSFET

## Maximum Ratings

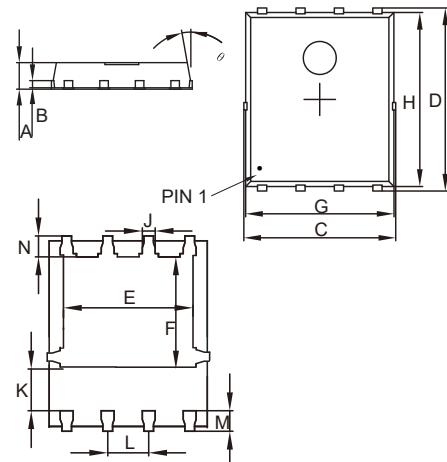
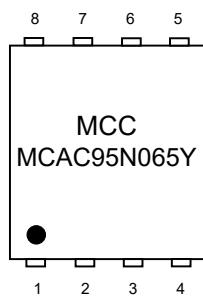
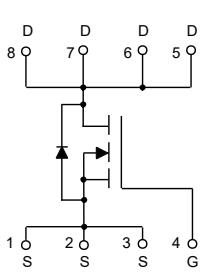
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient<sup>(Note 2)</sup>
- Thermal Resistance: 1.04°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	65	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_C=25^\circ\text{C}$	$I_D$	95	A
$T_C=100^\circ\text{C}$	$I_D$	60	
Pulsed Drain Current (Note 3)	$I_{DM}$	380	A
Total Power Dissipation (Note 4)	$P_D$	120	W
Avalanche Energy (Note 5)	$E_{AS}$	800	mJ

### Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. copper, in a still air environment with  $T_A=25^\circ\text{C}$ .
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
5.  $V_{DD}=50\text{V}$ ,  $V_G=10\text{V}$ ,  $L=4\text{mH}$ .

## Internal Structure and Marking Code



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.031	0.047	0.80	1.20	
B	0.010		0.254		TYP.
C	0.193	0.222	4.90	5.64	
D	0.232	0.250	5.90	6.35	
E	0.148	0.167	3.75	4.25	
F	0.126	0.154	3.20	3.92	
G	0.189	0.213	4.80	5.40	
H	0.222	0.239	5.65	6.06	
K	0.045	0.059	1.15	1.50	
J	0.012	0.020	0.30	0.50	
L	0.046	0.054	1.17	1.37	
M	0.012	0.028	0.30	0.71	
N	0.016	0.028	0.40	0.71	

**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	65			V
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =65V, V <sub>GS</sub> =0V			1	μA
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.8	2.2	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		2.1	2.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A		2.7	3.4	
Gate Resistance	R <sub>g</sub>	f=1MHz, Open drain		2		Ω
<b>Diode Characteristics</b>						
Continuous Body Diode Current	I <sub>S</sub>				95	A
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =25A,di/dt=100A/μs		57		ns
Reverse Recovery Charge	Q <sub>rr</sub>			64		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V,f=100KHz		5185		pF
Output Capacitance	C <sub>oss</sub>			1290		
Reverse Transfer Capacitance	C <sub>rss</sub>			42		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V,I <sub>D</sub> =50A		81		nC
Gate-Source Charge	Q <sub>gs</sub>			14		
Gate-Drain Charge	Q <sub>gd</sub>			13		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> =10V,V <sub>DD</sub> =30V, I <sub>D</sub> =25A, R <sub>GEN</sub> =2Ω		13.5		ns
Turn-On Rise Time	t <sub>r</sub>			13		
Turn-Off Delay Time	t <sub>d(off)</sub>			57		
Turn-Off Fall Time	t <sub>f</sub>			20		

## Curve Characteristics

Fig. 1 - Typical Output Characteristics

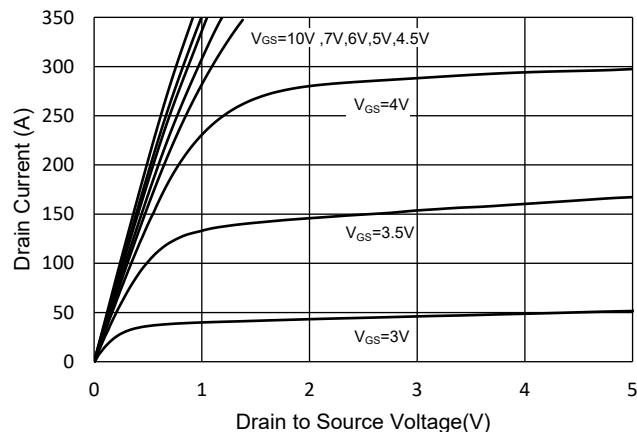


Fig.2 Transfer Characteristic

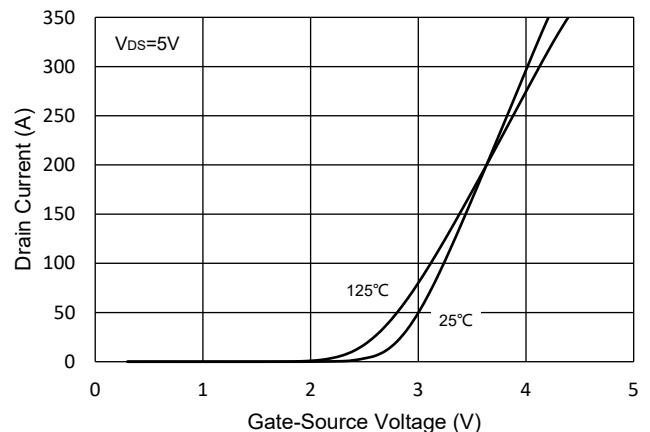


Fig.3 R<sub>dson</sub>-V<sub>gs</sub>

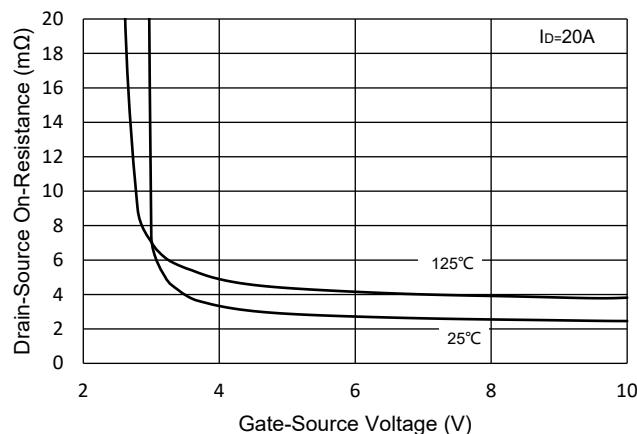


Fig.4 R<sub>d(on)</sub>-I<sub>d</sub>

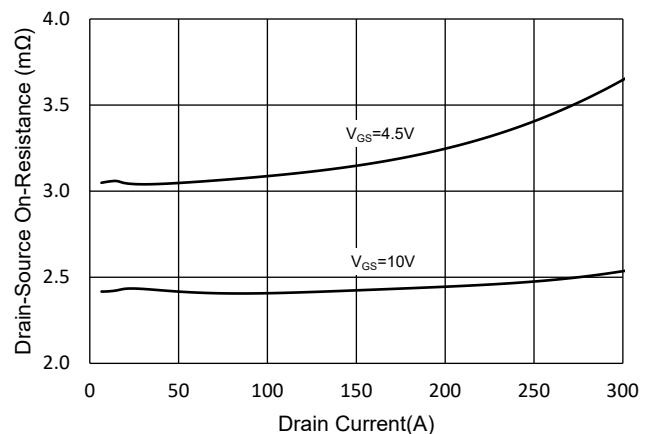


Fig.5 Capacitance Characteristics

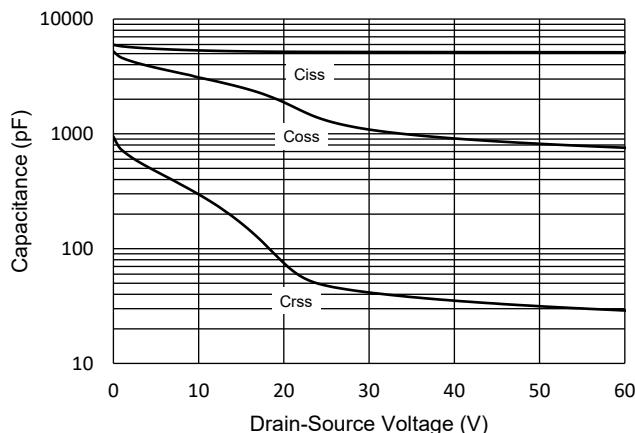
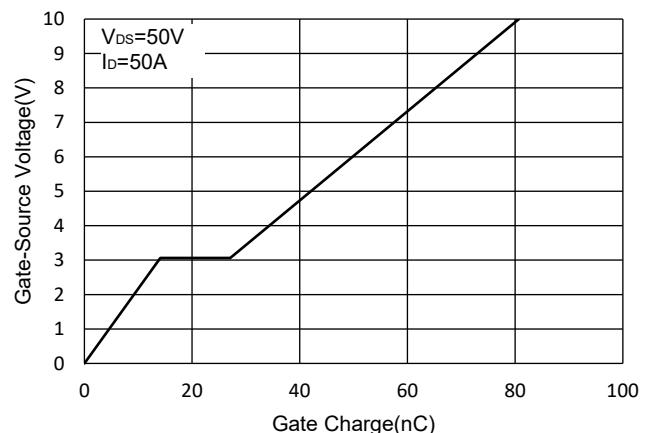
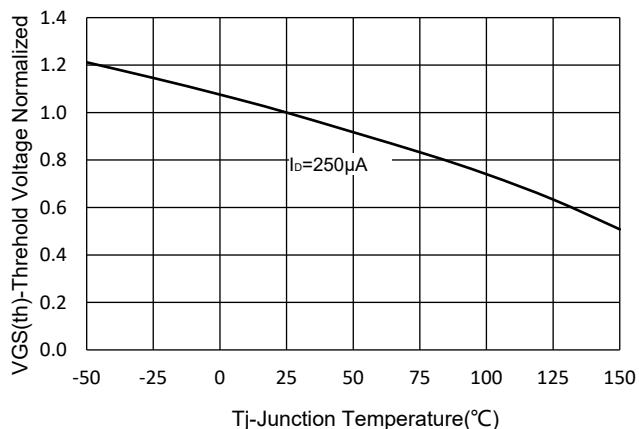


Fig.6 Gate Charge

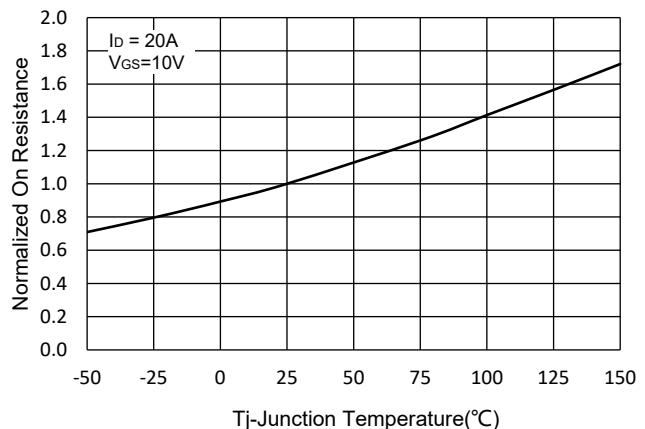


## Curve Characteristics

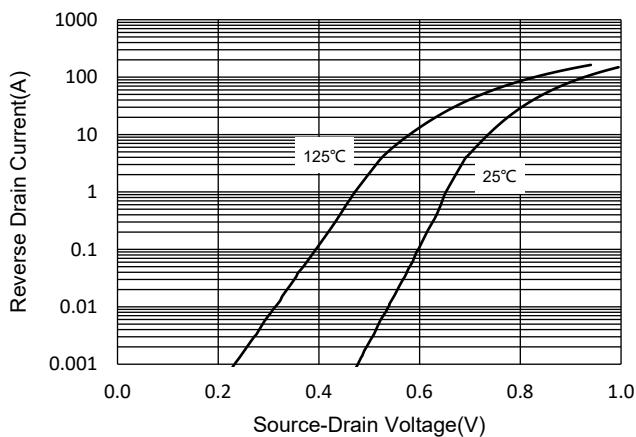
**Fig.7 Normalized Threshold Voltage**



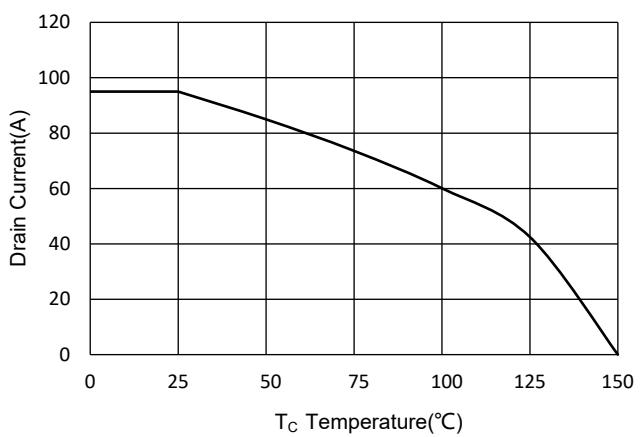
**Fig.8 Normalized On Resistance Characteristics**



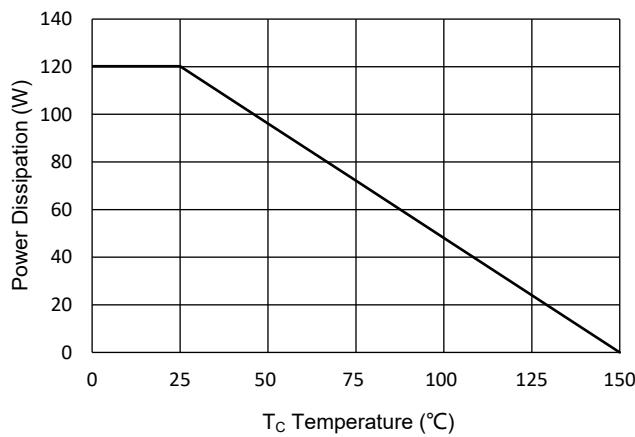
**Fig.9 IS-VSD**



**Fig.10 Drain Current vs. Case Temperature**



**Fig.11 Power Dissipation**



## Curve Characteristics

Fig.12 Safe Operation Area

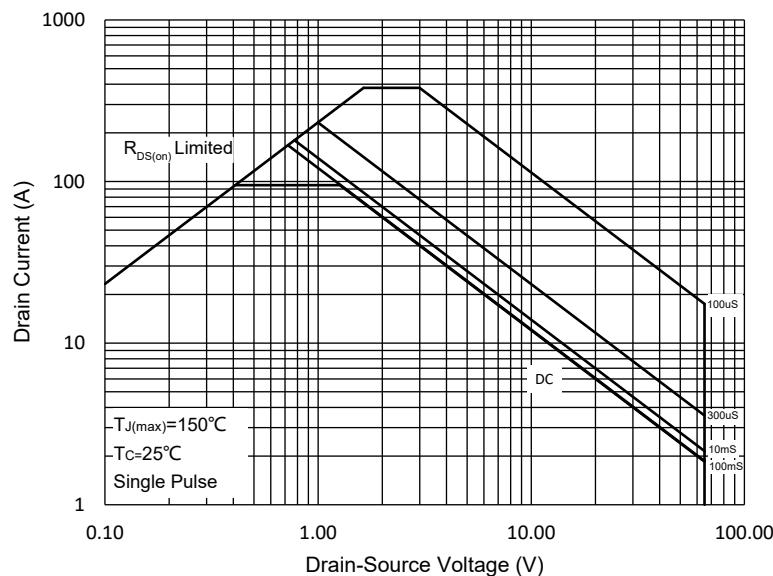
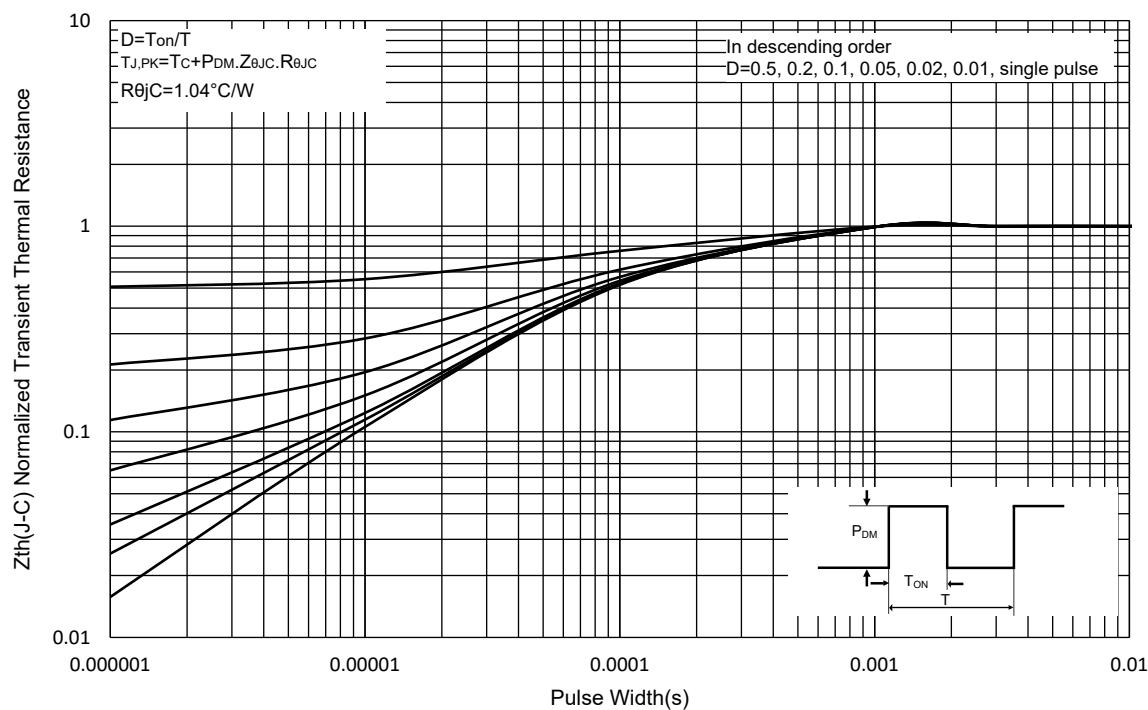


Fig.13 Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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