

IRFI9Z34N-VB Datasheet P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)	Q _g (Typ)	
- 60	0.100 at V _{GS} = - 10 V	- 20	12.5	
- 60	0.120 at V _{GS} = - 4.5 V	- 15	12.5	

FEATURES

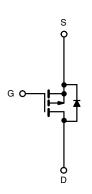
- TrenchFET® Power MOSFET
- 100 % UIS Tested

APPLICATIONS

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 2$	5 °C, unless othe	rwise noted			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current (T _{.1} = 175 °C)	T _C = 25 °C	- I _D	- 20		
Continuous Diain Current (1) = 175 C)	T _C = 100 °C		- 12		
Pulsed Drain Current		I _{DM}	- 60	Α	
Continuing Source Current (Diode Conduction)		I _S	- 12		
Avalanche Current		I _{AS}	- 12		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7.2	mJ	
Maximum Daylor Dissination	T _C = 25 °C	В	30 ^a	W	
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^b] vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunding to Ambient	t ≤ 10 sec	R _{thJA}	20	25	°C/W	
Junction-to-Ambient ^D	Steady State	TthJA 62	62	75		
Junction-to-Case		R _{thJC}	5	6		

Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.



Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit	
Static	1 -7			-71			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	- 3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = - 60 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ	
		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 175 °C			- 150		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 10			Α	
		V _{GS} = - 10 V, I _D = - 5 A		0.100		Ω	
		V _{GS} = - 10 V, I _D = - 5 A, T _J = 125 °C		0.150			
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C		0.200			
		V _{GS} = - 4.5 V, I _D = - 2 A		0.120			
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S	
Dynamic	.						
Input Capacitance	C _{iss}			550		pF	
Output Capacitance	C _{oss}	V _{DS} = - 25 V, V _{GS} = 0 V, f = 1 MHz		95			
Reverse Transfer Capacitance	C _{rss}]		60			
Total Gate Charge	Qg			12.5	19		
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3		nC	
Gate-Drain Charge	Q_{gd}	1		3.2		1	
Gate Resistance	R_{g}	f = 1 MHz		8.0		Ω	
Turn-On Delay Time ^c	t _{d(on)}			5	10		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_{L} = 3.57 \Omega$		14	25		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong -8.4 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 2.5 \Omega$		15	25	ns	
Fall Time ^c	t _f	1		7	12		
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C) ^b					
Pulsed Current	I _{SM}				- 20	Α	
Forward Voltage ^b	V _{SD}	I _F = - 2 A, V _{GS} = 0 V	- 0.9 - 1.3		- 1.3	V	
Reverse Recovery Time	t _{rr}	L _ 9 A di/dt _ 100 A/::2		50	80	ns	
Reverse Recovery Time	Q _{rr}	I _F = - 8 A, di/dt = 100 A/μs		80	120	nC	

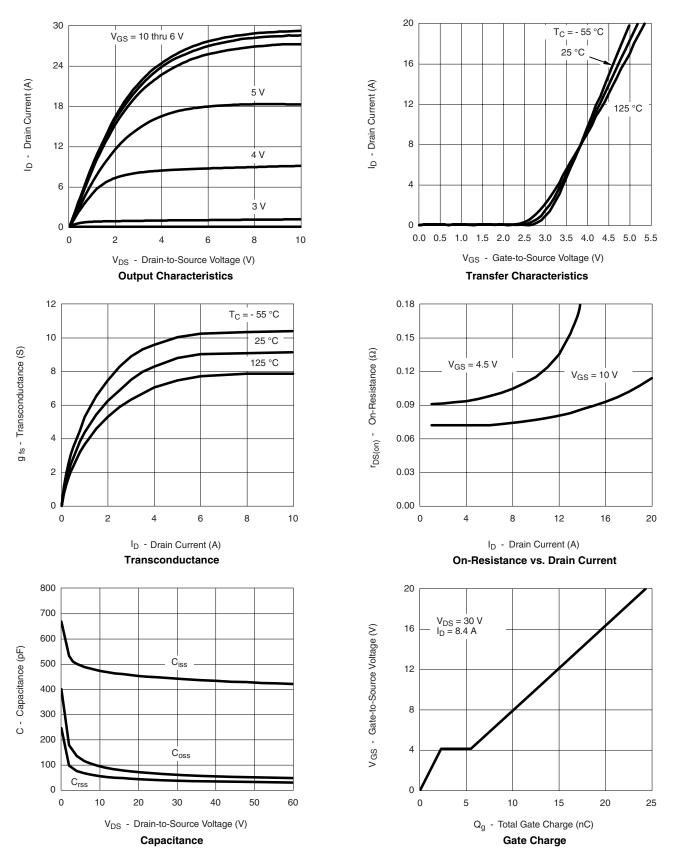
Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

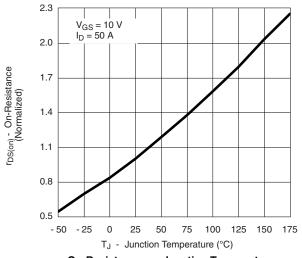


TYPICAL CHARACTERISTICS 25 °C unless noted

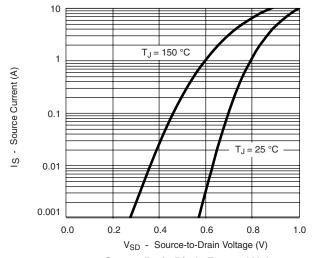




TYPICAL CHARACTERISTICS 25 °C unless noted



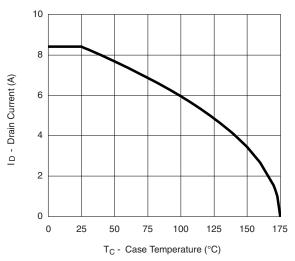




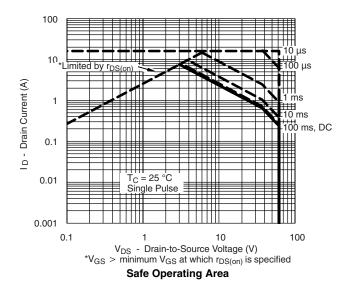
Source-Drain Diode Forward Voltage

THERMAL RATINGS

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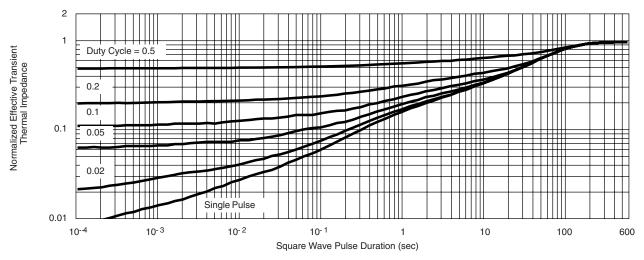
Drain Current vs. Case Temperature



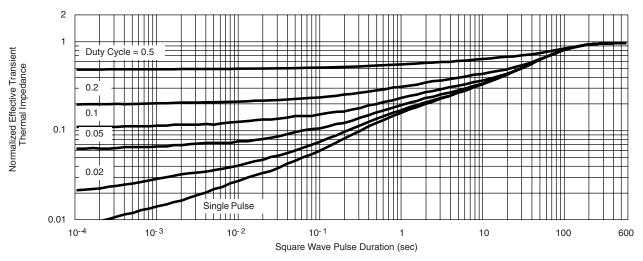
服务热线:400-655-8788



THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Ambient

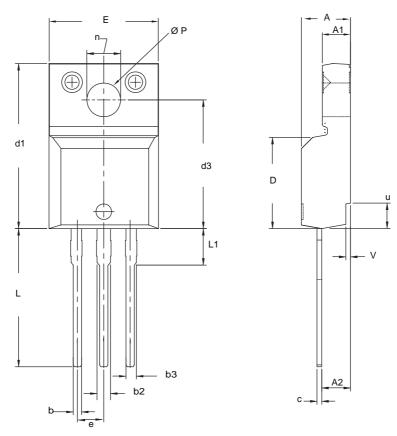


Normalized Thermal Transient Impedance, Junction-to-Case

服务热线:400-655-8788 5



TO-220 FULLPAK



DIM.	MILLIN	MILLIMETERS		HES	
	MIN.	MAX.	MIN.	MAX.	
Α	4.570	4.830	0.180	0.190	
A1	2.570	2.830	0.101	0.111	
A2	2.510	2.850	0.099	0.112	
b	0.622	0.890	0.024	0.035	
b2	1.229	1.400	0.048	0.055	
b3	1.229	1.400	0.048	0.055	
С	0.440	0.629	0.017	0.025	
D	8.650	9.800	0.341	0.386	
d1	15.88	16.120	0.622	0.635	
d3	12.300	12.920	0.484	0.509	
Е	10.360	10.630	0.408	0.419	
е	2.54	2.54 BSC		0.100 BSC	
L	13.200	13.730	0.520	0.541	
L1	3.100	3.500	0.122	0.138	
n	6.050	6.150	0.238	0.242	
ØΡ	3.050	3.450	0.120	0.136	
u	2.400	2.500	0.094	0.098	
٧	0.400	0.500	0.016	0.020	

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Notes

- To be used only for process drawing.
 These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
 All critical dimensions should C meet C_{pk} > 1.33.
 All dimensions include burrs and plating thickness.
 No chipping or package damage.



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