

CMF70R360

700V, 0.33Ω typ., 13A N-Channel Super Junction Power MOSFET

General Description

The CMF70R360 is power MOSFET using Cmos's advanced super junction technology that can realize very low on-resistance and gate charge. It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of Low EMI to designers as well as low switching loss.

Features

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

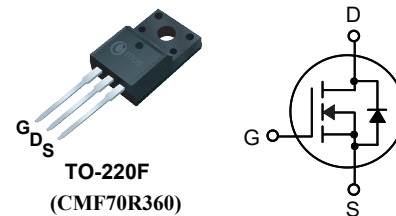
Product Summary

BVDSS	R _{DS(on)} max.	ID
700V	0.36Ω	13A

Applications

- Adaptor
- Motor Control
- DC – DC Converters

TO-220F Pin Configuration



Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-Source Voltage	700	V
I _D	Drain Current - Continuous (T _C = 25°C) - Continuous (T _C = 100°C)	13	A
		8	A
I _{DM}	Drain Current - Pulsed	52	A
V _{GSS}	Gate-Source Voltage	±30	V
E _{AS}	Single Pulsed Avalanche Energy ¹	453	mJ
P _D	Power Dissipation (T _C = 25°C)	33	W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Rating	Units
R _{θJC}	Thermal Resistance, Junction-to-Case Max.	3.79	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient Max.	56	°C/W

Electrical Characteristic (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	700	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 700\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	3	--	4	V
$R_{DS(on)}$	Static Drain-Source on-Resistance	$V_{GS} = 10\text{ V}, I_D = 6.5\text{ A}$	--	0.33	0.36	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 20\text{ V}, I_D = 6.5\text{ A}$	--	7.2	--	S
R_g	Gate Resistance	$V_{DS}=0\text{ V}, V_{GS}=0\text{ V}, f=1\text{ MHz}$	--	8.2	--	Ω

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS}=45\text{ V}, V_{GS} = 0\text{ V}$ $f = 1.0\text{ MHz}$	--	690	--	pF
C_{oss}	Output Capacitance		--	250	--	pF
C_{rss}	Reverse Transfer Capacitance		--	6	--	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 400\text{ V}, I_D = 6\text{ A}$ $R_G = 27\text{ }\Omega, V_{GS} = 10\text{ V}$	--	20	--	ns
t_r	Turn-On Rise Time		--	26	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	105	--	ns
t_f	Turn-Off Fall Time		--	32	--	ns
Q_g	Total Gate Charge	$V_{DS} = 480\text{ V}, I_D = 6\text{ A}$ $V_{GS} = 10\text{ V}$	--	26	--	nC
Q_{gs}	Gate-Source Charge		--	3.7	--	nC
Q_{gd}	Gate-Drain Charge		--	14	--	nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	13	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	52	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 11A	--	0.87	1.4	V
t _{rr}	Reverse Recovery Time	V _{DD} = 100V, I _F = 6A	--	210	--	ns
Q _{rr}	Reverse Recovery Charge	di _F / dt = 100 A/μs	--	2.05	--	μC

Note :

1.The EAS data shows Max. rating .The test condition is $V_{DS}=80\text{ V}, V_{GS}=10\text{ V}, L=30\text{ mH}, I_{AS}=5.5\text{ A}$.

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Typical Characteristics
