

### General Description

The 20P09 uses advanced process technology and design to provide excellent  $R_{DS(ON)}$ . It can be used in a wide variety of applications.

### Product Summary

BVDSS	$R_{DS(on)}$ max.	ID
-90V	140mΩ	-20A

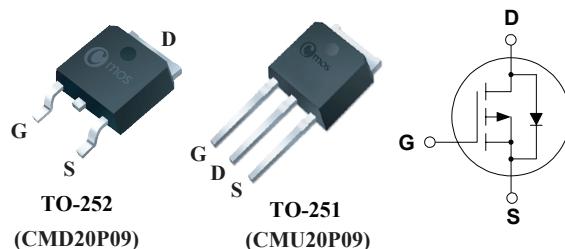
### Features

- P-Channel
- Low ON-resistance.
- 100% avalanche tested
- RoHS Compliant

### Applications

- Switching Voltage Regulators
- Motor Drivers

### TO-252/251 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-90	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current	-20	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current	-14	A
$I_{DM}$	Pulsed Drain Current	-80	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	312	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	75	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	62.5	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	1.67	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-90	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$ , $I_D=-15\text{A}$	---	110	140	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-10\text{A}$	---	126	160	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D =-250\mu\text{A}$	-2	---	-4	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-90\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$ , $I_D=-10\text{A}$	---	28	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	8.8	---	$\Omega$
$Q_g$	Total Gate Charge	$V_{\text{DS}}=-80\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $I_D=-15\text{A}$	---	70	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	10	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	20	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-50\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $I_D=-10\text{A}$ $R_G = 4.7\Omega$	---	30	---	ns
$T_r$	Rise Time		---	12	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	290	---	
$T_f$	Fall Time		---	41	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	6500	---	pF
$C_{\text{oss}}$	Output Capacitance		---	160	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	100	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-20	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-80	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=-20\text{ A}$ , $T_J=25^\circ\text{C}$	---	-0.87	-1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=-50\text{V}$  ,  $V_{\text{GS}}=-10\text{V}$ , $L=1\text{mH}$ , $I_{\text{AS}}=-25\text{A}$ .

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

**Typical Characteristics**
