

General Description

The CMSA75N04 combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDS(ON) .

This device is ideal for load switch and battery protection applications.

Features

- Fast switching
- Lower On-resistance
- 100% avalanche tested
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	75	A
$I_D @ T_C = 100^\circ C$		53	A
I_{DM}	Pulsed Drain Current	225	A
EAS	Single Pulse Avalanche Energy ¹	352	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	60	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	---	50	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction -Case	---	2.1	$^\circ C/W$

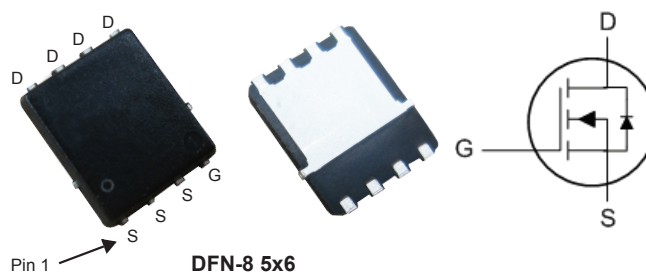
Product Summary

BVDSS	RDS(ON)	ID
40V	8m Ω	75A

Applications

- Motor control and drive
- Battery management
- UPS (Uninterruptible Power Supplies)

DFN-8 5x6 Pin Configuration



Type	Package	Marking
CMSA75N04	DFN- 8 5*6	CMSA75N04

N-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	40	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=10A$	---	---	8	m Ω
		$V_{GS}=4.5V$, $I_D=15A$	---	---	11	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=38V$, $V_{GS}=0V$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V$, $I_D=10A$	---	28	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	7	---	Ω
Q_g	Total Gate Charge	$V_{DS}=20V$, $I_D=20A$ $V_{GS}=4.5V$	---	20	---	nC
Q_{gs}	Gate-Source Charge		---	5	---	
Q_{gd}	Gate-Drain Charge		---	10	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=20V$, $V_{GS}=10V$, $R_G=3.3\Omega$ $I_D=1A$	---	10	---	ns
T_r	Rise Time		---	7	---	
$T_{d(off)}$	Turn-Off Delay Time		---	26	---	
T_f	Fall Time		---	30	---	
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	2000	---	pF
C_{oss}	Output Capacitance		---	340	---	
C_{rss}	Reverse Transfer Capacitance		---	180	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Diode continuous forward current	$V_G=V_D=0V$, Force Current	---	---	75	A
$I_{S,pulse}$	Diode pulse current		---	---	225	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=1A$, $T_J=25^{\circ}\text{C}$	---	---	1.2	V

Notes

1. The test condition is $V_{DS}=25V$, $V_{GS}=10V$, $L=0.5\text{mH}$, $I_D=38A$.

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