

CMB200N06

60V, 2.2mΩ typ., 180A N-Channel MOSFET

General Description

The CMB200N06 uses advanced trench technology and design to provide excellent RDS(ON). This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

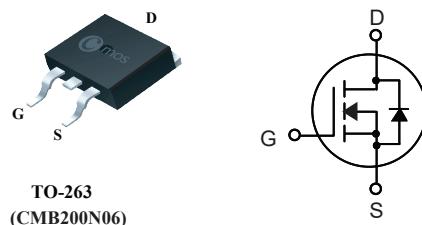
Product Summary

BVDSS	R _{D(S(on)) max.}	ID
60V	2.6mΩ	180A

Applications

- Motor control and drive
- Battery management
- Uninterruptible Power Supplies

TO-263 Pin Configuration



Features

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±25	V
I _D @T _c =25°C	Continuous Drain Current	180	A
I _D @T _c =100°C	Continuous Drain Current	120	A
I _{DM}	Pulsed Drain Current	720	A
EAS	Single Pulse Avalanche Energy ¹	2812	mJ
P _D @T _c =25°C	Total Power Dissipation	250	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient	---	62.5	°C/W
R _{θJC}	Thermal Resistance Junction-case	---	0.5	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=40\text{A}$	---	2.2	2.6	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D =250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$	---	1	100	uA
		$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}, T_J=100^\circ\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 25\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=20\text{A}$	---	44	---	S
Q_g	Total Gate Charge	$I_D=20\text{A}$	---	100	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DD}}=30\text{V}$	---	22	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	15	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{GS}}=10\text{V}$	---	35	---	ns
T_r	Rise Time	$V_{\text{DD}}=30\text{V}$	---	61	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$I_D=20\text{A}$	---	95	---	
T_f	Fall Time	$R_G=10\Omega$	---	35	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	7850	---	pF
C_{oss}	Output Capacitance		---	1350	---	
C_{rss}	Reverse Transfer Capacitance		---	930	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_{\text{GS}}=V_{\text{DS}}=0\text{V}$, Force Current	---	---	180	A
I_{SM}	Pulsed Source Current		---	---	720	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{SD}}=30\text{A}$, $T_J = 25^\circ\text{C}$	---	0.77	1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=60\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=75\text{A}$.

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

Typical Characteristics

