

Features

- ❑ IEC61000-4-2 (ESD) $\pm 30\text{kV}$ (air),
 $\pm 30\text{kV}$ (contact)
- IEC61000-4-4 (EFT) 80A (5/50ns)
- ❑ Peak Pulse Current($t_p=8/20\mu\text{s}$) 100A
- ❑ Protects one I/O line
- ❑ Working voltages : 7.0V
- ❑ Low leakage current
- ❑ ROHS compliant

Description

The TS0751LDX is designed for applications requiring transient overvoltage protection capability. They are intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment and other applications. These devices are ideal for situations where board space is at a premium. This series has been specifically designed to protect sensitive components which are connected to power data and transmission lines from overvoltage caused by ESD(electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).

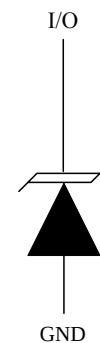
Applications

- ❑ Cell Phone Handsets and Accessories
- ❑ Microprocessor based equipment
- ❑ Personal Digital Assistants (PDA's)
- ❑ Notebooks, Desktops, and Servers
- ❑ Portable Instrumentation
- ❑ Networking and Telecom
- ❑ Serial and Parallel Ports.
- ❑ Peripherals

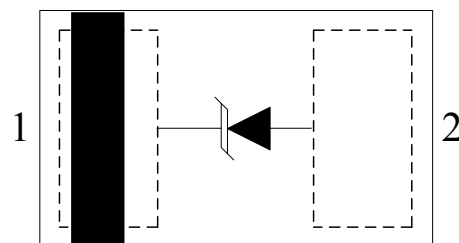
Mechanical Characteristics

- ❑ DFN1610-2L package
- ❑ Flammability Rating: UL 94V-0
- ❑ Packaging: Tape and Reel
- ❑ High temperature soldering guaranteed: $260^\circ\text{C}/10\text{s}$
- ❑ Reel size: 7 inch

Circuit Diagram



Pin Configuration



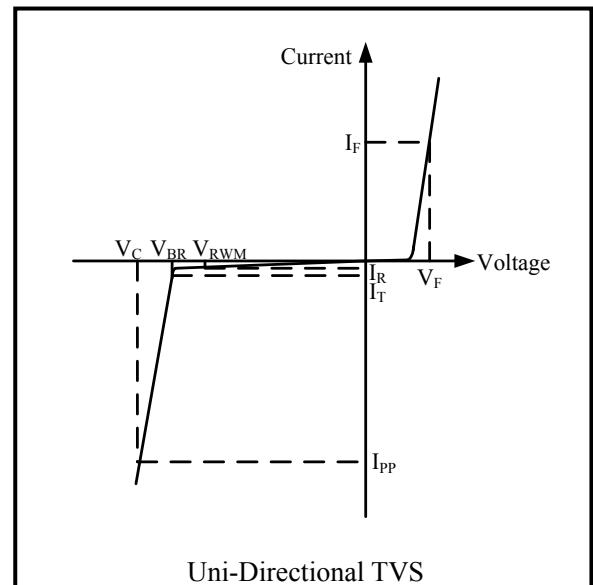
DFN1610-2L
(Top View)

Absolute Maximum Rating

Symbol	Parameter	Value	Units
V_{ESD}	ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	± 30 ± 30	kV
I_{PP}	Peak Pulse Current(8/20us)	100	A
T_{OPT}	Operating Temperature	-55/+150	°C
T_{STG}	Storage Temperature	-55/+150	°C
T_L	Lead Soldering Temperature	260 (10 sec.)	°C

Electrical Characteristics (T = 25°C)

Symbol	Parameter
V_{RWM}	Nominal Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{t1}	Trigger Voltage
I_{t1}	Trigger Current @ V_{t1}
V_h	Holding Voltage
I_h	Holding Current @ V_h
V_C	Clamping Voltage @ I_{PP}
V_{CR}	Reverse Clamping Voltage @ I_{PP}
I_{PP}	Maximum Peak Pulse Current
C_{ESD}	Parasitic Capacitance



Symbol	Test Condition	Minimum	Typical	Maximum	Units
V_{RWM}				7.0	V
I_R	$V_{RWM} = 7.0V, T = 25^\circ C$		0.01	0.1	μA
V_{BR}	$I_T = 1mA$	7.5	8.2	10.0	V
V_F	$I_T = 1mA$			1.2	V
V_C	$I_{PP} = 1A, t_p = 8/20\mu s$			10.0	V
V_C	$I_{PP} = 50A, t_p = 8/20\mu s$			15.0	V
V_C	$I_{PP} = 100A, t_p = 8/20\mu s$			20.0	V
C_{ESD}	$V_R = 0V, f = 1MHz$		760		pF

Typical Characteristics

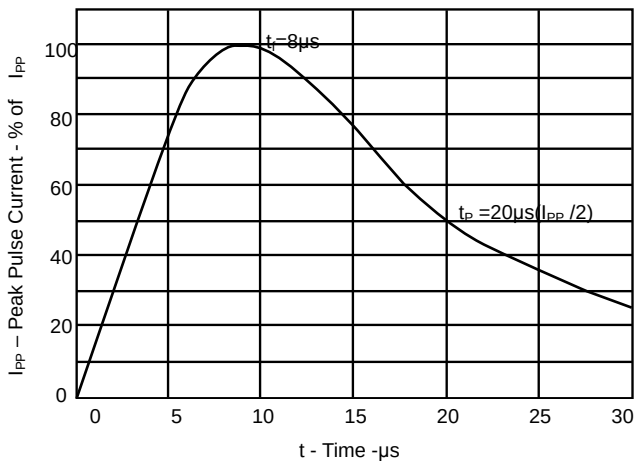


Fig 1. Pulse Waveform

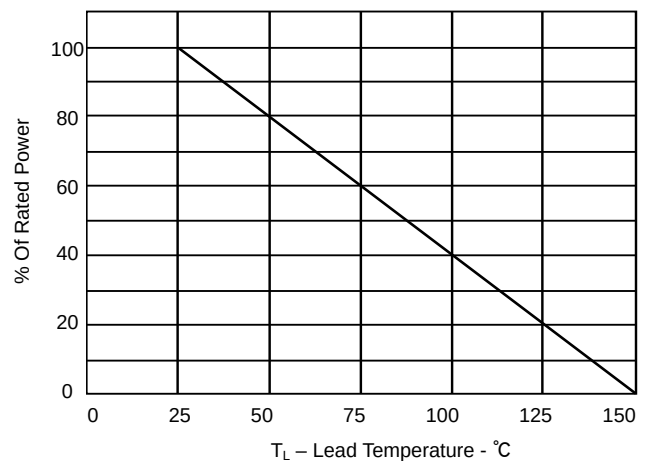


Fig 2. Power Derating Curve

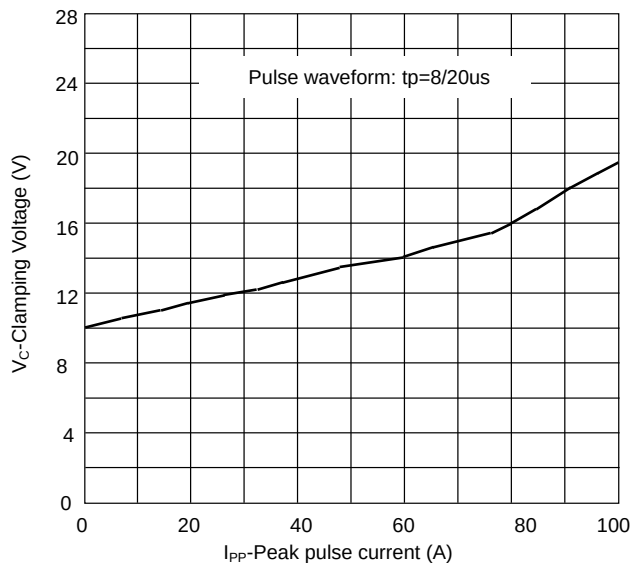


Fig 3. Clamping voltage vs. Peak pulse current

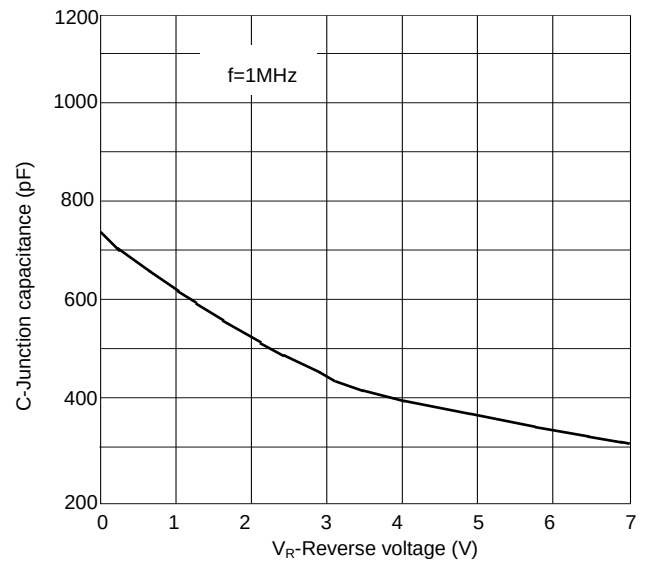
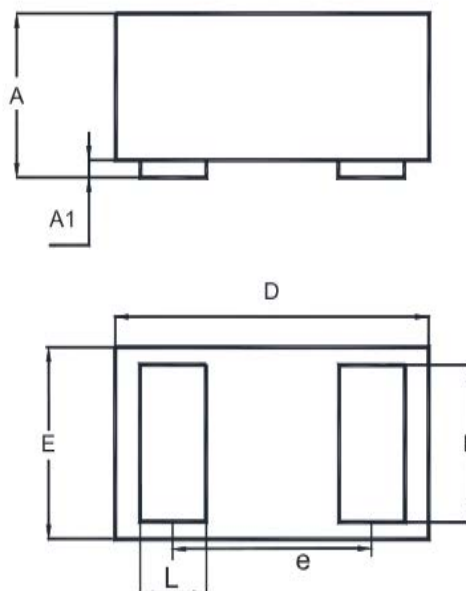


Fig 4. Capacitance vs. Reverse voltage

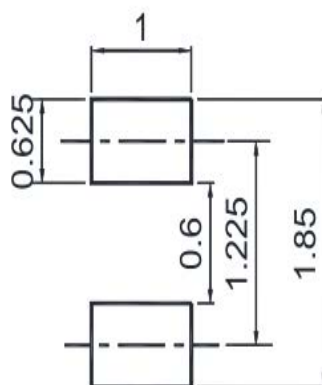
Package Outline Demensions

□ DFN1610-2L Package

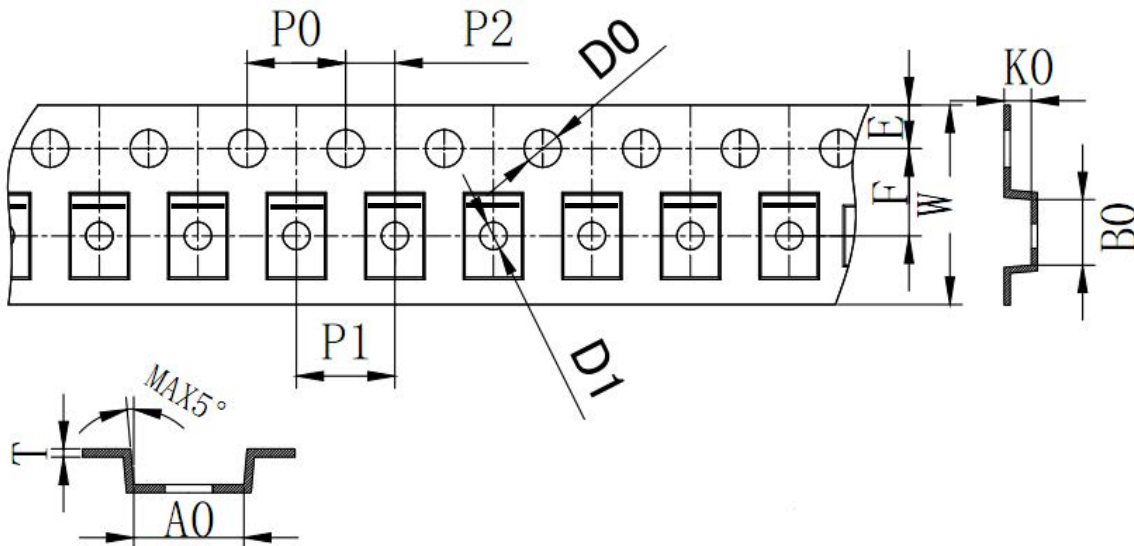


UNIT	A	A1	b	D	E	e	L
mm	0.55 0.45	0.05 0	0.95 0.85	1.65 1.55	1.05 0.95	1.1	0.45 0.35

Recommended Soldering Footprint

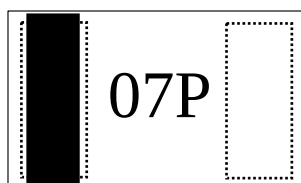


Tape and Reel Specification



SYMBOL	A0	B0	K0	P0	P1	P2
SPEC	1.15 ± 0.05	1.80 ± 0.05	0.63 ± 0.05	4.00 ± 0.10	2.00 ± 0.10	2.00 ± 0.05
SYMBOL	T	E	F	D0	D1	W
SPEC	0.20 ± 0.05	1.75 ± 0.10	3.50 ± 0.05	1.55 ± 0.05	$0.60^{+0.10}_{-0}$	$8.00^{+0.30}_{-0.10}$

Marking Codes



Note:

(1) "07P" is part number, fixed

Ordering Information

Part Number	Working Voltage	Quantity Per Reel	Reel Size
TS0751LDX	7.0V	3,000	7 Inch