

SIA537EDJ-T1-GE3-VB Datasheet

N- and P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY

	V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
N-Channel	30	0.018 at $V_{GS} = 10$ V	7 ^e	6.2
		0.020 at $V_{GS} = 8$ V	7 ^e	
		0.024 at $V_{GS} = 4.5$ V	7 ^e	
P-Channel	- 30	0.032 at $V_{GS} = - 10$ V	- 7 ^e	18.5
		0.034 at $V_{GS} = - 8$ V	- 7 ^e	
		0.040 at $V_{GS} = - 4.5$ V	- 6.5	

FEATURES

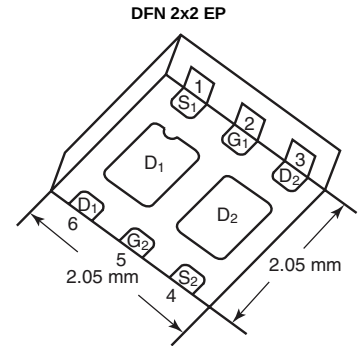
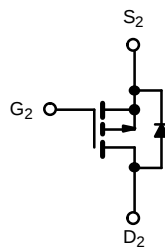
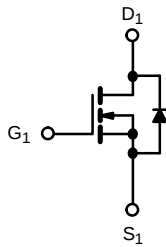
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Motor Drive
- Mobile Power Bank



ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	30	- 30	V
Gate-Source Voltage	V_{GS}	± 20	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	7 ^e	- 7 ^e	A
		6.2	- 6.2	
		6.2 ^{b, c}	- 6.0 ^{b, c}	
		5.2 ^{b, c}	- 5.0 ^{b, c}	
Pulsed Drain Current (10 μ s Pulse Width)	I_{DM}	40	- 40	
Source-Drain Current Diode Current	I_S	2.6	- 2.6	
		1.6 ^{b, c}	- 1.6 ^{b, c}	
Pulsed Source-Drain Current	I_{SM}	40	- 40	mJ
Single Pulse Avalanche Current	I_{AS}	10	- 20	
Single Pulse Avalanche Energy	E_{AS}	5	20	W
Maximum Power Dissipation	P_D	3.1	3.2	
		2	2.1	
		2 ^{b, c}	2 ^{b, c}	
		1.28 ^{b, c}	1.28 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	N-Channel		P-Channel		Unit
		Typ.	Max.	Typ.	Max.	
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	50	62.5	47	62.5	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	30	40	29	38	

Notes:

a. Based on $T_C = 25$ °C.

b. Surface mounted on 1" x 1" FR4 board.

c. $t = 10$ s.

d. Maximum under steady state conditions is 120 °C/W (n-channel) and 110 °C/W (p-channel).

e. Package limited.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions		Min.	Typ. ^a	Max.	Unit	
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	N-Ch	30			V	
		V _{GS} = 0 V, I _D = - 250 μA	P-Ch	- 30				
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA	N-Ch		40		mV/°C	
		I _D = - 250 μA	P-Ch		- 34			
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = 250 μA	N-Ch		- 4.1			
		I _D = - 250 μA	P-Ch		5			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	N-Ch	1.0		2.0	V	
		V _{DS} = V _{GS} , I _D = - 250 μA	P-Ch	- 1.0		- 2.0		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V	N-Ch			± 100	nA	
		V _{DS} = 0 V, V _{GS} = ± 12 V	P-Ch			± 100		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	N-Ch			1	μA	
		V _{DS} = - 30 V, V _{GS} = 0 V	P-Ch			- 1		
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C	N-Ch			10		
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C	P-Ch			- 10		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	N-Ch	20			A	
		V _{DS} = - 5 V, V _{GS} = - 10 V	P-Ch	- 20				
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 6.8 A	N-Ch		0.018		Ω	
		V _{GS} = - 10 V, I _D = - 8 A	P-Ch		0.032			
		V _{GS} = 8 V, I _D = 6.7 A	N-Ch		0.020			
		V _{GS} = - 8 V, I _D = - 6.5 A	P-Ch		0.034			
		V _{GS} = 4.5 V, I _D = 6.6 A	N-Ch		0.024			
		V _{GS} = - 4.5 V, I _D = - 5 A	P-Ch		0.040			
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 6.8 A	N-Ch		27		S	
		V _{DS} = - 15 V, I _D = - 6.7 A	P-Ch		25			
Dynamic ^a								
Input Capacitance	C _{iss}	N-Channel V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz P-Channel V _{DS} = - 20 V, V _{GS} = 0 V, f = 1 MHz	N-Ch		510		pF	
Output Capacitance	C _{oss}		P-Ch		620			
			N-Ch		95			
Reverse Transfer Capacitance	C _{rss}		P-Ch		115			
		N-Ch		33				
Total Gate Charge	Q _g	N-Channel V _{DS} = 20 V, V _{GS} = 10 V, I _D = 10 A P-Channel V _{DS} = - 20 V, V _{GS} = - 10 V, I _D = - 10 A N-Channel V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 10 A P-Channel V _{DS} = - 20 V, V _{GS} = - 4.5 V, I _D = - 10 A	N-Ch		6	10	nC	
			P-Ch		41.5	63		
	N-Ch			5.8	7			
	P-Ch			16	22			
	Gate-Source Charge		Q _{gs}	N-Ch		1.6		
				P-Ch		4.3		
Gate-Drain Charge	Q _{gd}	N-Ch		1.4				
		P-Ch		7				
Gate Resistance	R _g	f = 1 MHz	N-Ch	0.3	1.1	2.3	Ω	
			P-Ch	1.2	5.7	9.6		

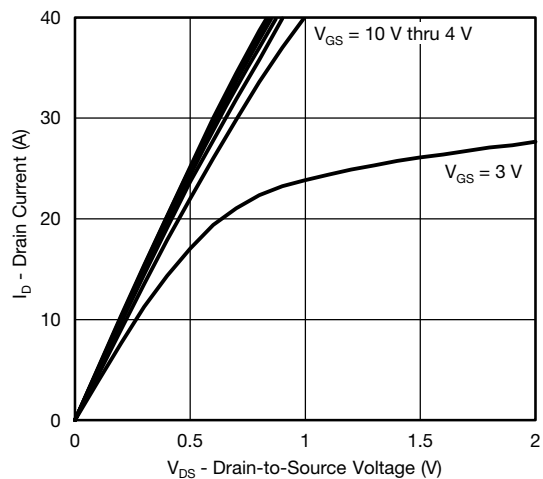
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions		Min.	Typ. ^a	Max.	Unit
Dynamic ^a							
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 20 V, R _L = 3.7 Ω I _D ≡ 5.4 A, V _{GEN} = 10 V, R _g = 1 Ω	N-Ch		4	8	ns
			P-Ch		10	16	
Rise Time	t _r		N-Ch		10	17	
			P-Ch		9	15	
Turn-Off Delay Time	t _{d(off)}	P-Channel V _{DD} = - 20 V, R _L = 2 Ω I _D ≡ - 10 A, V _{GEN} = - 10 V, R _g = 1 Ω	N-Ch		16	22	
			P-Ch		23	26	
Fall Time	t _f		N-Ch		5	9	
			P-Ch		10	16	
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 20 V, R _L = 3.7 Ω I _D ≡ 5.4 A, V _{GEN} = 4.5 V, R _g = 1 Ω	N-Ch		10	16	
			P-Ch		26	35	
Rise Time	t _r		N-Ch		11	20	
			P-Ch		16	26	
Turn-Off Delay Time	t _{d(off)}	P-Channel V _{DD} = - 20 V, R _L = 2 Ω I _D ≡ - 10 A, V _{GEN} = - 4.5 V, R _g = 1 Ω	N-Ch		13	22	
			P-Ch		16	26	
Fall Time	t _f		N-Ch		5	9	
			P-Ch		16	26	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	N-Ch			2.6	A
			P-Ch			- 2.6	
Pulse Diode Forward Current ^a	I _{SM}		N-Ch			40	
			P-Ch			- 40	
Body Diode Voltage	V _{SD}	I _S = 5.4 A	N-Ch		0.81	1.2	V
		I _S = - 2 A	P-Ch		- 0.77	- 1.2	
Body Diode Reverse Recovery Time	t _{rr}	N-Channel I _F = 5 A, dI/dt = 100 A/μs, T _J = 25 °C	N-Ch		12	25	ns
			P-Ch		31	57	
Body Diode Reverse Recovery Charge	Q _{rr}	P-Channel I _F = - 5 A, dI/dt = - 100 A/μs, T _J = 25 °C	N-Ch		10	17	nC
			P-Ch		29	47	
Reverse Recovery Fall Time	t _a		N-Ch		10		ns
			P-Ch		13		
Reverse Recovery Rise Time	t _b	N-Ch		7			
		P-Ch		23			

Notes:

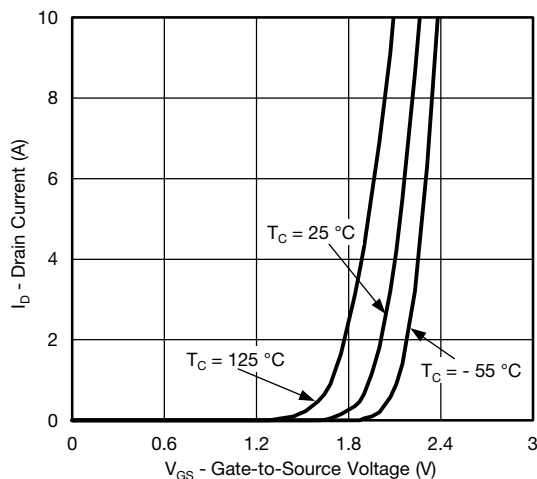
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

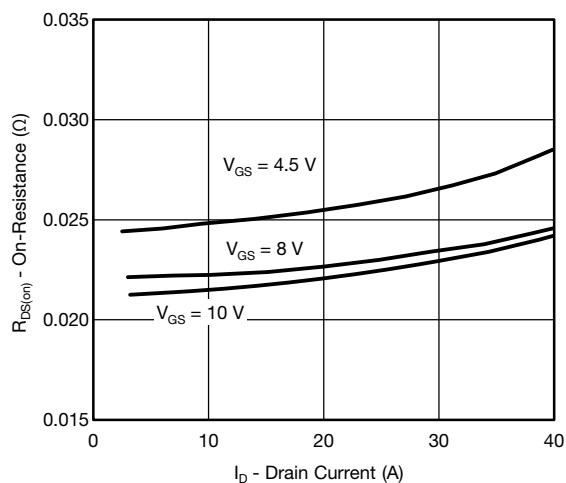
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


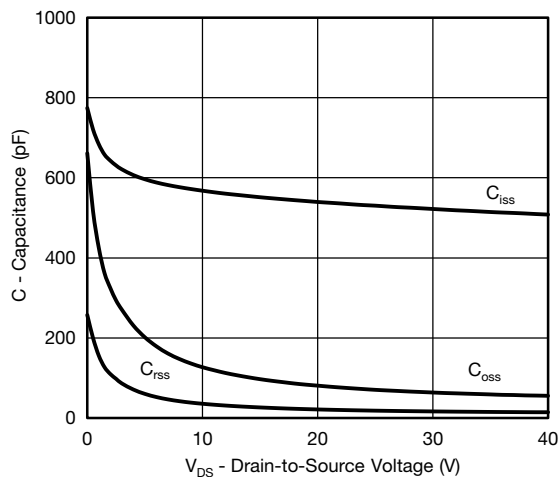
Output Characteristics



Transfer Characteristics



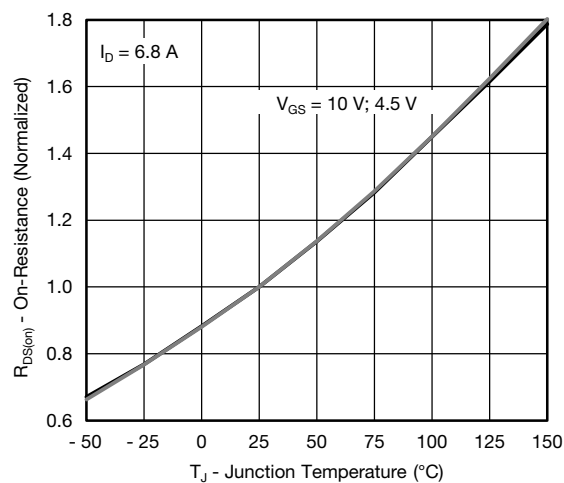
On-Resistance vs. Drain Current and Gate Voltage



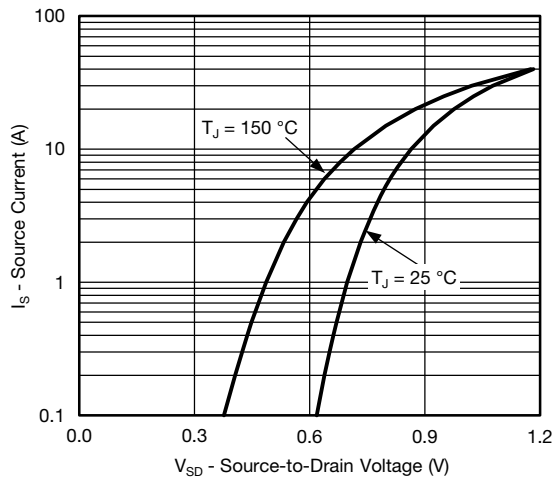
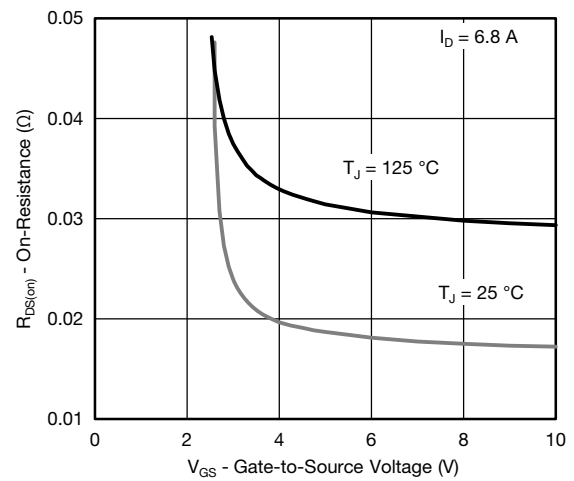
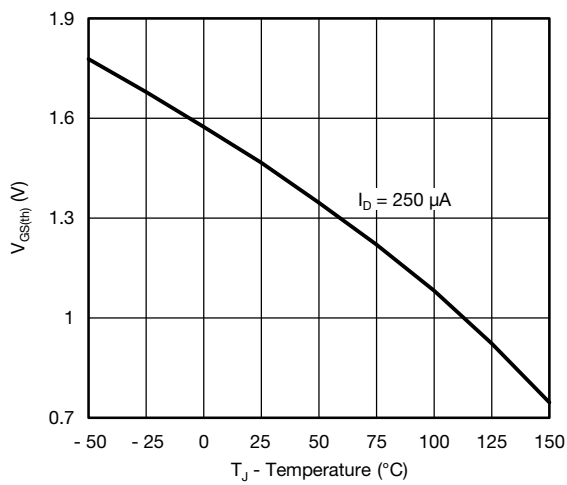
Capacitance

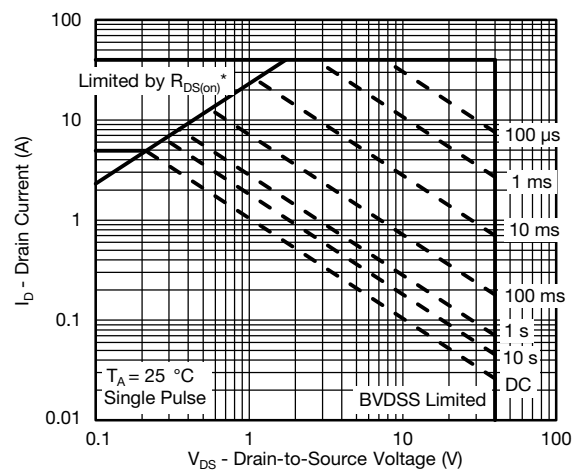


Gate Charge



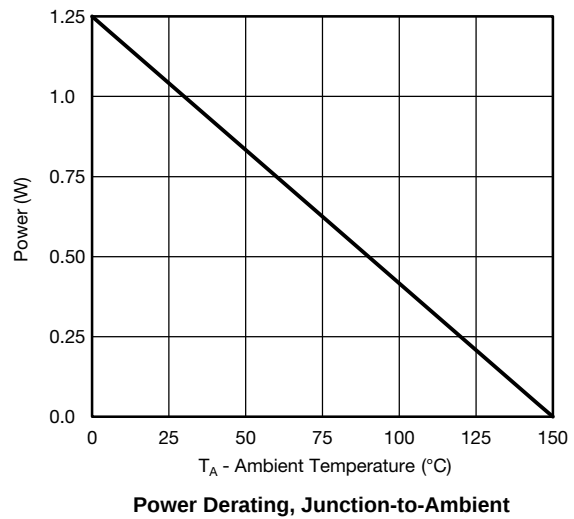
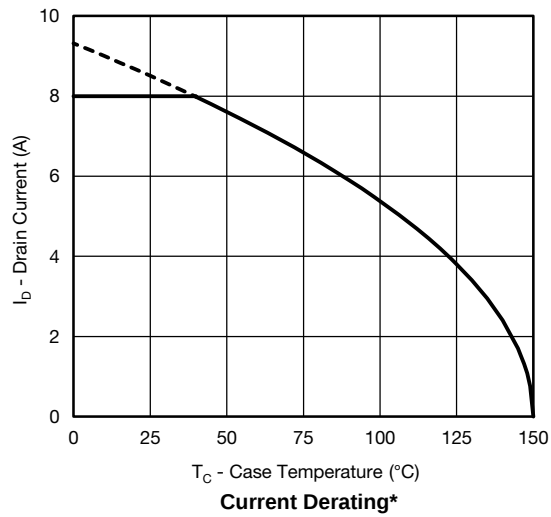
On-Resistance vs. Junction Temperature

N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

Threshold Voltage

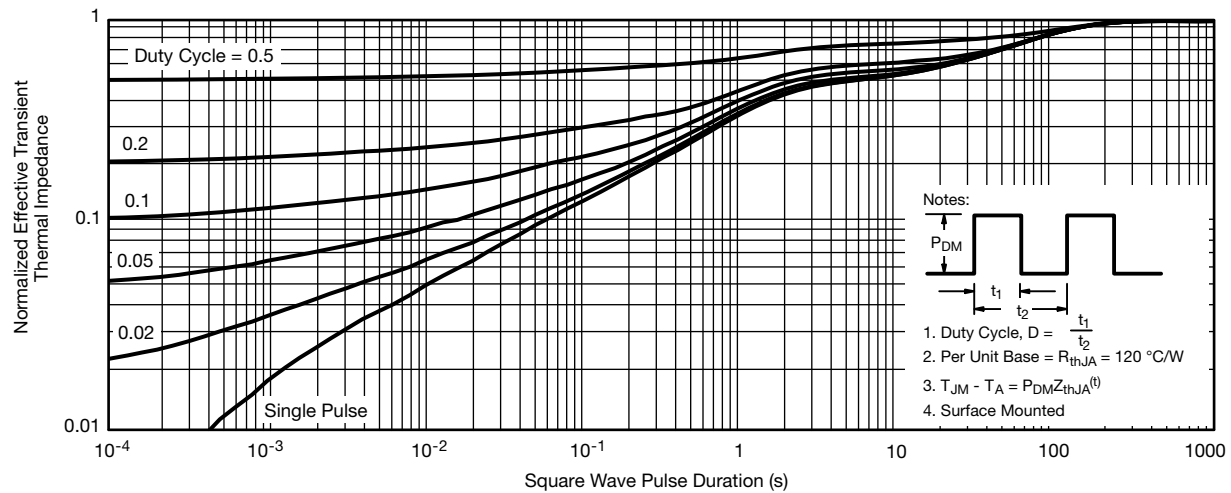
Single Pulse Power, Junction-to-Ambient


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

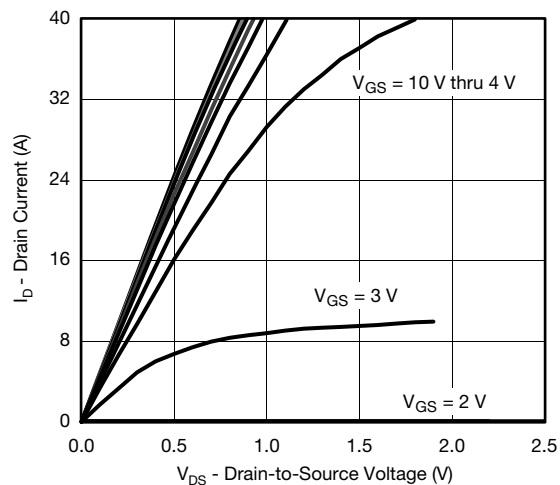
Safe Operating Area, Junction-to-Ambient

N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


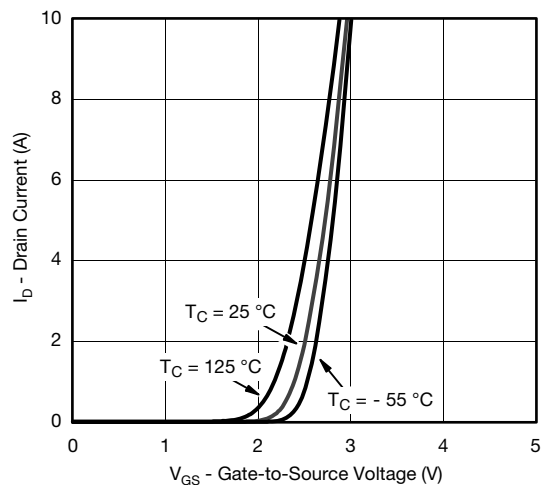
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Ambient

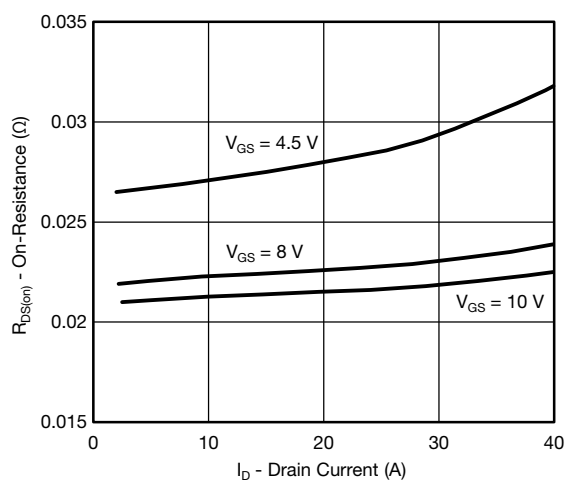
Normalized Thermal Transient Impedance, Junction-to-Foot

P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


Output Characteristics



Transfer Characteristics



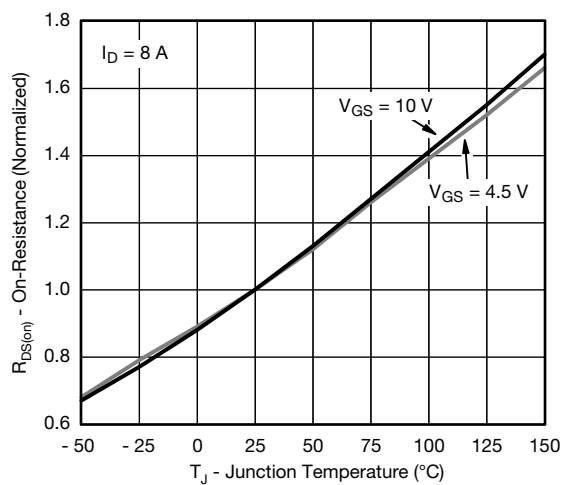
On-Resistance vs. Drain Current and Gate Voltage



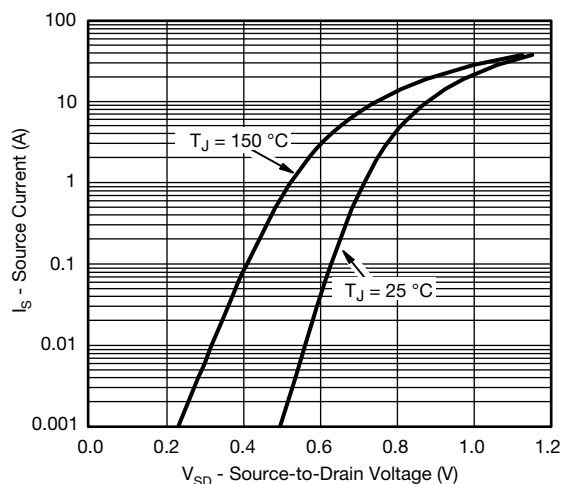
Capacitance

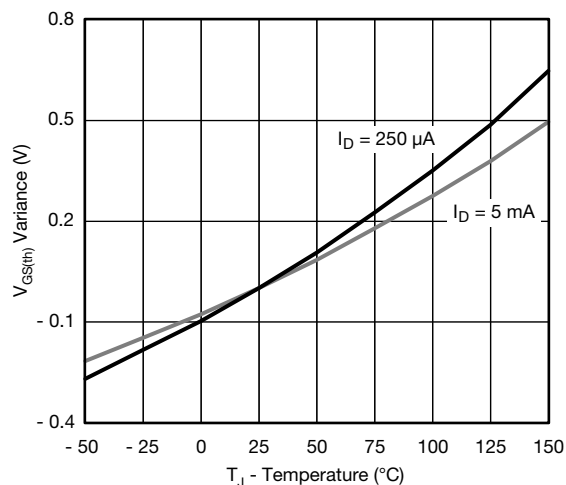
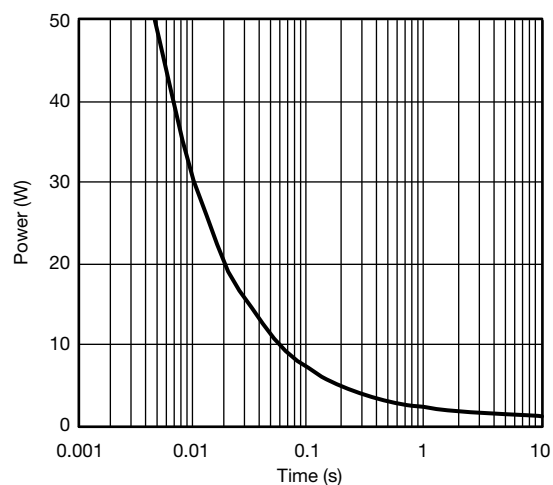
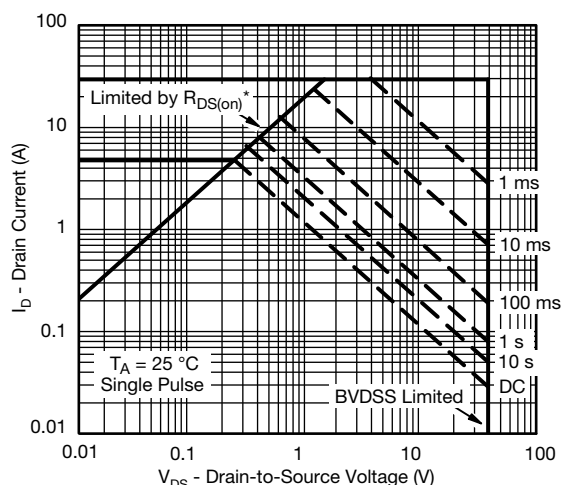


Gate Charge

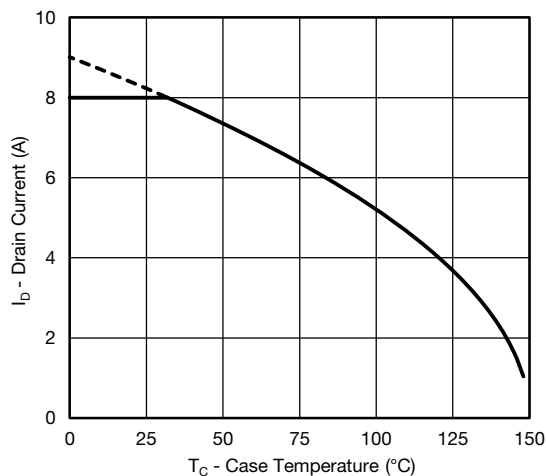


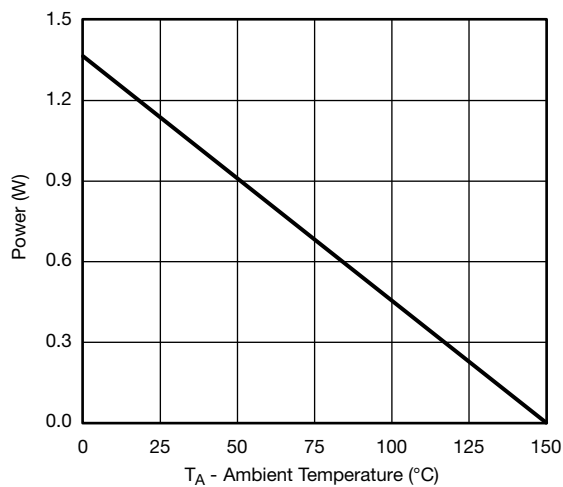
On-Resistance vs. Junction Temperature

P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

Threshold Voltage

Single Pulse Power, Junction-to-Ambient


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area, Junction-to-Ambient

P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Current Derating*

Power Derating, Junction-to-Foot

Power Derating, Junction-to-Ambient

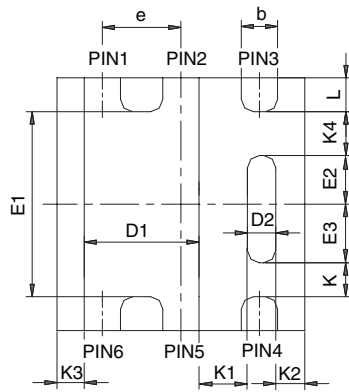
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

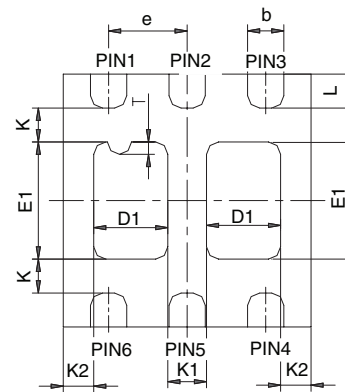
Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

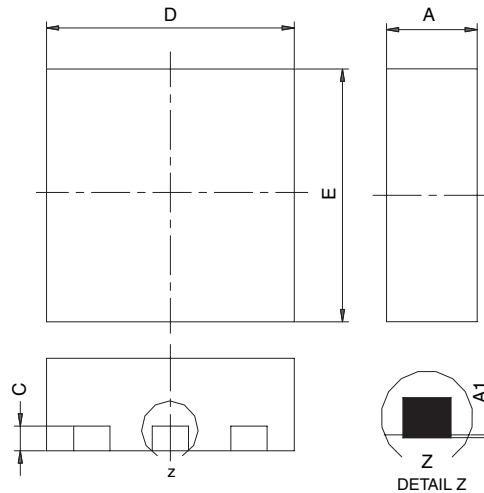
DFN 2x2



BACKSIDE VIEW OF SINGLE



BACKSIDE VIEW OF DUAL



Notes:

1. All dimensions are in millimeters
2. Package outline exclusive of mold flash and metal burr
3. Package outline inclusive of plating

DIM	SINGLE PAD						DUAL PAD					
	MILLIMETERS			INCHES			MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max
A	0.675	0.75	0.80	0.027	0.030	0.032	0.675	0.75	0.80	0.027	0.030	0.032
A1	0	-	0.05	0	-	0.002	0	-	0.05	0	-	0.002
b	0.23	0.30	0.38	0.009	0.012	0.015	0.23	0.30	0.38	0.009	0.012	0.015
C	0.15	0.20	0.25	0.006	0.008	0.010	0.15	0.20	0.25	0.006	0.008	0.010
D	1.98	2.05	2.15	0.078	0.081	0.085	1.98	2.05	2.15	0.078	0.081	0.085
D1	0.85	0.95	1.05	0.033	0.037	0.041	0.513	0.613	0.713	0.020	0.024	0.028
D2	0.135	0.235	0.335	0.005	0.009	0.013						
E	1.98	2.05	2.15	0.078	0.081	0.085	1.98	2.05	2.15	0.078	0.081	0.085
E1	1.40	1.50	1.60	0.055	0.059	0.063	0.85	0.95	1.05	0.033	0.037	0.041
E2	0.345	0.395	0.445	0.014	0.016	0.018						
E3	0.425	0.475	0.525	0.017	0.019	0.021						
e	0.65 BSC			0.026 BSC			0.65 BSC			0.026 BSC		
K	0.275 TYP			0.011 TYP			0.275 TYP			0.011 TYP		
K1	0.400 TYP			0.016 TYP			0.320 TYP			0.013 TYP		
K2	0.240 TYP			0.009 TYP			0.252 TYP			0.010 TYP		
K3	0.225 TYP			0.009 TYP								
K4	0.355 TYP			0.014 TYP								
L	0.175	0.275	0.375	0.007	0.011	0.015	0.175	0.275	0.375	0.007	0.011	0.015
T							0.05	0.10	0.15	0.002	0.004	0.006
ECN: C-07431 – Rev. C, 06-Aug-07												
DWG: 5934												

Figure 1 is a technical drawing of a 3D-printed part, showing a top-down view. The drawing includes dimensions in inches and millimeters (in parentheses). The overall dimensions are 2.500 (0.098) inches by 2.500 (0.098) inches. The central rectangular hole has a width of 0.613 (0.024) inches and a height of 0.950 (0.037) inches. The distance from the left edge to the center of the hole is 0.300 (0.012) inches, and the distance from the right edge to the center is 0.350 (0.014) inches. The distance from the top edge to the center of the hole is 0.325 (0.013) inches, and the distance from the bottom edge to the center is 0.275 (0.011) inches. The distance from the center of the hole to the right edge of the part is 0.160 (0.006) inches. The distance from the center of the hole to the left edge of the part is 0.475 (0.019) inches. The distance from the center of the hole to the top edge of the part is 0.275 (0.011) inches, and the distance from the center to the bottom edge is 0.275 (0.011) inches. The distance from the center of the hole to the right edge of the part is 0.650 (0.026) inches, and the distance from the center to the left edge is 1.600 (0.063) inches.

13

Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental ; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.