

General Description

The 10N65 uses advanced planar stripe DMOS technology to provide excellent $R_{DS(ON)}$ and superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

Features

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS Compliant

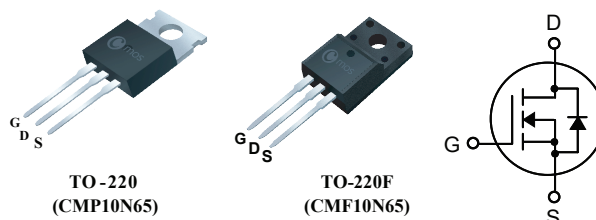
Product Summary

BVDSS	$R_{DS(ON)}$	ID
650V	1 Ω	10A

Applications

- Charger
- Adaptor
- Power Supply

TO-220/220F Pin Configuration



Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	220	220F	Units
V_{DSS}	Drain-Source Voltage	650		V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$)	10	10*	A
	- Continuous ($T_C = 100^\circ\text{C}$)	6.2	6.2*	A
I_{DM}	Drain Current - Pulsed ¹	40	40*	A
V_{GSS}	Gate-Source Voltage	± 30		V
E_{AS}	Single Pulsed Avalanche Energy ²	1102		mJ
dv/dt	Peak Diode Recovery dv/dt	4.5		V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	162	50	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300		$^\circ\text{C}$

* Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	220	220F	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.77	2.5	$^\circ\text{C/W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ. ³	0.5	---	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ^{3,4}	65	65	$^\circ\text{C/W}$

Electrical Characteristic (T_C=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250μA	650	--	--	V
BV _{DSS} / T _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.7	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	--	--	1	μA
		V _{DS} = 520 V, T _C = 125 °C	--	--	10	
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2	--	4	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 5A	--	0.7	1	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	2100	--	pF
C _{oss}	Output Capacitance		--	140	--	pF
C _{rss}	Reverse Transfer Capacitance		--	16	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 325 V, I _D = 10A R _G = 25Ω	--	30	--	ns
t _r	Turn-On Rise Time		--	75	--	ns
t _{d(off)}	Turn-Off Delay Time		--	140	--	ns
t _f	Turn-Off Fall Time		--	80	--	ns
Q _g	Total Gate Charge	V _{DS} = 520 V, I _D = 10A V _{GS} = 10 V	--	48	--	nC
Q _{gs}	Gate-Source Charge		--	7.5	--	nC
Q _{gd}	Gate-Drain Charge		--	18	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	10	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	40	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 10A	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 10A	--	430	--	ns
Q _{rr}	Reverse Recovery Charge	dl _F / dt = 100 A/μs	--	4.3	--	μC

note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. The EAS data shows Max. rating . The test condition is V_{DD}=80V , V_{GS}=10V , L=20mH , I_{AS}=10.5A.
3. The value of R_{θJA} is measured with the device in a still air environment with T_A =25°C.
4. The R_{θJA} is the sum of the thermal impedance from junction to case R_{θJC} and case to ambient.

This product has been designed and qualified for the consumer market.

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Cmos reserves the right to improve product design ,functions and reliability without notice.

Typical Characteristics

