

1.5A Fast Ultra Low Dropout Linear Regulators

Features

- Ultra low dropout voltage
- Low ground pin current
- Load regulation of 0.04%
- 15µA quiescent current in shutdown mode
- Guaranteed output current of 1.5A DC
- Available in TO-263 and TO-220 packages
- Output voltage accuracy ± 1.5%
- Sense option improves better load regulation
- Extremely low output capacitor requirements
- Overt emperature/overcurrent protection
- -40°C to +125°C junction temperature range

TO-220-5 TO-263-5

Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
LP3965T-1.8	TO-220-5	LP3965-1.8	TUBE	1000pcs/Box
LP3965T-2.5	TO-220-5	LP3965-2.5	TUBE	1000pcs/Box
LP3965T-3.3	TO-220-5	LP3965-3.3	TUBE	1000pcs/Box
LP3965T-5.0	TO-220-5	LP3965-5.0	TUBE	1000pcs/Box
LP3965T-ADJ	TO-220-5	LP3965-ADJ	TUBE	1000pcs/Box
LP3965S-1.8/TR	TO-263-5	LP3965-1.8	REEL	500pcs/Reel
LP3965S-2.5/TR	TO-263-5	LP3965-2.5	REEL	500pcs/Reel
LP3965S-3.3/TR	TO-263-5	LP3965-3.3	REEL	500pcs/Reel
LP3965S-5.0/TR	TO-263-5	LP3965-5.0	REEL	500pcs/Reel
LP3965S-ADJ/TR	TO-263-5	LP3965-ADJ	REEL	500pcs/Reel



General Description

The LP3965 series of fast ultra low-dropout linear regulators operate from a +2.5V to +7.0V input supply. Wide range of preset output voltage options are available. These ultra low dropout linear regulators respond very fast to step changes in load which makes them suitable for low voltage microprocessor applications. The LP3965 are developed on a CMOS process which allows low quiescent current operation independent of output load current. This CMOS process also allows the LP3965 to operate under extremely low dropout conditions.

Dropout Voltage:

Ultra low dropout voltage; typically 38mV at 150mA load current and 380mV at 1.5A load current.

Ground Pin Current:

Typically 5mA at 1.5A load current.

Shutdown Mode:

Typically 15µA quiescent current when the shutdown pin is pulled low.

SENSE:

Sense pin improves regulation at remote loads.

Precision Output Voltage:

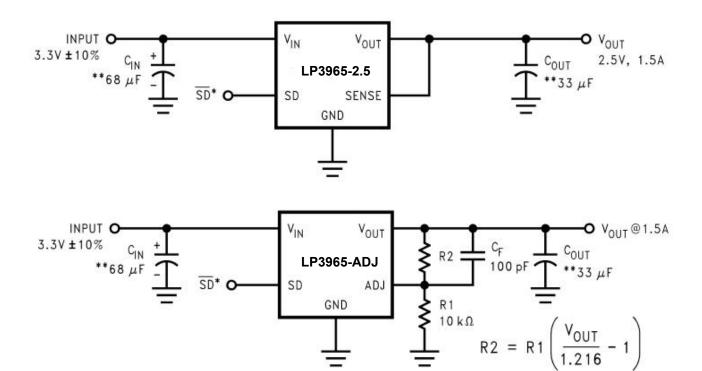
Multiple output voltage options are available ranging from $1.8V \ 2.5V \ 3.3V \ 5.0V$ and adjustable (LP3965), with a guaranteed accuracy of $\pm 1.5\%$ at room temperature, and $\pm 3.0\%$ over all conditions (varying line, load, and temperature).

Applications

- Microprocessor power supplies
- GTL, GTL+, BTL, and SSTL bus terminators
- Power supplies for DSPs
- SCSI terminator
- Post regulators
- High efficiency linear regulators
- Battery chargers
- Other battery powered applications



Typical Application Circuits

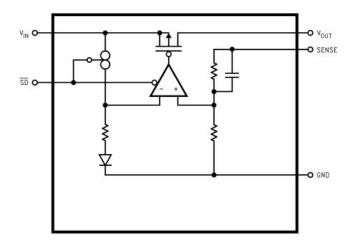


^{*}SD and ERROR pins must be pulled high through a $10k\Omega$ pull-up resistor. Connect the ERROR pin to ground if this function is not used. See applications section for more information.

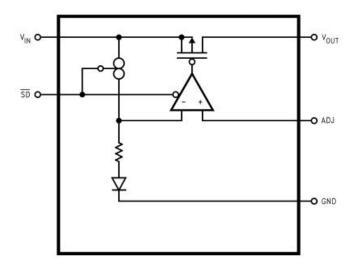
^{**} See Application Hints.



Block Diagram LP3965

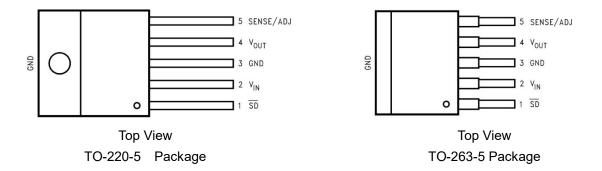


Block Diagram LP3965-ADJ





Connection Diagrams



Pin Description for TO220-5 and TO263-5 Packages

Pin#	LP3965				
FIII #	Name	Function			
1	SD	Shutdown			
2	V _{IN}	Input Supply			
3	GND	Ground			
4	Vouт	Output Voltage			
5	SENSE/ADJ	Remote Sense Pin or			
5	SENSE/ADJ	Output Adjust Pin			



Absolute Maximum Ratings (Note 1)

Condition	Min	Max	
Storage Temperature Range	-65°C	+150°C	
Lead Temperature(Soldering, 10 sec.)	-	245°C	
ESD Rating (Note 3)	-	2kV	
Power Dissipation (Note 2)	Internally Limited		
Input Supply Voltage (Survival)	-0.3V	+7.5V	
Shutdown Input Voltage (Survival)	-0.3V	V _{IN} +0.3V	
Output Voltage (Survival), (Note 6), (Note 7)	-0.3V	+7.5V	
IOUT (Survival)	Short Circuit Protected		
Maximum Voltage for SENSE Pin	-	V _{OUT} +0.3V	

Operating Ratings

Condition	Min	Max
Input Supply Voltage (Operating)(Note 12)	2.5V	7.0V
Shutdown Input Voltage (Operating)	-0.3V	+0.3V
Maximum Operating Current (DC)	-	1.5A
Operating Junction Temp. Range	-40°C	+125°C



Electrical Characteristics

Limits in standard typeface are for TJ = 25° C, and limits in **boldface type** apply over the **full operating temperature range.** Unless otherwise specified: $V_{IN} = V_{O(NOM)} + 1V$, $I_L = 10$ mA, $C_{OUT} = 33\mu$ F, $V_{SD} = V_{IN} - 0.3V$.

Symbol	Downwater	Conditions	Тур	LP3965	11:4-		
Symbol	Parameter	Conditions	(Note 4)	Min	Max	Units	
.,	Output Voltage	10 mA ≤ I∟≤ 1.5A		-1.5	+1.5	٥,	
Vo	Tolerance (Note 8)	V _{OUT} +1 ≤ V _{IN} ≤ 7.0V	0	-3.0	+3.0	%	
	Adjust Pin Voltage (ADJ	10 mA ≤ I _L ≤ 1.5A	4.040	1.198	1.234		
V_{ADJ}	version)	V _{OUT} +1.5V ≤ V _{IN} ≤ 7.0V	1.216	1.180	1.253	V	
ΔV _{OL}	Output Voltage Line	V _{OUT} +1V <v<sub>IN<7.0V,</v<sub>	0.02			%	
ΔVOL	Regulation (Note 8)	VOUT+IV <vin<1.uv,< td=""><td>0.06</td><td></td><td></td><td>70</td></vin<1.uv,<>	0.06			70	
AN / / A I	Output Voltage Load	40 4 1 14 5 4	0.04			0,	
$\Delta V_{O}/\Delta I_{OUT}$	Regulation (Note 8)	10 mA < I _L < 1.5 A	0.09			%	
		L = 150 m A	38		45		
V - V	Drangut Voltage (Note 10)	I _L = 150 mA	38		55	m)/	
V _{IN} - V _{OUT}	Dropout Voltage (Note 10)	I _L = 1.5 A	200		450	mV	
		IL- 1.5 A	380		550		
		I _L = 150 mA	4		9		
la	Ground Pin Current In	1 <u>C</u> = 100 HW.	4		10	mA	
I_{GND}	Normal Operation Mode	I _L = 1.5 A	5		14		
		IL = 1.5 A	5		15		
	Ground Pin Current In	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	4.5		25		
I_{GND}	Shutdown Mode (Note 11)	V _{SD} ≤ 0.2V	15		75	μA	
	Dook Output Current	(Note 2)	2.5	2.0		_	
I _{O(PK)}	Peak Output Current	Peak Output Current (Note 2)		1.7		A	
SHORT CI	RCUIT PROTECTION						
I _{SC}	Short Circuit Current		4.5			Α	
OVER TEN	IPERATURE PROTECTION						
Tsh(t)	Shutdown Threshold		165			°C	
Tsh(h)	Thermal Shutdown		10			°C	
ish(n)	Hysteresis		10				
SHUTDOW	VN INPUT						
\/	Shutdown Throshold	Output = High	V _{IN}	V _{IN} -0.3			
V _{SDT}	Shutdown Threshold	Output = Low	0		0.2	\ \ \	



Electrical Characteristics

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Cumbal	Davamatan	Conditions	Тур	LP3965	(Note 5)	l lucita
Symbol	Parameter	Conditions	(Note 4)	Min	Max	Units
T_{dOFF}	Turn-off delay	I _L = 1.5 A	20			μs
T_{dON}	Turn-on delay	I∟ = 1.5 A	25			μs
I _{SD}	SD Input Current	V _{SD} = VIN	1			nA
		AC PARAMETERS				
		V _{IN} = V _{OUT} + 1.5V				
		C _{OUT} = 100uF	60			
		V _{OUT} = 3.3V				dB
PSRR	Ripple Rejection	$V_{IN} = V_{OUT} + 0.3V$				ub
		C _{OUT} = 100uF	40			
		V _{OUT} = 3.3V				
$p_{n(I/f}$	Output Noise Density	f = 120Hz	0.8			μV
•	Output Noise Voltage	BW = 10Hz – 100kHz	z – 100kHz 150			11\/ (rma)
e n	(rms)	BW = 300Hz – 300kHz	100			μV (rms)

Note 1: Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating ratings indicate conditions for which the device is intended to be functional, but does not guarantee specific performance limits. For guaranteed specifications and test conditions, see Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: At elevated temperatures, devices must be derated based on package thermal resistance. The devices in TO220 package must be derated at θ jA = 50° C/W (with 0.5in², 1oz. copper area), junction-to-ambient (with no heat sink). The devices in the TO263 surface-mount package must be derated at θ jA = 60° C/W (with 0.5in2, 1oz. copper area), junction-to-ambient. The devices in SOT223 package must be derated at θ jA = 90° C/W (with 0.5in2, 1oz. copper area), junction-to-ambient.

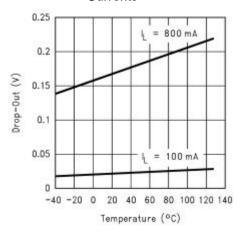
- Note 3: The human body model is a 100pF capacitor discharged through a $1.5k\Omega$ resistor into each pin.
- Note 4: Typical numbers are at 25°C and represent the most likely parametric norm.
- **Note 5:** Limits are 100% production tested at 25°C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's Average Outgoing Quality Level (AOQL).
- Note 6: If used in a dual-supply system where the regulator load is returned to a negative supply, the LP396X output must be diode-clamped to ground.
- **Note 7:** The output PMOS structure contains a diode between the VIN and VOUT terminals. This diode is normally reverse biased. This diode will get forward biased if the voltage at the output terminal is forced to be higher than the voltage at the input terminal. This diode can typically withstand 200mA of DC current and 1Amp of peak current.
- **Note 8:** Output voltage line regulation is defined as the change in output voltage from the nominal value due to change in the input line voltage. Output voltage load regulation is defined as the change in output voltage from the nominal value due to change in load current. The line and load regulation specification contains only the typical number. However, the limits for line and load regulation are included in the output voltage tolerance specification.
- **Note 9:** Dropout voltage is defined as the minimum input to output differential voltage at which the output drops 2% below the nominal value. Dropout voltage specification applies only to output voltages of 2.5V and above. For output voltages below 2.5V, the drop-out voltage is nothing but the input to output differential, since the minimum input voltage is 2.5V.
- Note 10: This specification has been tested for $-40^{\circ}\text{C} \le \text{TJ} \le 85^{\circ}\text{C}$ since the temperature rise of the device is negligible under shutdown conditions.
- Note 12: The minimum operating value for V_{IN} is equal to either $[V_{OUT(NOM)} + V_{DROPOUT}]$ or 2.5V, whichever is greater.



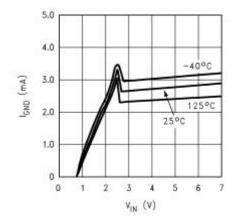
Typical Performance Characteristics

Unless otherwise specified, Vin =Vo(NoM) + 1V, Vout = 2.5V, Cout = 33µF, Iout = 10mA, Cin = 68µF, Vsp = Vin, and Ta = 25°C.

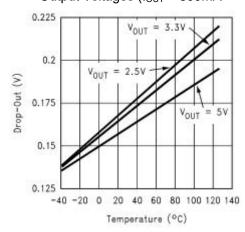
Drop-Out Voltage vs Temperature for Different Load Currents



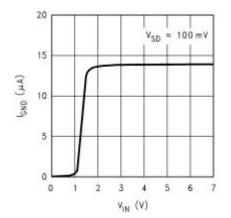
Ground Pin Current vs Input Voltage (V_{SD}=V_{IN})



Drop-Out Voltage vs Temperature for Different Output Voltages (I_{OUT} = 800mA



Ground Pin Current vs Input Voltage (V_{SD}=100mV)

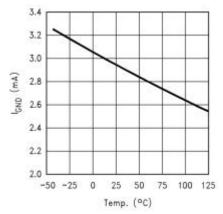




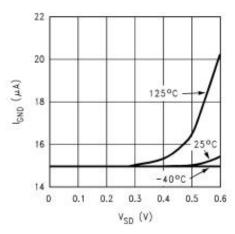
Typical Performance Characteristics

Unless otherwise specified, VIN = VO(NOM) + 1V, VOUT = 2.5V, $COUT = 33\mu F$, IOUT = 10mA, $CIN = 68\mu F$, VSD = VIN, and $TA = 25^{\circ}C$. (Continued)

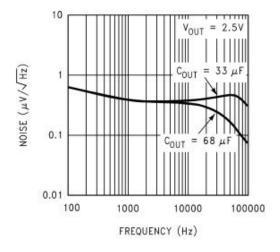
Ground Current vs Temperature (V_{SD}=V_{IN})



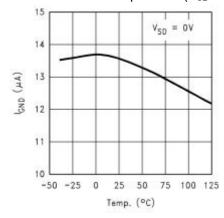
Ground Pin Current vs Shutdown Pin Voltag



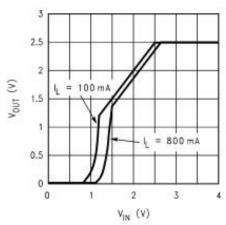
Output Noise Density, Vout = 2.5V



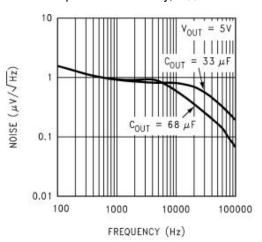
Ground Current vs Temperature (V_{SD}=0V)



Input Voltage vs Output Voltage



Output Noise Density, V_{OUT}= 5V

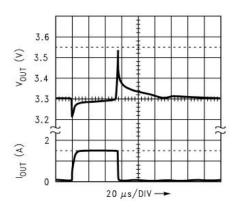




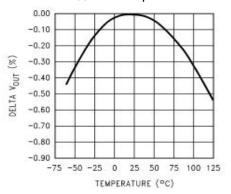
Typical Performance Characteristics

Unless otherwise specified, VIN = VO(NOM) + 1V, VOUT = 2.5V, $COUT = 33\mu F$, IOUT = 10mA, $CIN = 68\mu F$, VSD = VIN, and $TA = 25^{\circ}C$. (Continued)

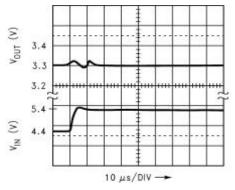
Load Transient Response



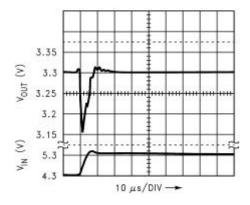
δV_{OUT} vs Temperature



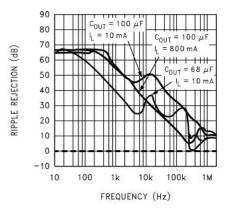
Line Transient Response



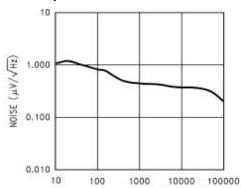
Line Transient Response ($I_{OUT} = 1.5A$)



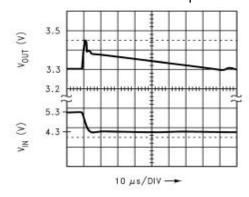
Ripple Rejection vs Frequenc



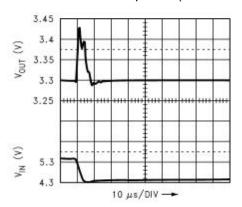
Noise Density V_{IN} = 3.5V, VOUT = 2.5V, I_L =10 mA



Line Transient Response



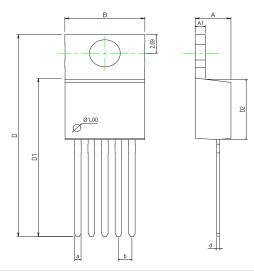
Line Transient Response (I_{OUT} = 1.5A)





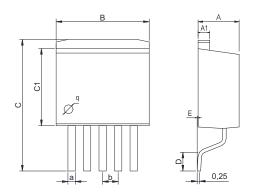
Physical Dimensions

TO-220-5



Dimensions In Millimeters(TO-220-5)									
Symbol:	Α	A1	В	D	D1	D2	а	d	b
Min:	4.52	1.25	10	28.2	22.4	8.69	0.71	0.33	1.70BSC
Max:	4.62	1.29	10.3	28.9	22.6	8.79	0.97	0.42	1.70000

TO-263-5



Dimensions In Millimeters(TO-263-5)									
Symbol:	А	A1	В	С	C1	D	E	а	Ф
Min:	4.45	1.22	10	13.7	8.40	1.90	0	0.71	1.70BSC
Max:	4.62	1.32	10.4	14.6	8.90	2.10	0.20	0.97	1.70030

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Revision History

DATE	REVISION	PAGE
2014-6-10	New	1-14
2023-7-24	Modify the package dimension diagram TO-220-5 Update encapsulation type Update Lead Temperature	12、1、6



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