

### General Description

The WSD3810DN33 is the highest performance trench Dual N-Channel MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD3810DN33 meet the RoHS and Green Product requirement 100%  $E_{AS}$  guaranteed with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100%  $E_{AS}$  Guaranteed
- Green Device Available

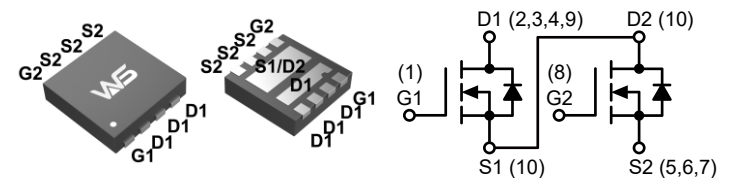
### Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$	$I_{TEM}$
30V	9.0m $\Omega$	18A	Q1
30V	8.5m $\Omega$	18A	Q2

### Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

### DFN33X33-8S Pin Configuration



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating		Units	
		Q1	Q2		
$V_{DS}$	Drain-Source Voltage	30	30	V	
$V_{GS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$		
$I_D$	Drain Current (Continuous) <sup>1,3</sup>	$T_C=25^\circ\text{C}$	18	18	A
		$T_C=100^\circ\text{C}$	12.3	12.3	
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	45	45		
$E_{AS}$	Single Pulse Avalanche Energy	$V_{DD}=25\text{V}$ , $V_{GS}=10\text{V}$ , $L=1.0\text{mH}$ , $R_G=25\Omega$		mJ	
$P_D$	Power Dissipation	$T_C=25^\circ\text{C}$		W	
$T_{STG}$	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ\text{C}$	
$T_J$	Operating Junction Temperature Range	-55 to 150	-55 to 150		

### Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	---	6	$^\circ\text{C/W}$

**Q1 Electrical Characteristics** ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
<b>Static</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	---	---	1.0	$\mu A$
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_{DS}=250\mu A$	1.0	1.6	2.5	V
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=10A$	---	9.0	10.8	m $\Omega$
		$V_{GS}=4.5V, I_D=8A$	---	12	17.5	
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=5A$	---	12	---	S
$V_{SD}$	Diode Forward Voltage	$I_{SD}=1A, V_{GS}=0V$	---	---	1.3	V
<b>Switching</b>						
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_D=5A$	---	8	---	nC
$Q_{gs}$	Gate-Source Charge		---	1.6	---	
$Q_{gd}$	Gate-Drain Charge		---	1.2	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=15V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	8.5	---	ns
$T_r$	Turn-on Rise Time		---	10	---	
$T_{d(off)}$	Turn-off Delay Time		---	14	---	
$T_f$	Turn-off Fall Time		---	10.6	---	
<b>Dynamic</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0\text{MHz}$	---	455	---	pF
$C_{oss}$	Output Capacitance		---	318	---	
$C_{rss}$	Reverse Transfer Capacitance		---	22	---	

**Note:**

- The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The value in any given application depends on the user's specific board design.
- Repetitive rating, pulse width limited by junction temperature.
- The current rating is based on the  $t \leq 10s$  junction to ambient thermal resistance rating.

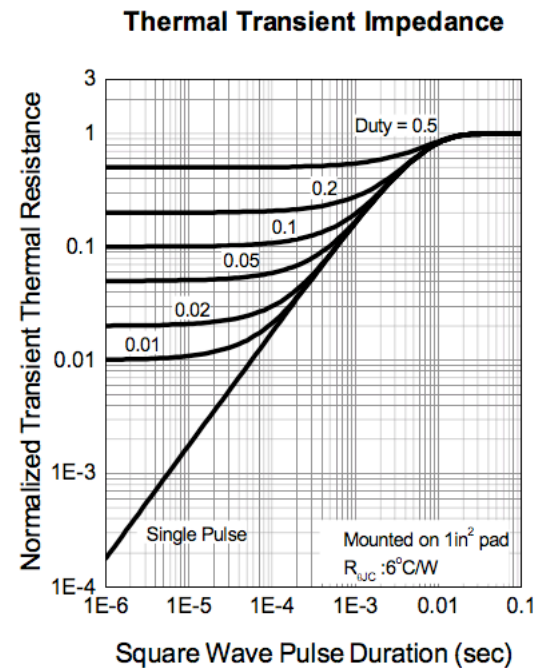
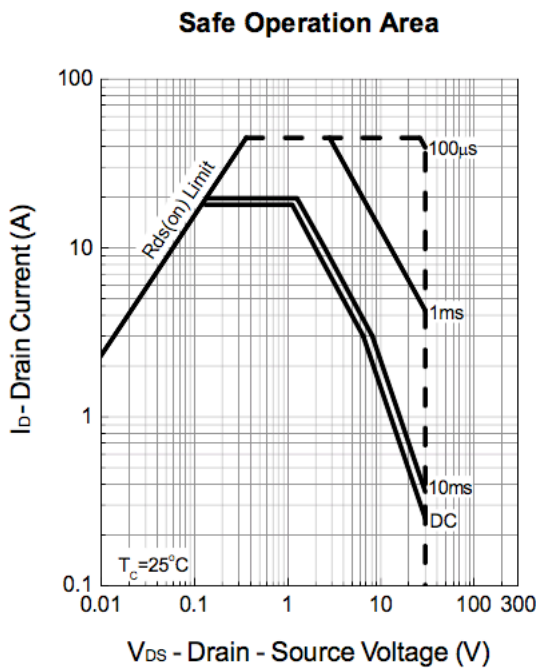
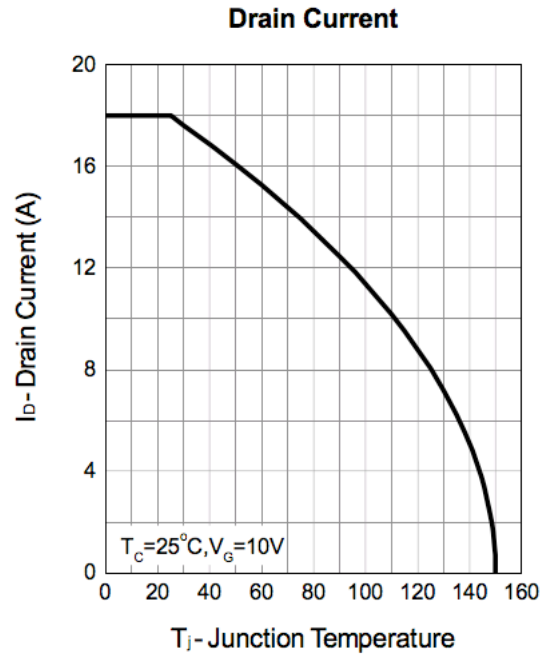
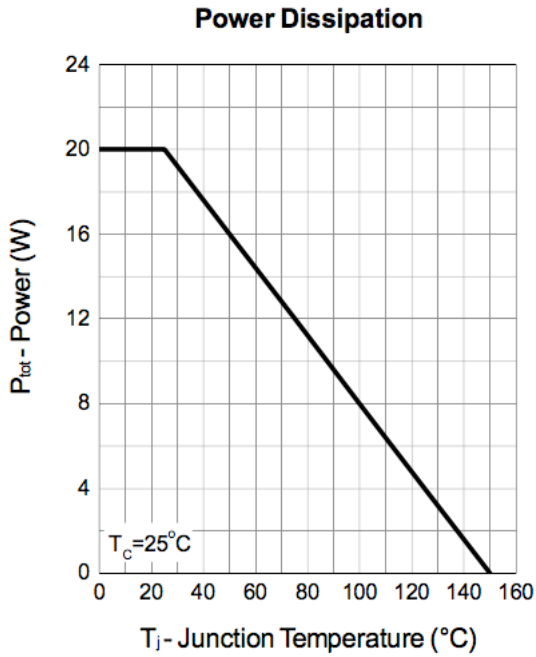
**Q2 Electrical Characteristics** ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
<b>Static</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	---	---	1.0	$\mu A$
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_{DS}=250\mu A$	1.0	1.6	2.5	V
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=10A$	---	8.5	10.5	m $\Omega$
		$V_{GS}=4.5V, I_D=8A$	---	12.5	16	
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=5A$	---	12	---	S
$V_{SD}$	Diode Forward Voltage	$I_{SD}=1A, V_{GS}=0V$	---	---	1.3	V
<b>Switching</b>						
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_D=5A$	---	8	---	nC
$Q_{gs}$	Gate-Source Charge		---	1.6	---	
$Q_{gd}$	Gate-Drain Charge		---	1.2	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=15V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	8.5	---	ns
$T_r$	Turn-on Rise Time		---	10	---	
$T_{d(off)}$	Turn-off Delay Time		---	14	---	
$T_f$	Turn-off Fall Time		---	10.6	---	
<b>Dynamic</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0\text{MHz}$	---	455	---	pF
$C_{oss}$	Output Capacitance		---	318	---	
$C_{rss}$	Reverse Transfer Capacitance		---	22	---	

**Note:**

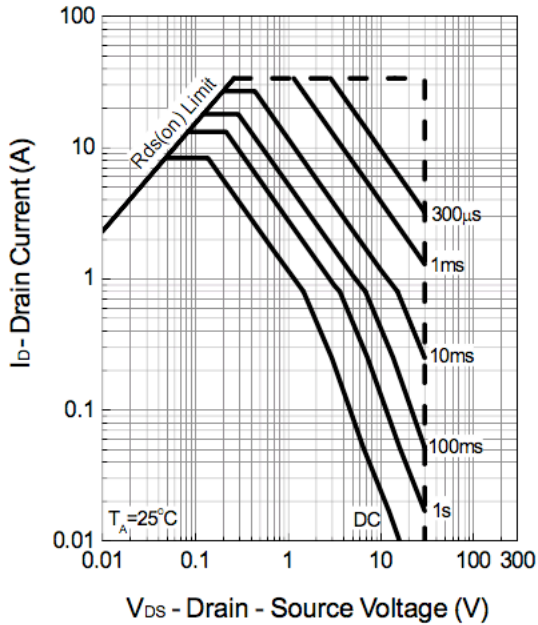
- The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The value in any given application depends on the user's specific board design.
- Repetitive rating, pulse width limited by junction temperature.
- The current rating is based on the  $t \leq 10\text{s}$  junction to ambient thermal resistance rating.

**Q1 Typical Characteristics**

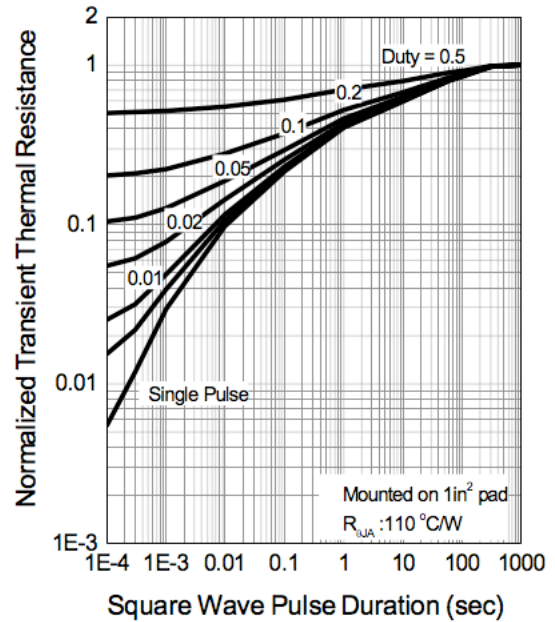


**Q1 Typical Characteristics (Cont.)**

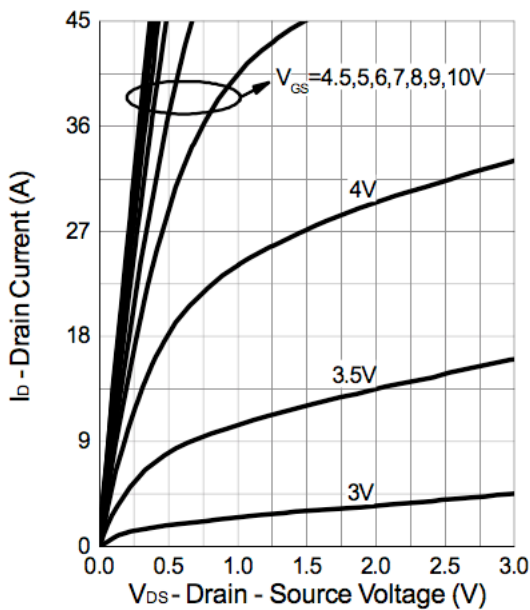
**Safe Operation Area**



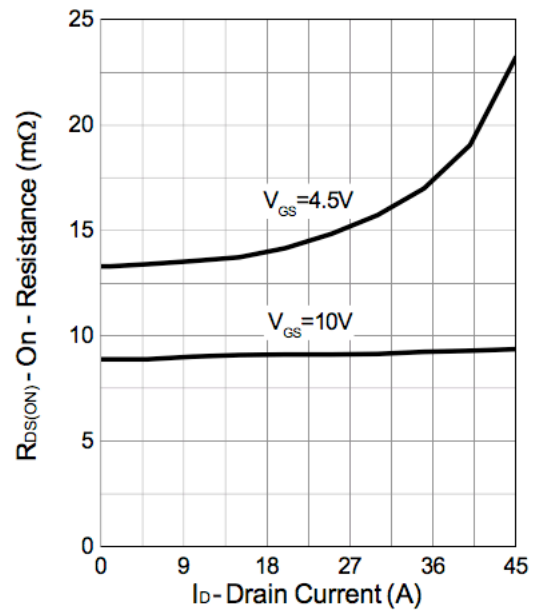
**Thermal Transient Impedance**



**Output Characteristics**

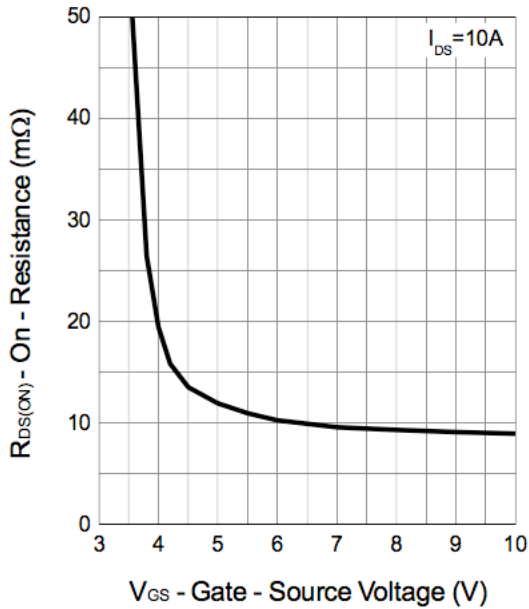


**Drain-Source On Resistance**

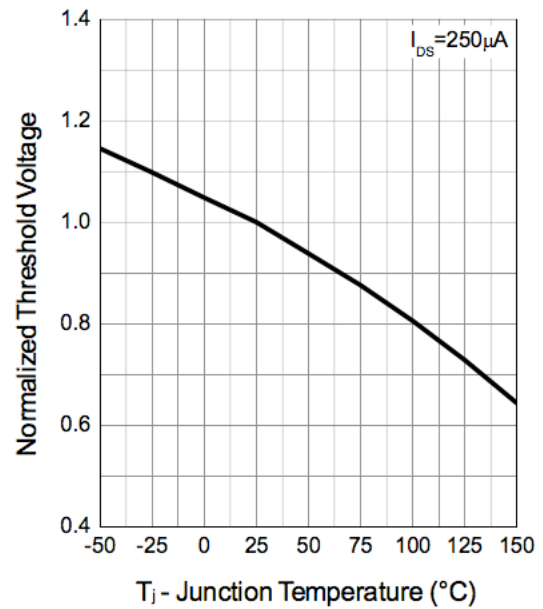


**Q1 Typical Characteristics (Cont.)**

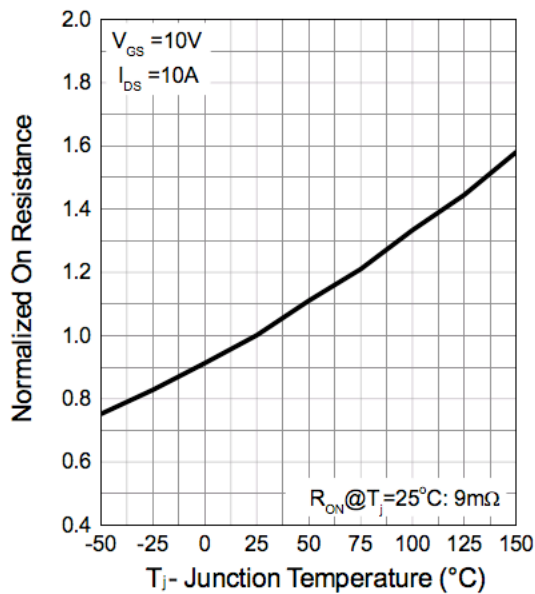
**Gate-Source On Resistance**



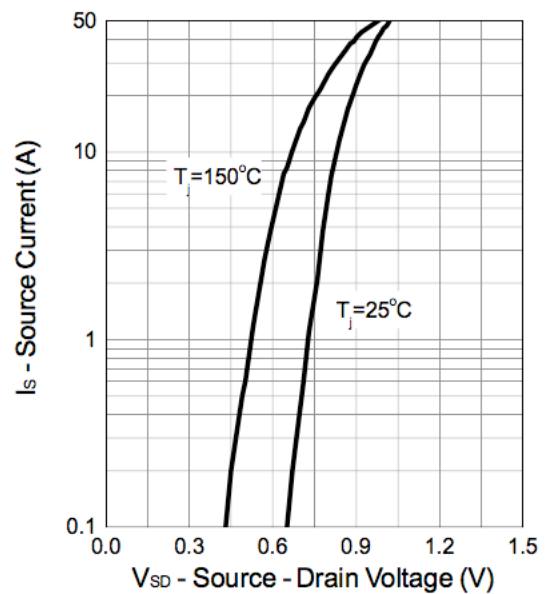
**Gate Threshold Voltage**



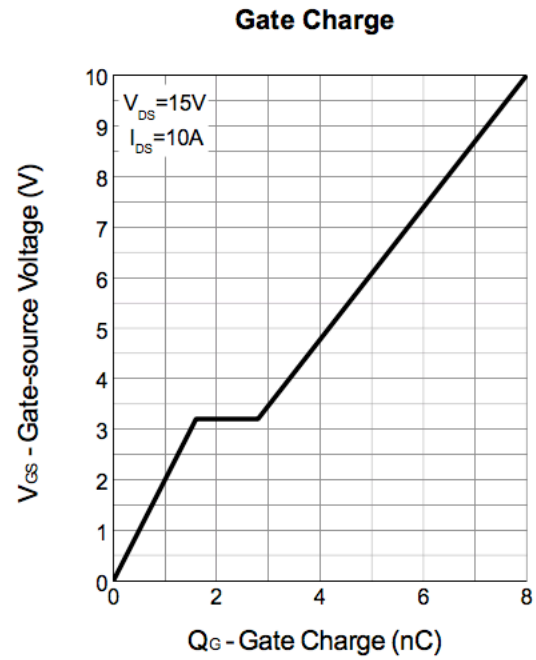
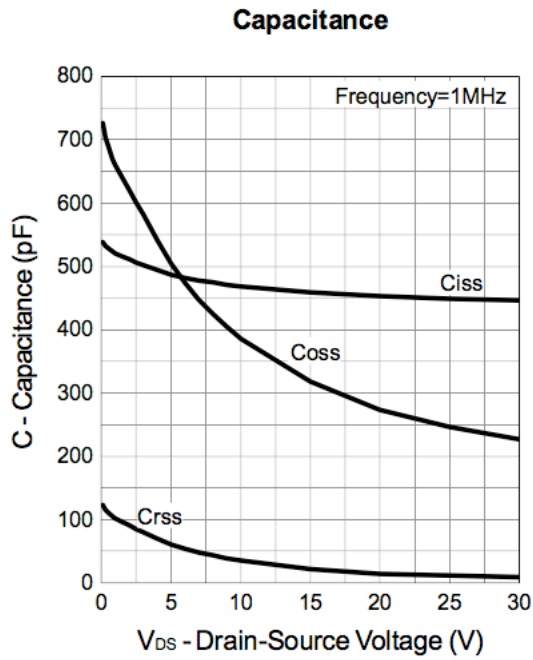
**Drain-Source On Resistance**



**Source-Drain Diode Forward**

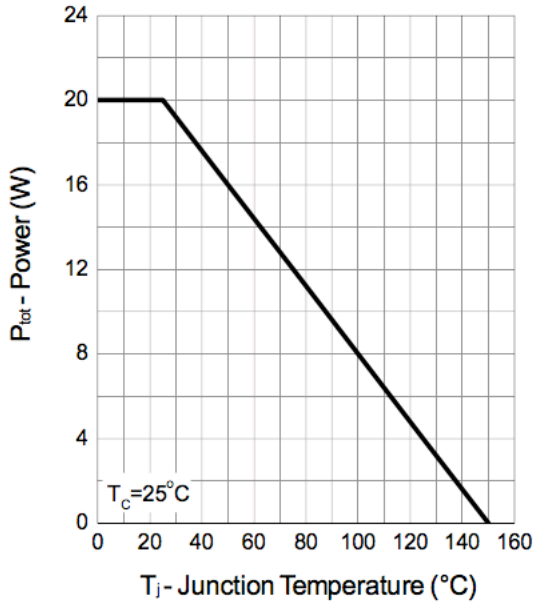


**Q1 Typical Characteristics (Cont.)**

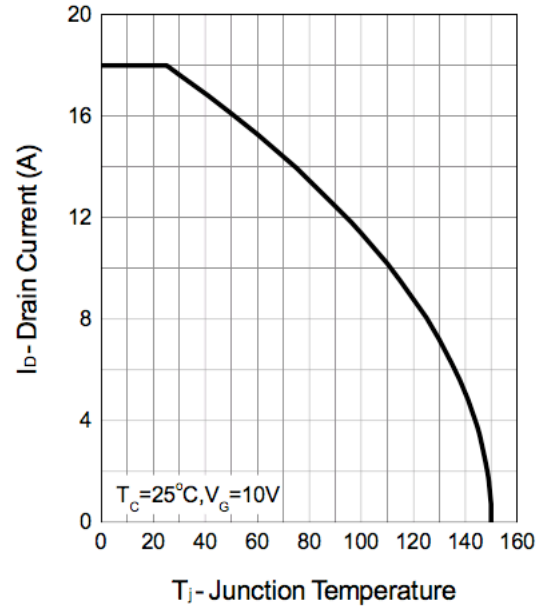


**Q2 Typical Characteristics**

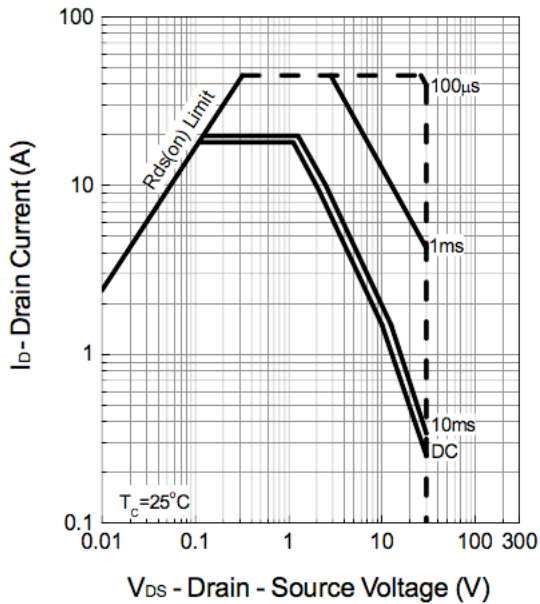
**Power Dissipation**



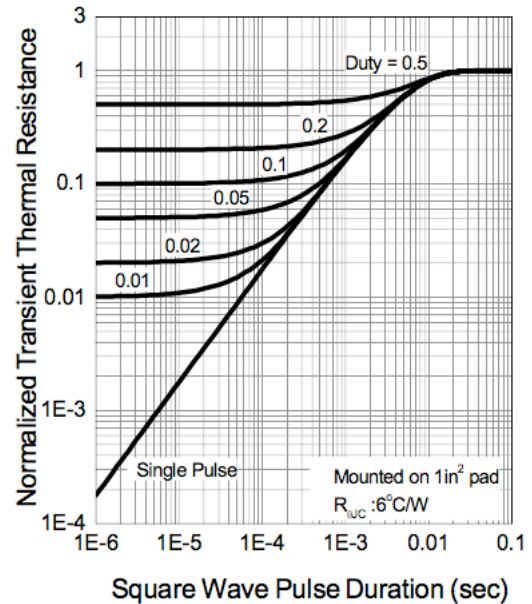
**Drain Current**



**Safe Operation Area**



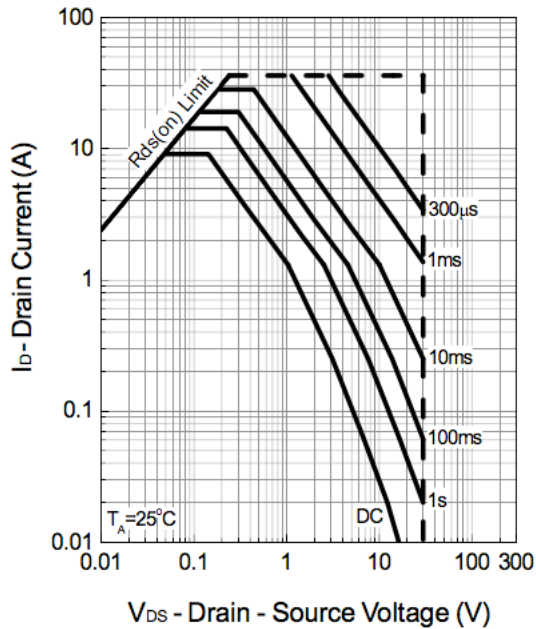
**Thermal Transient Impedance**



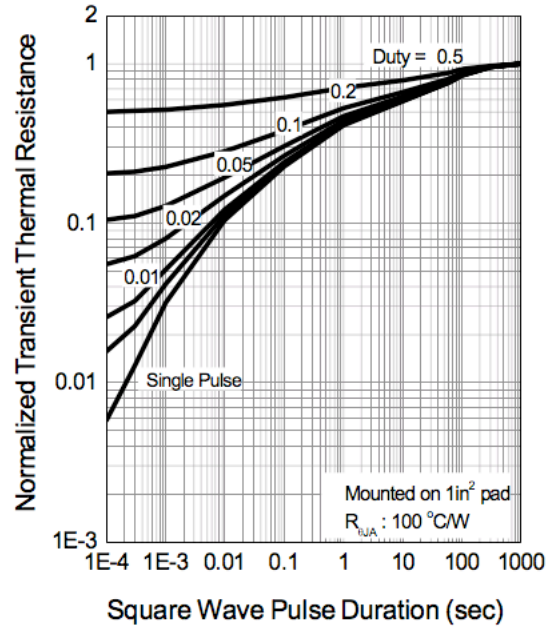


**Q2 Typical Characteristics (Cont.)**

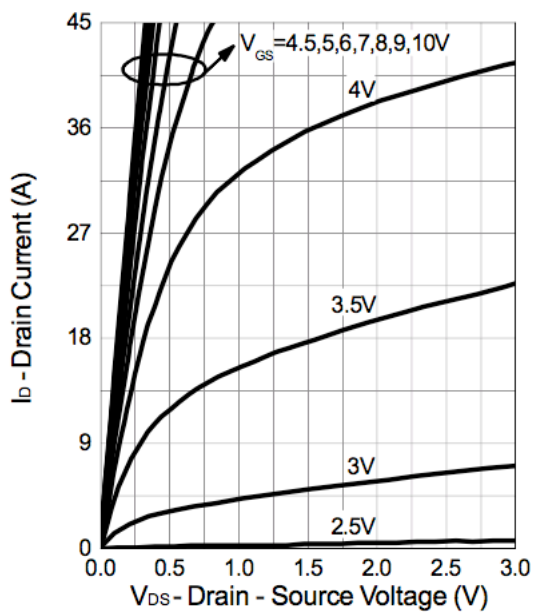
**Safe Operation Area**



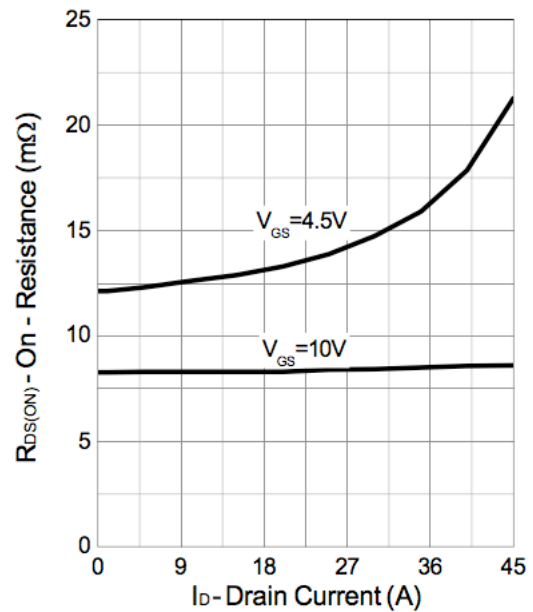
**Thermal Transient Impedance**



**Output Characteristics**

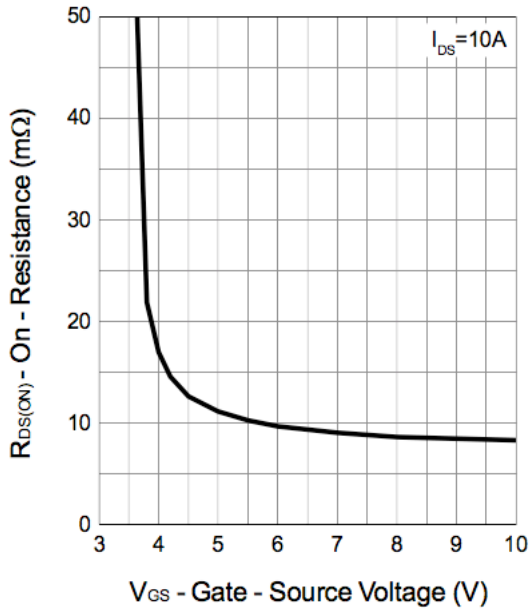


**Drain-Source On Resistance**

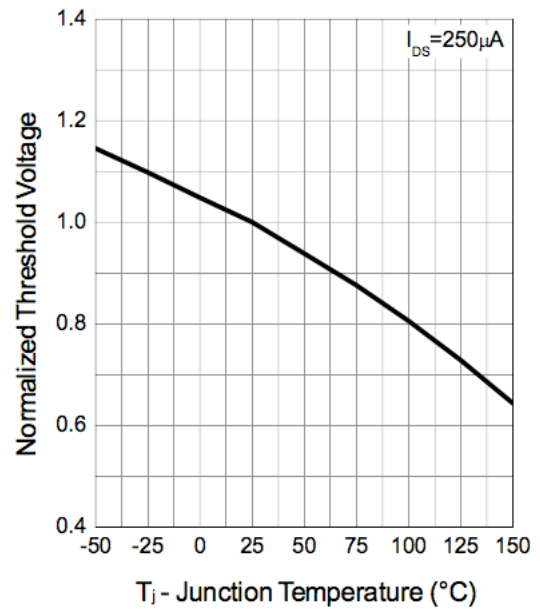


**Q2 Typical Characteristics (Cont.)**

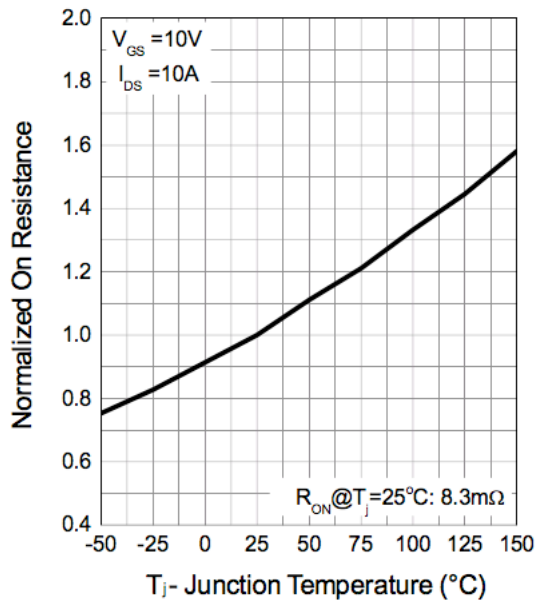
**Gate-Source On Resistance**



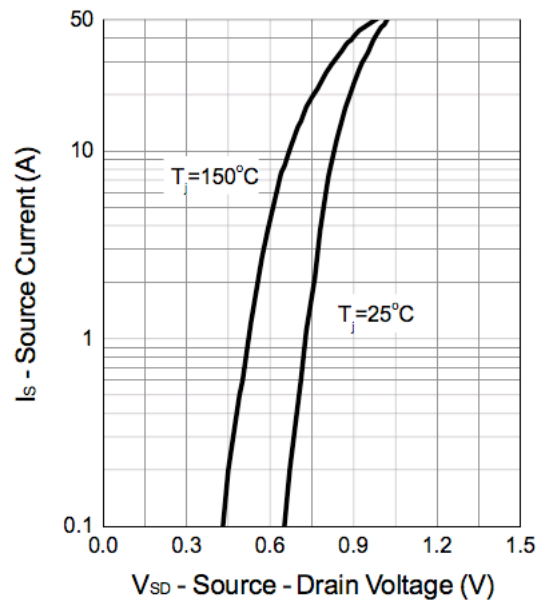
**Gate Threshold Voltage**



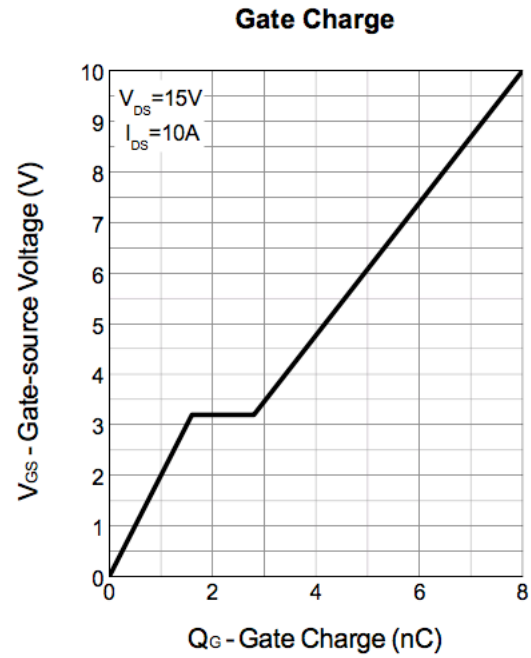
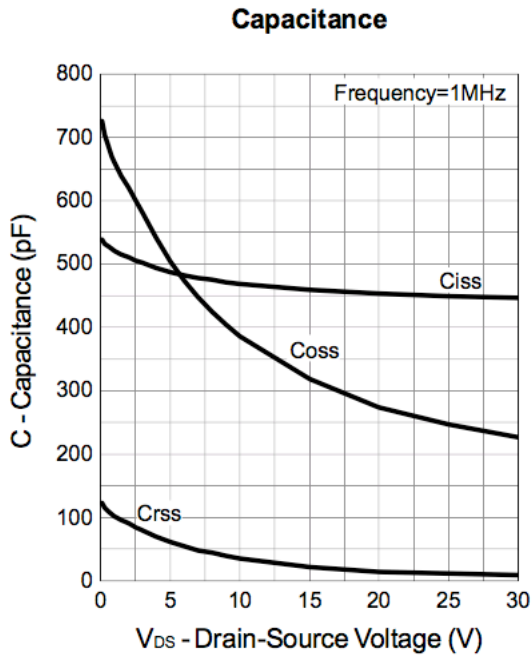
**Drain-Source On Resistance**



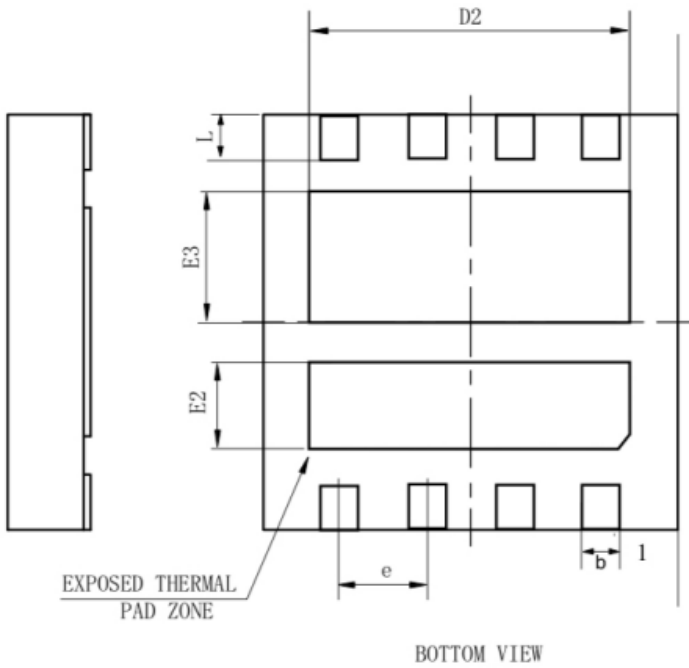
**Source-Drain Diode Forward**



**Q2 Typical Characteristics (Cont.)**



Packaging information



SYMBOL	MILLIMETER		
	MIN	<b>MD</b>	MAX
A	0.70	0.75	0.80
A1	—	0.02	0.05
b	0.25	0.30	0.35
c	0.18	0.20	0.25
D	3.20	3.30	3.40
D2	2.50	2.60	2.70
E	3.20	3.30	3.40
E2	0.65	0.70	0.75
E3	0.95	1.05	1.15
e	0.70 BSC		
L	0.30	0.35	0.40

## Attention

- 1, Any and all Winsok power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Winsok power representative nearest you before using any Winsok power products described or contained herein in such applications.
- 2, Winsok power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Winsok power products described or contained herein.
- 3, Specifications of any and all Winsok power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, Winsok power Semiconductor CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- 5, In the event that any or all Winsok power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Winsok power Semiconductor CO., LTD.
- 7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Winsok power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- 8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement,etc. When designing equipment, refer to the "Delivery Specification" for the Winsok power product that you intend to use.
- 9, this catalog provides information as of Sep.2014. Specifications and information herein are subject to change without notice.