

Description

Features

- Trench Power LV MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low RDS(ON)

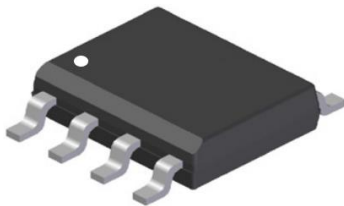
V_{DS}	-30	V
$R_{ds(on),typ}@V_{gs}=-10V$	12	mΩ
$R_{ds(on),typ}@V_{gs}=-4.5V$	17	mΩ
I_D	-10	A

Application

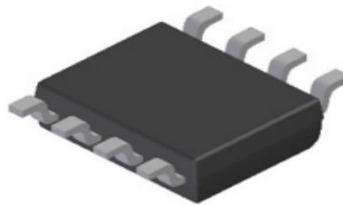
- High current load application
- Load switching
- Quick Charge



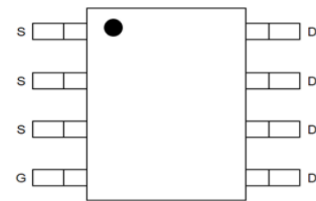
Top View



Bottom View



Pin Configuration



Package Marking and Ordering Information

Part	Marking	Package	Packing	Reel Size	Tape Width	Qty
CMT4435S	T4435S	SOP-8L	Reel	NA	NA	4000

Key Performance Parameters

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D@T_A=25^{\circ}C$	Continuous Drain Current, $V_{GS}=-10V$	-10	A
$I_D@T_A=100^{\circ}C$	Continuous Drain Current, $V_{GS}=-10V$	-6	A
IDM	Pulsed Drain Current ¹	-40	A
EAS	Single Pulse Avalanche Energy ⁴	64	mJ
$PD@T_A=25^{\circ}C$	Total Power Dissipation	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_j	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	70	82	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, Unless otherwise noted)

Symbol	Parameter	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
BVDSS	Drain-Source Breakdown Voltage	-30	--	--	V	$V_{GS}=0V$, $I_D=-250\mu A$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	--	12	16	$m\Omega$	$V_{GS}=-10V$, $I_D=-10A$
		--	17	21		$V_{GS}=-4.5V$, $I_D=-5A$
$V_{GS(th)}$	Gate Threshold Voltage	-1	-1.6	-2.5	V	$V_{GS}=V_{DS}$, $I_D=-250\mu A$
IDSS	Drain-Source Leakage Current	--	--	1	μA	$V_{DS}=-30V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$
		--	--	10	μA	$V_{DS}=-24V$, $V_{GS}=0V$, $T_J=125^{\circ}\text{C}$
IGSS	Gate-Source Leakage Current	--	--	± 100	nA	$V_{GS}=\pm 20V$, $V_{DS}=0V$
Qg	Total Gate Charge ^{2,3}	--	42	--	nC	$V_{DS}=-15V$, $V_{GS}=-10V$, $I_D=-10A$
Qgs	Gate-Source Charge ^{2,3}	--	8	--		
Qgd	Gate-Drain Charge ^{2,3}	--	9	--		
$T_{d(on)}$	Turn-On Delay Time ^{2,3}	--	7	--	ns	$V_{DD}=-15V$, $V_{GS}=-10V$, $R_G=3\Omega$ $I_D=-10A$
T_r	Rise Time ^{2,3}	--	3	--		
$T_{d(off)}$	Turn-Off Delay Time ^{2,3}	--	92	--		
T_f	Fall Time ^{2,3}	--	51	--		
Ciss	Input Capacitance	--	2250	--	pF	$V_{DS}=-15V$, $V_{GS}=0V$, $f=1\text{MHz}$
Coss	Output Capacitance	--	300	--		
Crss	Reverse Transfer Capacitance	--	225	--		

Diode Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	--	--	-11	A
I_{sm}	Pulsed Source Current		--	--	-44	A
T_{rr}	Body Diode Reverse Recovery Time	$I_F=-10A$, $di/dt=100A/\mu s$	--	15	--	ns
Qrr	Body Diode Reverse Recovery Charge		--	7	--	nC
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=-10A$, $T_J=25^{\circ}\text{C}$	--	--	-1.2	V

Note:

1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.
4. The EAS data shows Max. rating. The test condition is $V_{DD}=-15V$, $V_{GS}=-10V$, $L=0.5mH$.

Typical Performance Characteristics

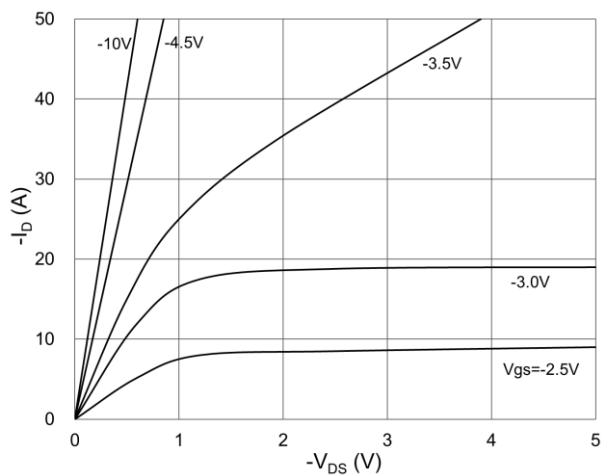
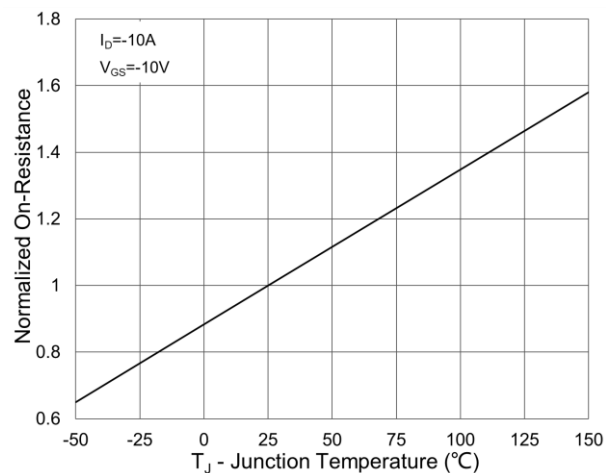
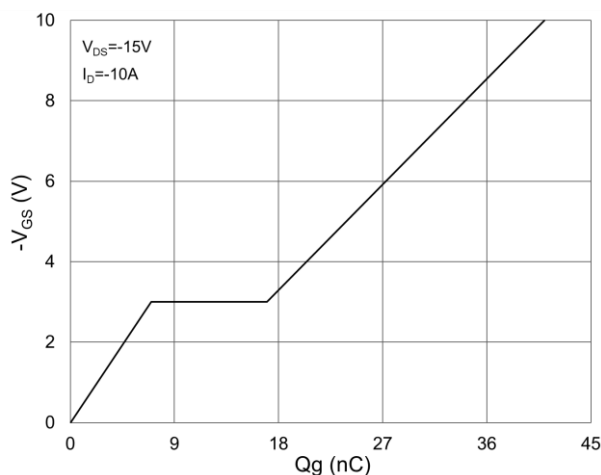
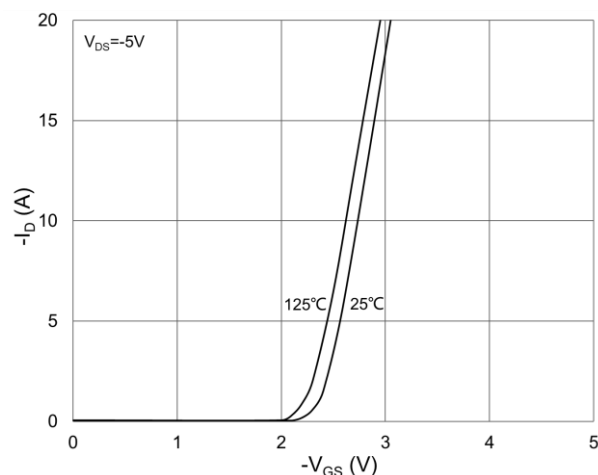
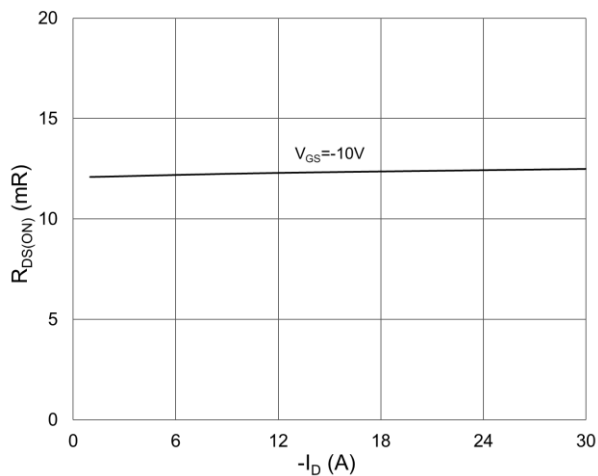
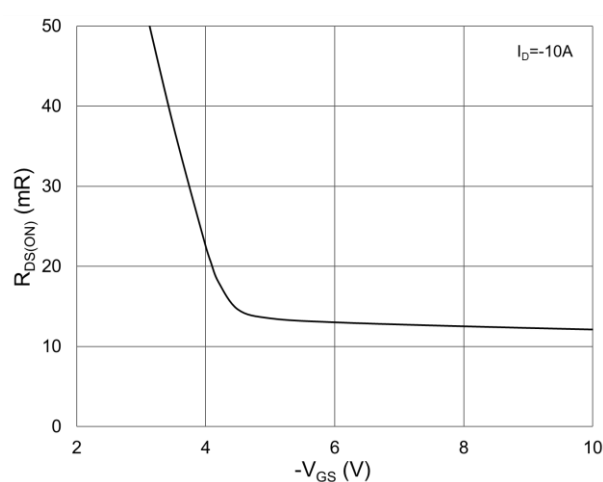
Fig1 Output Characteristics

Fig2 Normalized $R_{DS(on)}$ vs. T_J

Fig3 Gate Charge Waveform

Fig4 Transfer Characteristics

Fig5 $R_{DS(on)}$ vs. Drain Current and Gate Voltage

Fig6 $R_{DS(on)}$ vs. Gate Voltage


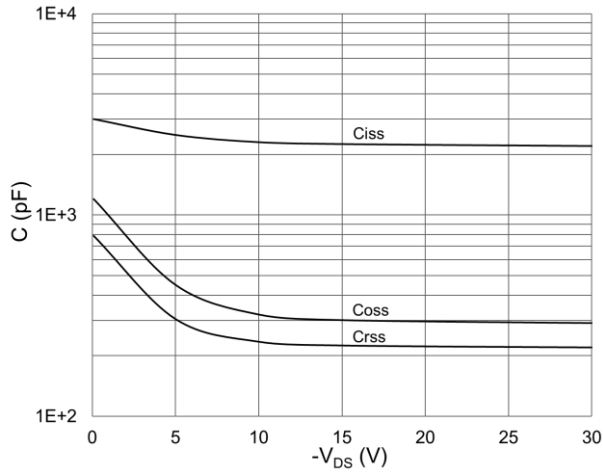
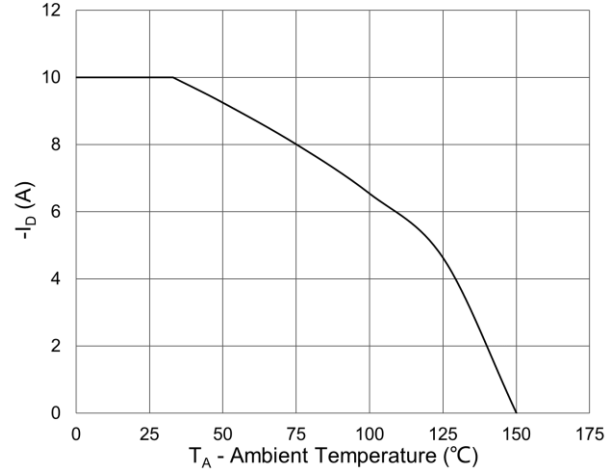
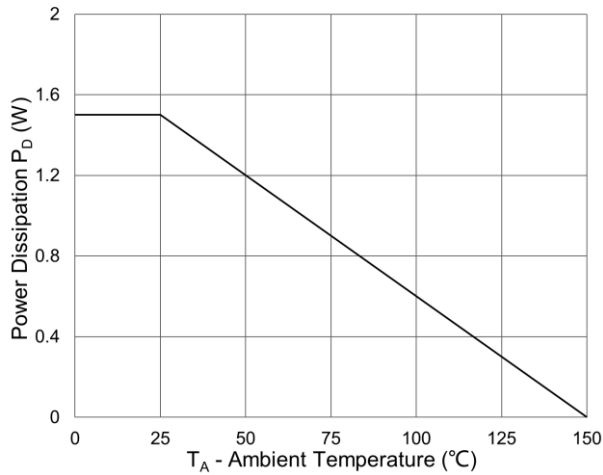
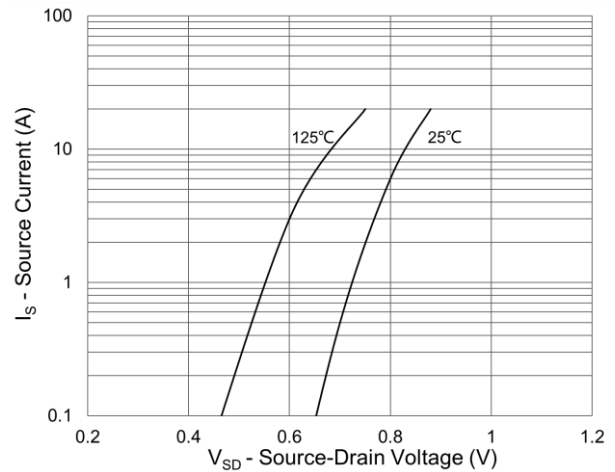
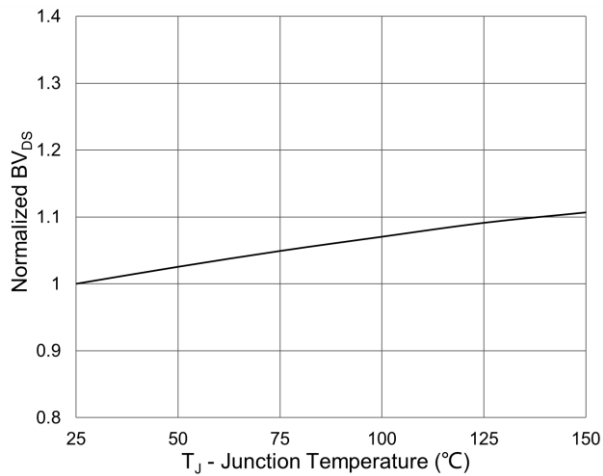
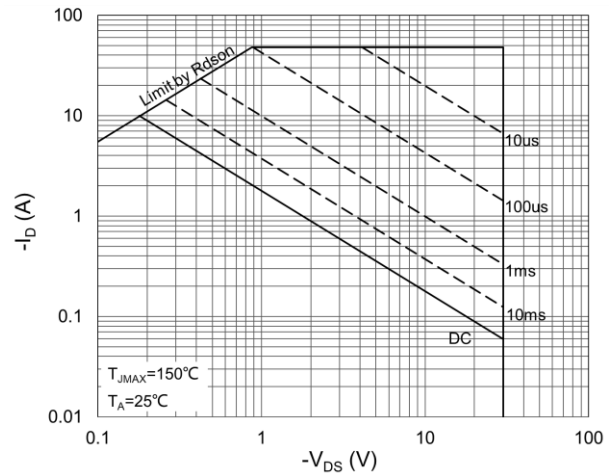
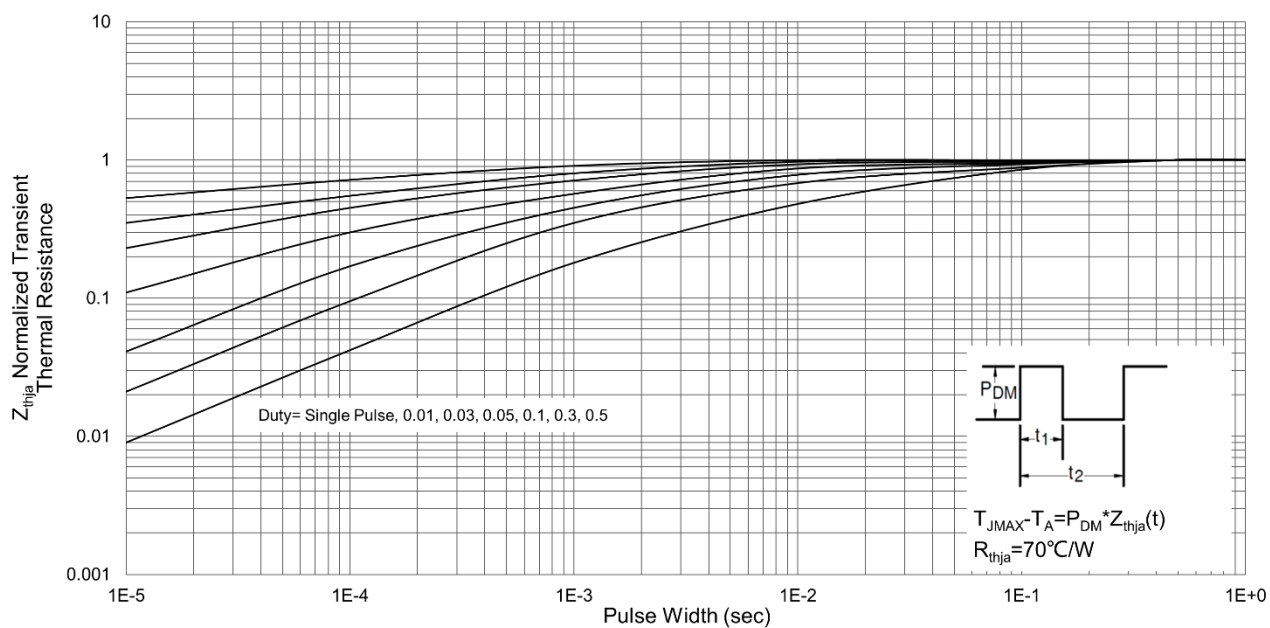
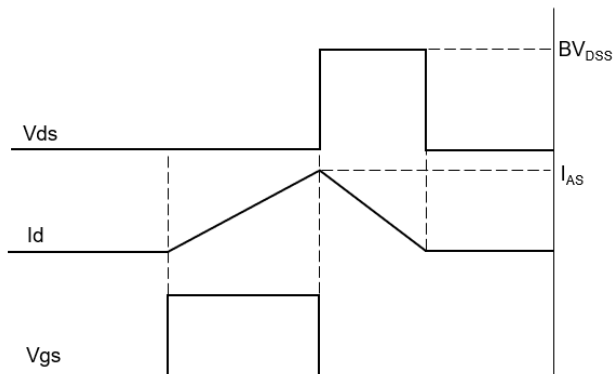
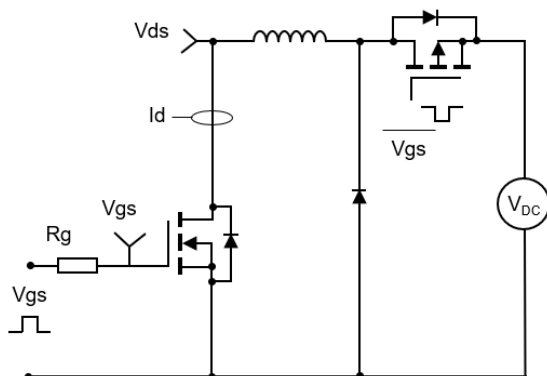
Fig7 Capacitance Characteristics

Fig8 Drain Current Derating

Fig9 Power Dissipation

Fig10 Source-Drain Diode Forward Characteristics

Fig11 Normalized Breakdown Voltage vs. T_J

Fig12 Maximum Safe Operation Area


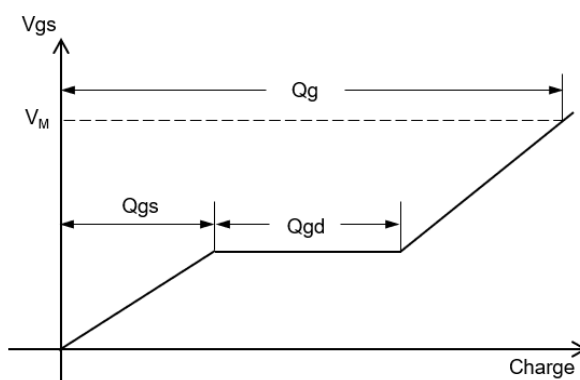
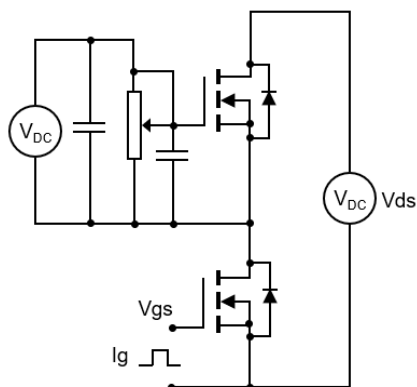
Fig13 Normalized Transient Impedance


Test Circuit & Waveform

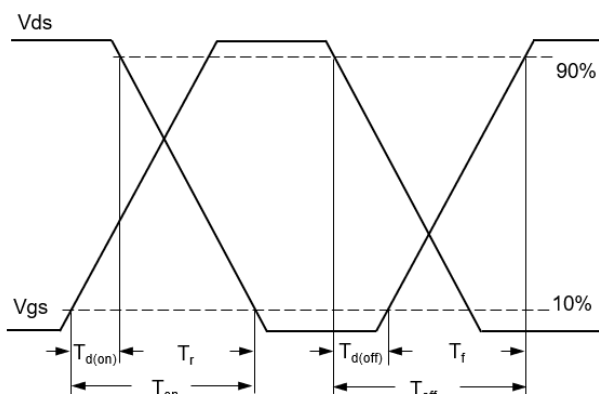
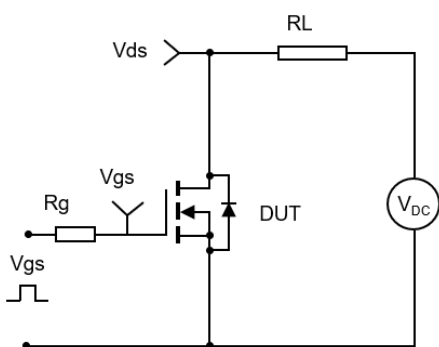
1. Unclamped Inductive Switching Test Circuit & Waveform



2. Gate Charge Test Circuit & Waveform

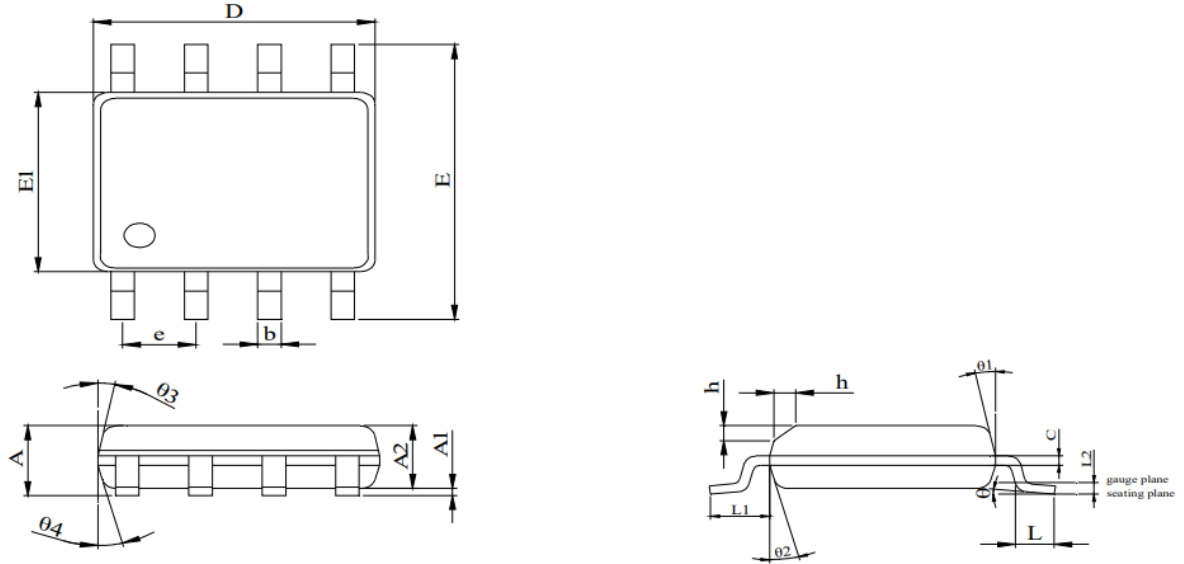


3. Resistive Switching Test Circuit & Waveform



SOP-8L Package Information

Package Outline



DIM	MILLIMETER		
	MIN.	NOM.	MAX.
A	1.35	1.50	1.65
A1	0.05	0.10	0.15
A2	1.35	1.40	1.50
b	0.38	--	0.50
c	0.17	--	0.25
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27(BSC)		
L	0.45	0.60	0.80
L1	1.04 REF		
L2	0.25 BSC		
h	0.30	0.40	0.50
θ	0°	--	8°
θ_1	10°	12°	14°
θ_2	8°	10°	12°
θ_3	10°	12°	14°
θ_4	8°	10°	12°

Recommend Soldering Footprint

