SMTC1510A Wire Wound Molded SMD Power Inductors











- 大电流、低DCR、高效率
- 磁屏蔽型抗电磁干扰强适用于高密度安装
- 高可靠性,通过采用T-core成型结构享有卓越 的抗震动性能
- 极低的直流电阻和超低的交流损耗,适用于高 开关频率
- 符合 RoHS, 无卤和 REACH
- 符合 AEC-Q200

◆用途:

- PDA,笔记本,台式机,服务器应用程序
- 大电流 POL 转换器
- 电池供电设备、基站
- 分布式电源系统中的 DC/DC 转换器

◆环境:

工作温度: -55℃ 至+155℃(包括线圈自身温升)

◆试验设备:

- 电感值: WK3260B 或同等仪器
- 电流: WK3260B+WK3265B 或同等仪器
- 直流电阻: Chroma 16502 或同等仪器

◆产品型号:

1	
	,

SMTC

<u>1510</u> ② <u>A</u>

Features: • High curr

- High current, low DCR, high efficiency
- Magnetic shielding type, strong anti- electromagnetic
 Interference, suitable for high- density installation
- High reliability, excellent vibration resistance through the use of T-core molding structure
- Extremely low DCR and ultra low AC losses for high switching frequencies
- RoHS, Halogen Free and REACH Compliance
- AEC-Q200 Compliant

Applications:

- PDA , notebook ,desktop ,server applications
- High current POL converters
- Battery powered devices . Base station
- DC/DC converters in distributed power systems

Environmental Data:

• Operating Temperature: -55°C to +155°C (Including coils self-temperature rise)

Test Equipment:

- L: WK3260B LCR meter or equivalent
- Isat & Irms: WK3260B+WK3265B or equivalent
- DCR:Chroma 16502 or equivalent

Product Identification:

<u>100</u> <u>4</u>)

<u>IVI</u> (5) $\frac{1}{6}$

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	类型 Type
	T-CORE 结构成型贴片功率电感
SMTC	T-core
SIVITO	molding Structure

Power Inductors

外形尺寸(L×W×H) (mm)
External Dimensions (L×W×H)
(mm)

1510
16.5×15.5×9.7

3

Automotive

4

Inductance

10 uH

(5)

公差 Inductance Tolerance
J:±5%,K: ±10%, L: ±15%

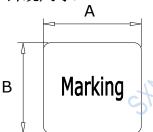
M: ±20%,P: ±25%, N: ±30%

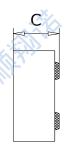
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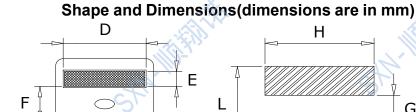
包装 Packing			
В	散装Bulk Package		
Т	编带Tape & Reel		

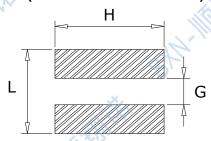


◆外观尺寸:









Recommended Land Pattern

Part No		. *	2,	ITEM		-××		Unit:m	nm
T dit 140	Α	В	С	D	E		L	Н	G -
SMTC1510A	16.5±0.3	15.5±0.3	9.7±0.3	13.2±0.5	3.2±0.3	7.0±0.3	15.0Ref	15.0Ref	6.0Ref

▶规格特性:

Specifications:

SMTC1510A Series Electrical Characteristics (Electrical specifications at 25[°]C)

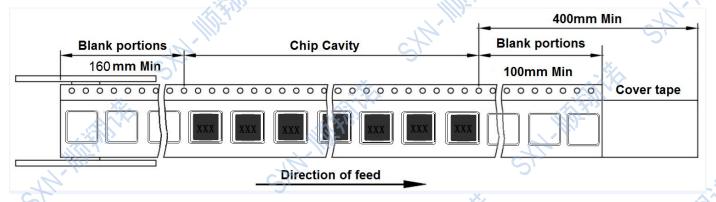
Dow No.	Inductance 100KHz /0.1V		DCR (mΩ)		Current(A)	\(\frac{1}{2}\);	erature urrent(A)
Part No	L(μH) '@0A	Tol	Max	Max	Typical	20℃ Rise	40℃ Rise
SMTC1510A-1R0M	1.0	±20%	1.12	80.0	85.0	54.0	63.0
SMTC1510A-4R7M	4.7	±20%	3.8	39.0	43.0	22.0	30.0
SMTC1510A-5R6M	5.6	±20%	4.2	34.0	38	21.0	28.0
SMTC1510A-6R8M	6.8	±20%	4.6	31.0	36.0	20.0	26.0
SMTC1510A-8R2M	8.2	±20%	7.2	28.0	32.0	19.0	25.0
SMTC1510A-100M	10	±20%	8.6	26.0	29.0	-18.0	24.0
SMTC1510A-150M	15	±20%	11.5	18.0	20.0	14.0	18.0
SMTC1510A-220M	22	±20%	15.8	16.0	18.0	10.5	14.2
SMTC1510A-330M	33	±20%	20	14.0	16.7	8.6	12.3

- Heating Rating Current (Irms) will cause the coil temperature rise of 40 °C approximately (△ t).
- Saturation Current (Isat) will cause L0 to drop 30% approximately.
- Special inquiries besides the above common used types can be met on your requirement.
- The part temperature (ambient + temp rise) should not exceed 125℃ under the worst case operating condition. Circuit design, component, PCB trace size and thickness airflow and other cooling provisions all could affect the part temperature. Part temperature should be verified in the end application.

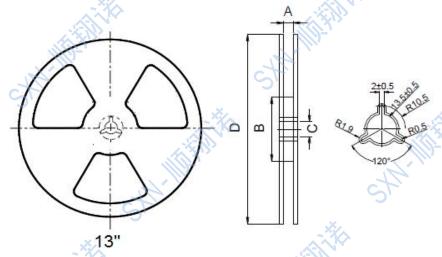


◆产品包装: Packaging:

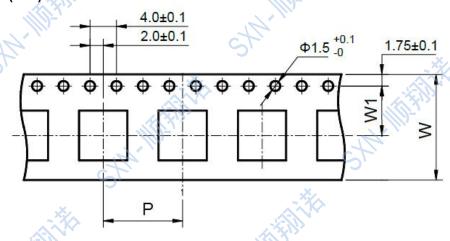
• Tape and Reel Spec ifications: (Dimensions are in mm)



• Reel dimensions (mm):



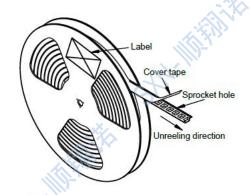
●Tape Dimension (mm):



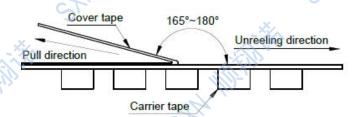
Part No.	Tape Dimension			Reel Dimensions				REEL	Inside	Outside
Fait No.	W	Р	W1	Α	В	2,C	D	(PCS)	Box(PCS)	Carton(PCS)
SMTC1510A	32 ±0.3	24 ±0.1	14.2 ±0.1	32.0±0.5	97±0.5	13.0±0.2	330±2.0	150	300	1200



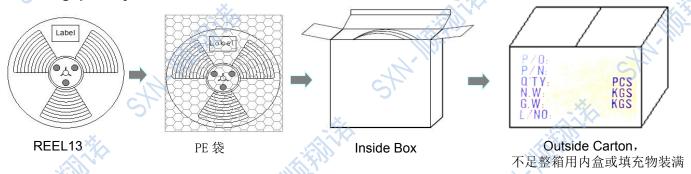
• Cover tape peel off condition



- a) Cover tape peel force shall be 10 to 120g
- b) Noodle strip peeling angle165° to 180°



Packing quantity:



T-core molding Structure Power Inductors ◆可靠性测试: Reliability Testin



Reliability Testing:

<u> </u>	*/	Renability resting:
Items	Requirements	Test Methods and Remarks
	1. Pulling test:	Solder the inductor to the testing jig using leadfree
	Define: A: sectional area of terminal	solder. Then apply a force in the
Terminal Strength	A≦8mm2 force≧5N time:30sec	Keep time: 10±1s Speed: 1.0mm/s.
Reference docu	8mm2 <a 10n="" 10sec<="" 20mm2="" force="" td="" time:="" ≤="" ≥=""><td></td>	
ments: GB/T	20mm2 <a 10sec<="" force="" td="" time:="" ≥20n=""><td>163</td>	163
2423.60-2008	2.Solder paste thickness:0.12mm	Jan
端子強度(SMT)	3.Meet the above requirements without any	
	loose terminal	ST
CH	1 Torminal diameter(d) mm 0.25 / d <	Dull Farewith a farea aball ha analiced anadyally to
3,	1.Terminal diameter(d) mm 0.35 <d< td=""><td>Pull Force:the force shall be applied gradually to</td></d<>	Pull Force:the force shall be applied gradually to
		the terminal and thenmaintained for 10 seconds.
erminal Strength	10sec2.Terminal diameter(d) mm0.50 < d	110-
Reference docu	0.80Applied force:10N Duration:	St
ments: GB/T	10sec3.Terminal diameter(d) mm0.80 < d	F F
2423.60-2008	1.25Applied force:20N Duration:	Pulling test
端子強度(DIP)×	10sec4.Terminal diameter(d) mmD>	The state of the s
	1.25Applied force:40N Duration:	
	10sec5.Meet the above requirements	A. Illi
	without any loose terminal.	ct e
47,	1.No visible mechanical damage.	1.Solder the inductor to the test jig (glass epoxy
2,	.x.	board
		2.shown in Using a leadfree solder. Then apply a
		force in the direction shown
Resistance to Flexure	110,	3.Flexure: 2mm.
JIS C 5321:1997	st'	4.Pressurizing Speed: 0.5mm/sec.
抗弯曲性试验		5.Keep time: 30 sec.
		20 H10
~-XX		R230
	(A) III	45[1.772] 45[1.772] Flexure
4.	St	49[.712] 49[.712] 43
Dropping	1.No case deformation or change	1.Drop the packaged products from 1m high in 1
	inappearance.	
Reference documents:	2.No short and no open.	angle, 3 ridges and 6surfaces, twice in each
GB/T 2423.7-2018	19-1	direction.
落下試驗	's st	<u> </u>
Coldorability	1.No visible mechanical damage.	1.Solder temperture:240 ± 2 ℃
Solderability	- 3	2.Duration: 3 sec.
Reference documents:	32,703	3. Solder: Sn/3.0Ag/0.5Cu.
GB/T 2423.28-2005	or reminded made have 60 /6 minimum delacti	4.Flux: 25% Resin and 75% ethanol in weight
可焊性试验	Soverage 1	Solitaria 1070 Gallarior in Weight
	ST	_



Items	Requirements	Test Methods and Remarks				
	1.No visible mechanical damage.	1.Solder the inductor to the testing jig (glass epoxy				
	2. Inductance change: Within ±10%.	boardshown in) using leadfree solder.				
	3.Q factor change: Within ±20%.	2.The inductor shall be subjected to a simple				
	Cu pad Solder mask	harmonic motion having total amplitude of 1.5mm,				
		the frequency being varieduniformly between the				
		approximate limits of 10 and 55 Hz.				
Vibration		3.The frequency range from 10 to 55 Hz and				
Reference documents:		return to 10 Hz shallbe traversed in approximately				
GB/T 2423.10-2019	Glass Epoxy Board	1 minute. This motion shall be applied for a period				
振動試验		of 2 hours in each 3mutually perpendicular				
	The state of the s	directions(total of 6 hours).				
	1,1119	Freq				
	He S.	55Hz \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \				
	-×.					
		10Hz				
	<u> </u>	0 1Min Time				
	1.No visible mechanical damage.	1.Start at (85~125℃) for T time, rush to				
THY	2. Inductance change: Within ±10%.(Mn-Zn:	(-55~40℃) for T time as one cycle, go through100				
Sr	Within ≦30%)	cycles.				
Thermal Shock	3.Q factor change: Within ±20%.	2.Transforming interval: Max. 20 sec.				
Reference documents:	(A) The second of the second o	3.Tested cycle: 100 cycles.				
GB/T 2423.22-2012	1/1/2	4.The chip shall be stabilized at normal condition				
Method Na	51	for 1~2 hours				
冷热冲击试验	X /.	125°C/85°C 30 min. 30 min.				
4.4 X.4.4. FT PM-977		Ambient				
		Temperature 30 min.				
		20sec. (max.)				
4. Illi	etta	ジ				
	1.No visible mechanical damage.	1.Temperature:M(-55~-40±2℃)				
	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours				
	Within ≦30%)	3.The chip shall be stabilized at normal condition for				
Low temperature Storage	3.Q factor change: Within ±20%.	1~2 hoursbefore measuring.				
Reference documents:	st'	Room				
GB/T 2423.1-2008	.36 .	Temp OSH / Test				
Method Ab		0 96H / Test 97H 98H Time				
低温储存试验		M°C				
	111111111111111111111111111111111111111	Temp Low temperature				
	the state of the s	51				

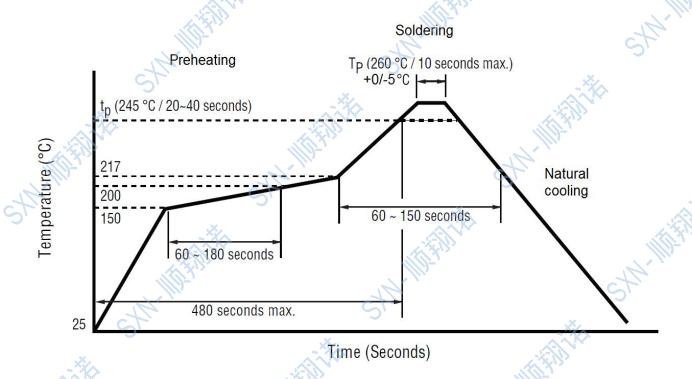


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Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	1.Temperature:N(125~85±2℃).
High temperature	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours
Storage	Within ≦30%)	3.The chip shall be stabilized at normal condition
Reference documents:	3.Q factor change: Within ±20%.	for 1~2 hoursbefore measuring.
GB/T 2423.2-2008		Temp High temperature
Method Bb		N'C
高温储存试验	A. III	Room Temp
1月111111111111111111111111111111111111	St	0 96H 97H 98H Time
(2)	No visible mechanical damage.	1.Temperature: 60±2°C
	2. Inductance change: Within ±10%.(Mn-Zn:	
Damp Heat	Within ≦ 30%)	3.Duration: 96±2 hours.
(Steady States)	3.Q factor change: Within ±20%.	4.The chip shall be stabilized at normal condition
	5.0 factor change. Within 120%.	·
Reference documents:)'	for 1~2 hoursbefore measuring.
GB/T 2423.3-2016		60°C Temp & Humidity 93%RH High temperature
恒定湿热试验		Room Conditions
		Test
	12	0 96H 97H 98H Time
Heat endurance of	1.No significant defects in appearance.	1.Refer to the above reflow curve and go through
_	2. ∆ L/L ≦ 10% (Mn-Zn: ∆ L/L ≦ 30%)	the reflow for twice.
	3. ∆ Q/Q≦30% (SMD series only)	2.The peak temperature : 260+0/-5℃
GJB 360B-2009	4. △ DCR/DCR≦10%	72,
回流焊耐热性试验		51
42	No case deformation or change in	To dip parts into IPA solvent for 5±0.5Min,then
	appearance or obliteration of marking	drying them at room temp for 5Min,at last ,to
test Reference documents:		brushing making 10 times.
IEC 68-2-45:1993		
耐溶剂性试验		c the
11 11 11 11 11 11 11 11 11 11 11 11 11	ath	×,
Overload test	1.During the test no smoke, no peculiar,	-%
Reference documents:	smell, no fire	
JIS C5311-6.13	2.The characteristic is normal after test	Apply twice as rated current for 5 minutes.
过负荷试验	$\mathcal{D}_{\Sigma_{\lambda}}$	att
192	St	-5
	1.During the test no breakdown	
X,	2.The characteristic is normal after test	For parts with two coils
MIL-STD-202G Method		2. DC1000V, Current: 1mA, Time: 1Min.
301		3. Refer to catalogue of specific products
绝缘耐压测试	TH,	St
	GT.	



◆推荐回流焊温度曲线

Recommended reflow soldering curve:



The recommended reflow conditions as above graph, is set according to our soldering equipment. DUE to various manufactures may have different reflow soldering equipment, products, process conditions, set methods. And so on, when setting the reflow conditions, Please adjust and confirm according to users' environment/equipment.



使用注意事项

REMINDERS FOR USING THESE PRODUCTS



● 保存时间为12 个月以内,保存条件(温度5~40°C以下、湿度35 ~ 66%RH 以下),需充分注意。若超过保存时间,端子电极的可焊性将可能老化。

The storage period is within 12 months. Be sure to follow the storage conditions (temperature: 5~40°C, humidity: 35 to 65% RH or less). If the storage period elapses, the soldering of the terminal electrodes may deteriorate.

• 请勿在气体腐蚀环境(盐、酸、碱等)下使用和保存。

Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).

• 手上的油脂会导致可焊性降低,应避免用手直接接触端子。

Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering Always ensure optimum conditions for soldering.

• 请小心轻拿轻放,避免由于产品的跌落或取出不当而导致的损坏。

Please always handle products carefully to prevent any damage caused bydropping down or inappropriate removing.

• 端子过度弯曲会导致断线,请不要过度弯曲端子。

Don't bend the terminals with excessive stress in case of any wire fracture.

• 不要清洗产品,如需要清洗时请联系我司。

Don't rinse coils by yourself and please contact SXN if necessary.

• 请勿将本产品靠近磁铁或带有磁力的物体

Don't expose the products to magnets or magnetic fields

- 在实施焊接前,请务必进行预热。预热温度与焊接温度及芯片温度的温度差要在150°C 以内。 Before soldering, be sure to preheat components. The preheating temperature should be set so that the temperature difference between the solder temperature and chip temperature does not exceed 150°C.
- 安装后的焊接修正应在规格书规定的条件范围内。若加热过度可能导致短路、性能降低、寿命减少。
 Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
- 装置会因通电而自我发热(温度上升),因此在热设计方面需留有充分余地。
 Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
- 非磁屏蔽型在基板设计时需注意配置线圈,受到电磁干扰可能会导致误动作。
 Carefully lay out the coil for the circuit board design of the non-magnetic shield type. A malfunction may occur due to magnetic interference.