

HX13053-S CAN Bus Transceiver

General Description

The HX13053-S serves as the intermediary between the Controller Area Network (CAN) protocol controller and the physical bus, primarily designed for high-speed applications in passenger cars, with a maximum speed of up to 1 MBaud. It facilitates differential transmission to the bus and differential reception for the CAN controller.



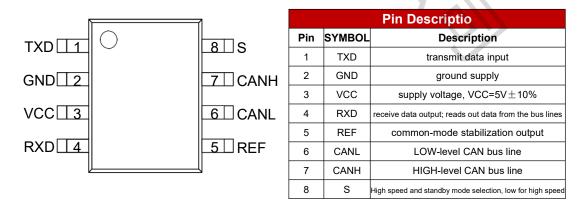
Features

- Operating voltage range: VCC = $5V \pm 10\%$
- Fully compliant with the ISO 11898 standard
- High-speed operation (up to 1 MBaud)
- Transceiver disengages from the bus in unpowered state (zero load)
- At least 110 nodes can be connected
- Very low-current standby mode with remote wake-up capability via the bus
- Differential receiver with high common-mode range for ElectroMagnetic Immunity (EMI)
- Transmit Data (TXD) dominant time-out function
- Bus pins protected against transients in automotive environments
- Thermally stabilized

Applications

- Automotive electronics
- Security systems
- Industrial control automation
- Intelligent instrumentation
- Building automation systems
- Road traffic control automation
 - Point-to-point and point-to-multipoint communications
 - Level converter
 - Serial servers

PIN CONFIGURATIONS AND FUNCTIONS



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Absolute Maximum Rating							
SYMBOL	PARAMETER	MIN	MAX	UNIT			
VCC	Supply voltage range	4.75	5.25	V			
TXD, RXD, S	MCU Side Port	-0.3	VCC+0.3	V			
CANL, CANH, REF	Bus-side port voltage	-60	+60	V			
Vtr	Pin 6, 7 Transient Voltage	-200	+200	V			
	storage temperature	-55	150	°C			
	ambient temperature	-40	85	°C			
	Welding temperature range		300	°C			
SOP8	Continuous power consumption		400	mW			

SPECIFICATIONS

(VCC=5V±10%, Temp=TMIN~TMAX, typical values at VCC=+5V, Temp=25°C, unless otherwise noted)

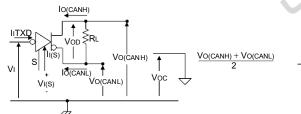
Bus Transmitter DC Characteristics								
SYMBOL	PARAMETER	TESTCONDITIONS	MIN	MAX	UNIT	SYMBOL		
VOH(D)	CANH output voltage (dominant)	VI=0V, S=0V, RL=60Ω	2.9	3.4	4.5			
VOL(D)	CANL output voltage (dominant)	F1、2	0.8		1.5			
VO(R)	Bus Output Differential Voltage(implicit)	VI=3V,S=0V, RL=60Ω, F1、2	2	2.5	3	V		
VOD(D)	Bus Output Differential Voltage (dominant)	VI=0V,S=0V, RL=60Ω, F1、2	1.5		3	V		
	Bus Output Differential Voltage	VI=3V, S=0V, F1、 2	-0.012		0.012	V		
VOD(R)	(implicit)	VI=3V, S=0V, NO LOAD	-0. 5		0.05	V		
Vdom(TX)sym	dominant output voltage symmetry	Vdom(TX)sym=VCC- VCANH VCANL	-400		400	mV		
VTXsym	Output Voltage Symmetry	VTXsym= VCANH + VCANL	0.9Vcc		1.1Vcc	V		
VOC	Common mode output voltage	S=0V, F8	2	2.5	3	V		
∆VOC	Explicit and implicit common mode output voltage difference			30		mV		
	Short-circuit output current	CANH=-12V, CANL=open, F11	-105	-72		mA		
100		CANH=12V, CANL=open, F11		0.36	1			
IOS		CANL=-12V, CANH=open, F11	-1	0.5				
		CANL=12V, CANH=open, F11		71	105			
IO(R)	Hidden output current	-27V <canh<32v 0<vcc<5.25v<="" td=""><td>-2.0</td><td></td><td>2.5</td><td>mA</td></canh<32v>	-2.0		2.5	mA		
VIL	Low Level Input			0.36	1			
		CANH=-12V, CANL=open, F 11	-1	0.5				
los Io(r)	Short-circuit output current Hidden output current	CANH=12V, CANL=open, F 11		71	105			
10(R)	Current	-27V <canh<32v 0<vcc<5.25v<="" td=""><td>-2.0</td><td></td><td>2.5</td><td>mA</td></canh<32v>	-2.0		2.5	mA		
Bus Tran	smitter Switch Characteristic							
tPLH	Transmission delay (low to high)	S=0V, F4	25	65	120	ns		
tPHL	Transmission delay (high to low)		25	45	90	ns		
tr	Differential output rise delay time			25		ns		
tf	Differential output fall delay time			50		ns		
tEN	Enable time from listen mode to dominant	F 7			10	μs		
tdom	Explicit timeout	F 10	300	450	700	μs		
tBUS	Bus wake-up time		0.7		5	μs		

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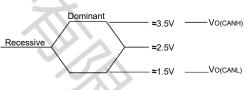
Bus Re	ceiver DC Paramete					
SYMBOL	PARAMETER	TESTCONDITIONS	MIN	MAX	UNIT	SYMBOL
VIT+	Positive Input Threshold	S=0V, F5		800	900	mV
VIT-	Negative Input Threshold		500	650		
VHYS	Comparator Threshold Hysteresis Interval		100	125		
Vон	High Level Output Voltage	IO=-2mA,F6	4	4.6		V
Vol	Low Level Output Voltage	IO=2Ma,F6		0.2	0.4	V
I(OFF)	Bus input current at power down	CANH or CANL=5V, Other pin=0V			5	μA
Cı	CANH, CANL input capacitance to ground			13		pF
CID	CANH, CANL Differential Input Capacitors			5		pF
Rin	CANH, CANL Input Resistance		15	30	40	ΚΩ
Rid	CANH, CANL Differential Input Resistors	TXD=3V,S=0V	30		80	ΚΩ
RImatch	RI(CANH), RIN(CANL) mismatches	CANH=CANL	-3%		3%	
Vсом	Common mode voltage range		-12		12	V
Bus Re	ceiver Switching Characteris					
tPLH	Propagation delay (low to high)	S=0V or VCC F6	60	100	130	ns
tPHL	Propagation delay (high to low)		45	70	90	ns
tr	RXD signal rise time	K		8		ns
tf	RXD signal fall time			8		ns
Device	Switching Character					
Td(LOOP1)	Loop delay 1, driver input to receiver output, implicit to explicit	S=0V, F9	90		190	ns
Td(LOOP2)	Loop Delay 2, Driver Input to Receiver Output, Explicit to Implicit	<i>X</i>	90		190	ns
Over-te	emperature protectio					
Tj(sd)	Over temperature shutdown		155	165	180	°C
TXD Pi	n Characteristi					
Vo	Common mode stabilised output voltage	-500uA <lo<500ua< td=""><td>0.3VCC</td><td></td><td>0.7VCC</td><td>V</td></lo<500ua<>	0.3VCC		0.7VCC	V
IO(S)	leakage current	S=2V,-12V <vo<12v< td=""><td>-5</td><td></td><td>5</td><td>μA</td></vo<12v<>	-5		5	μA
lih(txd)	TXD port high level input current	VI=VCC	-2		2	μΑ
IIL(TXD)	TXD Port Low Level Input Current	VI=0	-50		-10	μΑ
IO(off)	Current in TXD when VCC=0V	VCC=0V, TXD=5V			1	μΑ
VIH	Input High LowerLimit		2		VCC+0.3	V
VIL	Input Low Limit		-0.3		0.8	v
TXDO	TXD Port Dangle Voltage			н		logic
Po <u>we</u> r	consumption characteristic					
ICC	Silent Mode Power Consumption	S=VCC, VI=VCC		5	12	μA
	Dominant power consumption	VI=0V, S=0V LOAD=60Ω		50	70	mA
	Implicit power consumption	VI=VCC,S=0V NO LOAD		6	10	mA

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	high; L=low; X=no N Transceive							
Vcc	TXD ⁽¹⁾	S ⁽¹⁾	CANH ⁽¹⁾	CANL ⁽¹⁾	BUS	STATE	RXD ⁽¹⁾	
4.5V~5.5V	L	L	н	L	doi	minant	L	
4.5V~5.5V	H (or float)	Х	0.5VCC	0.5VCC	in	nplicit	н	
4.5V~5.5V	x	H(or float)	0.5VCC	0.5VCC	implicit		H	
0 <vcc<4.5v< td=""><td>x</td><td>х</td><td>0V<vcanh<vcc< td=""><td>0V<vcanl<vcc< td=""><td>in</td><td>nplicit</td><td>х</td></vcanl<vcc<></td></vcanh<vcc<></td></vcc<4.5v<>	x	х	0V <vcanh<vcc< td=""><td>0V<vcanl<vcc< td=""><td>in</td><td>nplicit</td><td>х</td></vcanl<vcc<></td></vcanh<vcc<>	0V <vcanl<vcc< td=""><td>in</td><td>nplicit</td><td>х</td></vcanl<vcc<>	in	nplicit	х	
Table 2 Drive	r Function							
INPUTS			OUT	TPUTS	Bus State		s State	
L	Ţ	7	н	L	L Dominate(d		ite(dominan	
H (or floa)	x		Z	Z	Z Recessive(imp		sive(implicit)	
Х	H(or fl	pat)	Z	Z	Z Re		Recessive(implicit)	
Table 3 Rece	iver Functio	n						
VID=CANH-CANL			RXD ⁽¹⁾		Bus State ⁽¹⁾			
VID≥0.9V		100	L		Dominate(dominant)			
0.5< VI	0<0.9V		'X					
VID≤0.5V			H Recessive (ir		essive (im	plicit)		
Open			Н		Recessive (implicit)			



F1.Driver Voltage, Current Test Definitions





CL=100pF

Vcc

VO(D)

0.5V VO(R)

Vcc/2

 <a>RL=60±1[™]

CANH

CANL

tPHL

TXD

 \int

Vī

Vo

tPLH

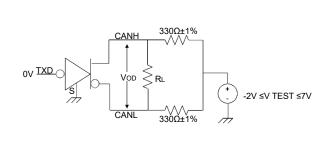
0.9

tr

s

Vcc/2

Ý



F3. Driver VOD Test Circuit

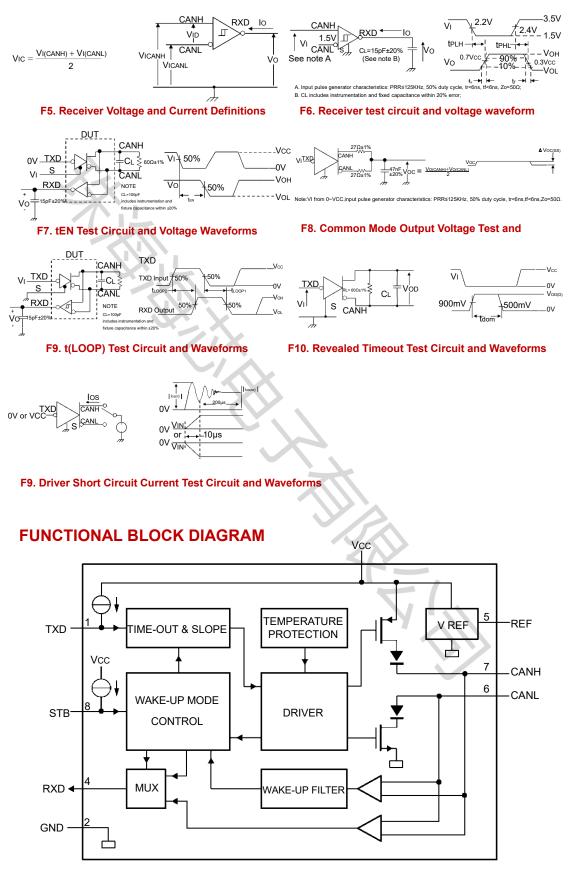
F4. Driver Test Circuit and Voltage Waveforms

90%

100%

tı

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DESCRIPTION

Brief description

The HX13053-S is an interface chip that connects the CAN protocol controller with the physical bus. It can be used in trucks, buses, cars, industrial control, and other applications. With a data transmission rate of 1Mbps, the HX13053-S has the capability to transmit differential signals between the bus and the CAN protocol controller, fully compliant with the ISO 11898 standard.

Short circuit protection

The HX13053-S's driver stage incorporates a current limit protection feature, designed to safeguard the circuit from shorts to the positive and negative supply voltages. In the event of a short-circuit, power consumption would rise, but thanks to the short-circuit protection function, the driver stage is protected from potential damage.

Fail Safe

The TXD pin features a pull-up to VCC connection, ensuring that the bus remains in a recessive state when the TXD pin is not powered. The S pin has a pull-up to VCC pass-through, ensuring that the transceiver remains in the standby state when the S pin is not powered. In the event of a power supply drop, the TXD, S, and RXD pins will become unconnected to prevent reverse power flow through these pins.

Over Temperature Protection

The HX13053-S features an over-temperature protection function. When the junction temperature exceeds 160°C, the current of the driver stage is reduced. As the driver tube is the main energy-consuming component, reducing the current lowers power consumption, ultimately cooling the chip. Other parts of the chip continue to operate normally during this process.

Significant Timeout Function

The TXD dominant timeout timer circuit is built-in to prevent the bus line from being driven to a permanently dominant state (blocking all network communications) if the TXD pin is forced permanently low due to a hardware or software application failure. The timer is triggered by a negative edge on the TXD pin.

If the low level on the TXD pin persists longer than the internal timer value (tdom), the transmitter is disabled, driving the bus into a recessive state. The timer is reset by a positive edge on the TXD pin.

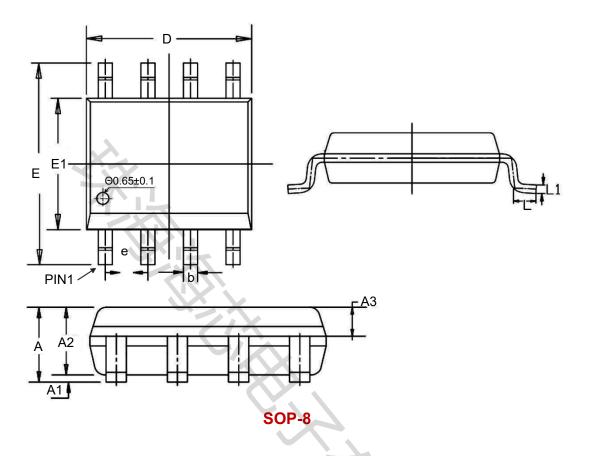
Control Modes

The control pin S allows selection between two operation modes: high-speed mode and standby mode. In high-speed mode—the normal mode—the transceiver sends and receives data over the CANH and CANL buses by grounding the S pin. In this mode, the differential receiver converts analogue data on the bus to digital data and outputs it via a multiplexer (MUX) to the RXD pin.

If the S pin is left unconnected or connected to a high voltage level, it enters standby mode. In this mode, the transmitter and receiver are turned off, and the bus line is monitored by a low-power differential comparator. When a high voltage level is applied to the S pin, it activates the low-power receiver and wake-up filter. As soon as the low-power differential comparator detects a dominant bus level above tBUS, the RXD pin goes low.



DIMENSIONAL DRAWINGS



<u>UNIT:mm</u>			
	MIN	NOM	MAX
Α	1.450	1.550	1.650
A1	0.100	0.150	0.200
A2	1.300	1.400	1.500
A3	0.600	0.650	0.700
b	0.380		0.510
е	1.240	1.270	1.300
D	4.800	4.900	5.000
E	5.800	6.000	6.200
E1	3.800	3.900	4.000
L	0.450	0.600	0.750
L1		0.25BSC	

Part Number	Package Type	Package	quantity	
HX13053-S	SOP-8	Taping	2500	

Disclaimer

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