

# GaN 650V GaN HEMT RC65D600E

## Description

The RC65D600E Series 650V, 600mΩ gallium nitride (GaN) FETs are normally-off devices. RealChip GaN FETs offer better efficiency through lower gate charge, faster switching speeds, and lower dynamic onresistance, delivering significant advantages over traditional silicon (Si) devices.

RealChip is a leading-edge wide band gap supplier with world-class innovation.

## Automotive

- Adapter
- Renewable energy
- Telecom and data-com
- Servo motors
- Industrial
- Automotive

## General Features

Easy to drive—compatible with standard gate drivers

Low conduction and switching losses

RoHS compliant and Halogen-free

## Benefits

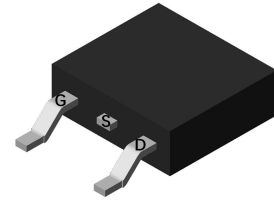
Increased efficiency through fast switching

Increased power density

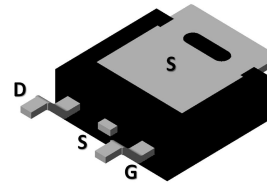
Reduced system size and weight

## Ordering Information

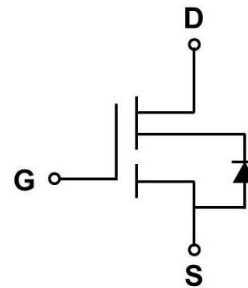
Part Number	Package	Package Configuration
RC65D600E	TO252	Source



Top



Bottom



Circuit Symbol

## Features

$BV_{DSS}$	$R_{DS(ON)}$	$I_{DS}$	$Q_G$
650V	600mΩ	4.8A	7nC

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## Absolute Maximum Ratings

$T_C=25^\circ\text{C}$  unless otherwise stated

Symbol	Parameter	Limit value	Unit	
$V_{DSS}$	Drain to source voltage ( $T_J = -55^\circ\text{C}$ to $150^\circ\text{C}$ )	650		
$V_{(TR)DSS}$	Drain to source voltage-transient <sup>a</sup>	900	V	
$V_{GSS}$	Gate to source voltage	-20~+20		
$I_D$	Continuous drain current @ $T_C=25^\circ\text{C}$ <sup>b</sup>	4.8	A	
	Continuous drain current @ $T_C=125^\circ\text{C}$ <sup>b</sup>	2.1		
$I_{DM}$	Pulse drain current (pulse width: 100 $\mu\text{s}$ )	14	A	
$P_D$	Maximum power dissipation @ $T_C=25^\circ\text{C}$	25	W	
$T_C$	Operating temperature	Case	-55~150	$^\circ\text{C}$
$T_J$		Junction	-55~150	$^\circ\text{C}$
$T_S$	Storage temperature	-55~150	$^\circ\text{C}$	

a. In off-state, spike duty cycle  $D<0.01$ , spike duration  $<1\mu\text{s}$

b. For increased stability at high current operation

# GaN 650V GaN HEMT RC65D600E

## Thermal Resistance

Symbol	Parameter	Limit value	Unit
$R_{\theta JC}$	Junction-to-case	5	°C /W

# GaN 650V GaN HEMT

## RC65D600E

### Electrical Parameters

T<sub>J</sub>=25°C unless otherwise stated

Symbol	Parameter	Min	Typ	Max	Unit	Test Conditions
<b>Forward Device Characteristics</b>						
V <sub>(BL)DSS</sub>	Drain-source voltage	650	-	-	V	V <sub>GS</sub> = 0V
V <sub>GS(th)</sub>	Gate threshold voltage	-	1.9	-	V	
ΔV <sub>GS(th)</sub> /T <sub>J</sub>	Gate threshold voltage temperature coefficient	-	-7	-	mV/°C	V <sub>DS</sub> =1V, I <sub>DS</sub> =1mA
R <sub>DS(on)</sub>	Drain-source on-resistance	-	600	720	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =1A, T <sub>J</sub> =25°C
		-	1260	-		V <sub>GS</sub> =10V, I <sub>D</sub> =1A, T <sub>J</sub> =150°C
I <sub>DSS</sub>	Drain-to-source leakage current	-	-	10	μA	V <sub>DS</sub> =650V, V <sub>GS</sub> = 0V, T <sub>J</sub> =25°C
		-	-	100		V <sub>DS</sub> =650V, V <sub>GS</sub> = 0V, T <sub>J</sub> =150°C
I <sub>GSS</sub>	Gate-to-source forward leakage current	-	-	±100	nA	V <sub>GS</sub> = ±20V
C <sub>ISS</sub>	Input capacitance	-	243	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =400V, f=1MHz
C <sub>OSS</sub>	Output capacitance	-	5.3	-		
C <sub>RSS</sub>	Reverse capacitance	-	0.4	-		
Q <sub>G</sub>	Total gate charge	-	7	-	nC	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V to 10V, I <sub>D</sub> =1A
Q <sub>GS</sub>	Gate-source charge	-	2.1	-		
Q <sub>GD</sub>	Gate-drain charge	-	0.9	-		
Q <sub>OSS</sub>	Output charge	-	9	-	nC	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V to 400V, f=1MHz
t <sub>D(on)</sub>	Turn-on delay	-	6	-	ns	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V to 10V, I <sub>D</sub> =2.1A, R <sub>G-on(ext)</sub> =6.8Ω, R <sub>G-off(ext)</sub> =2.2Ω, L=250μH
t <sub>R</sub>	Rise time	-	15	-		
t <sub>D(off)</sub>	Turn-off delay	-	7	-		
t <sub>F</sub>	Fall time	-	14	-		

# GaN 650V GaN HEMT RC65D600E

## Electrical Parameters

T<sub>J</sub>=25°C unless otherwise stated

Symbol	Parameter	Min	Typ	Max	Unit	Test Conditions
<b>Reverse Device Characteristics</b>						
V <sub>SD</sub>	Source-Drain reverse voltage	-	2.2	-	V	V <sub>GS</sub> =0V, I <sub>SD</sub> =2.5A
t <sub>RR</sub>	Reverse recovery time	-	14	-	ns	I <sub>F</sub> =2.5A, V <sub>DD</sub> =400V, dI <sub>F</sub> /dt=165A/μs
Q <sub>RR</sub>	Reverse recovery charge	-	6.5	-	nC	

# GaN 650V GaN HEMT RC65D600E

## Typical Characteristics

$T_J=25^\circ\text{C}$  unless otherwise stated

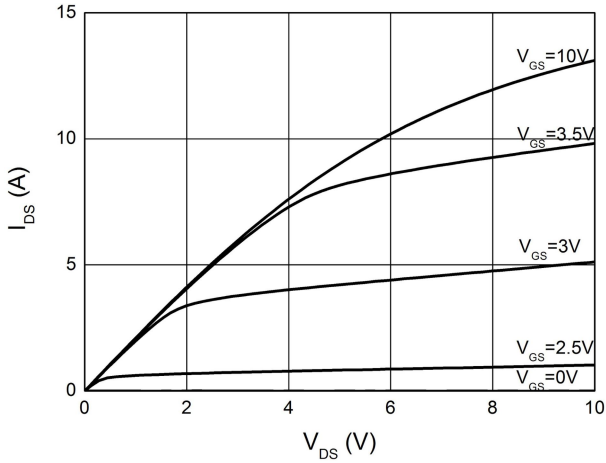


Figure 1. Typical Output Characteristics  $T_J=25^\circ\text{C}$

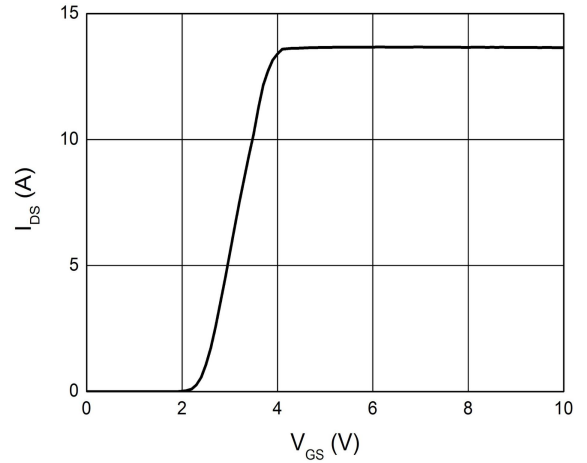


Figure 2. Typical Transfer Characteristics  $T_J=25^\circ\text{C}$   
( $V_{DS}=10V$ )

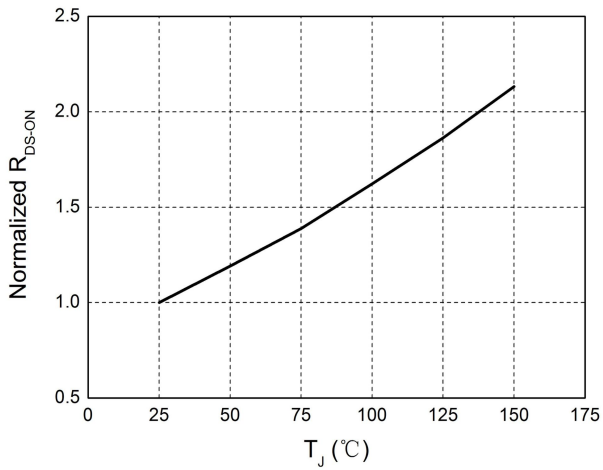


Figure 3. Normalized On-resistance

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## Typical Characteristics

$T_J=25^\circ\text{C}$  unless otherwise stated

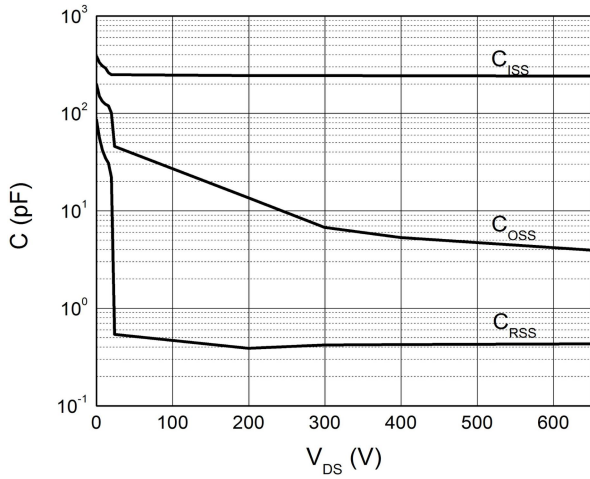


Figure 4. Typical Capacitance (f=1MHz)

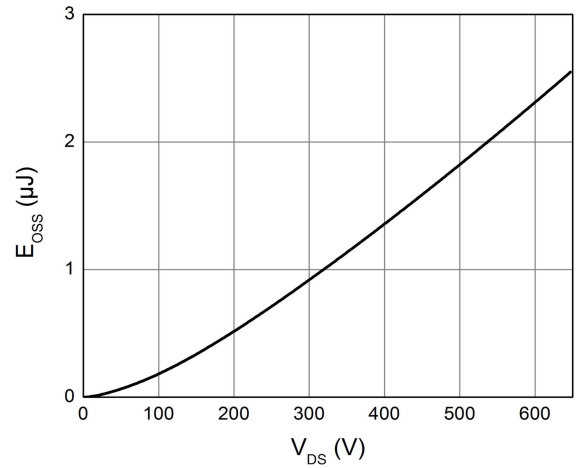


Figure 5. Typical  $C_{OSS}$  Stored Energy

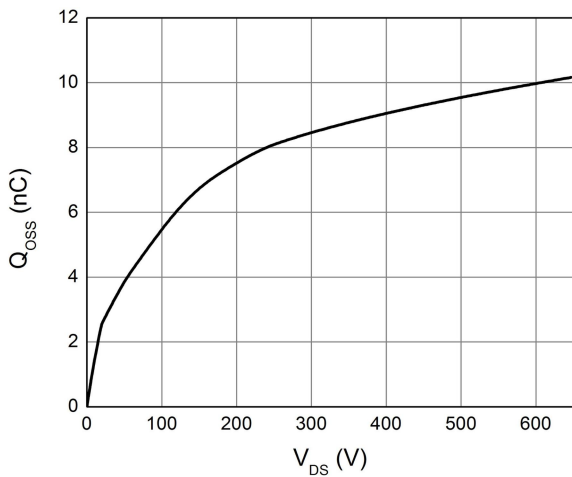


Figure 6. Typical  $Q_{OSS}$

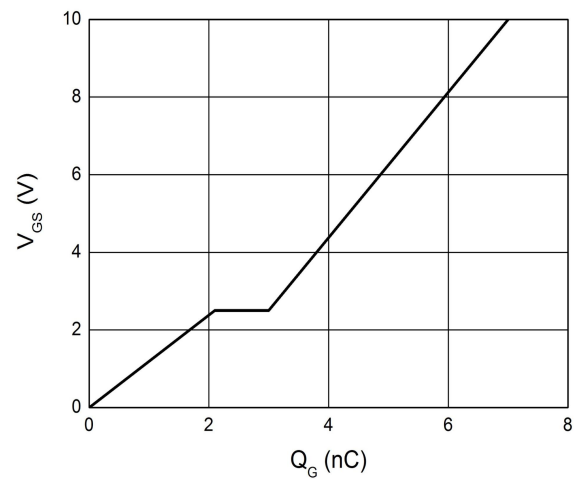


Figure 7. Typical Gate Charge ( $V_{DS}=400\text{V}$ ,  $I_D=1\text{A}$ )

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## Typical Characteristics

$T_J=25^\circ\text{C}$  unless otherwise stated

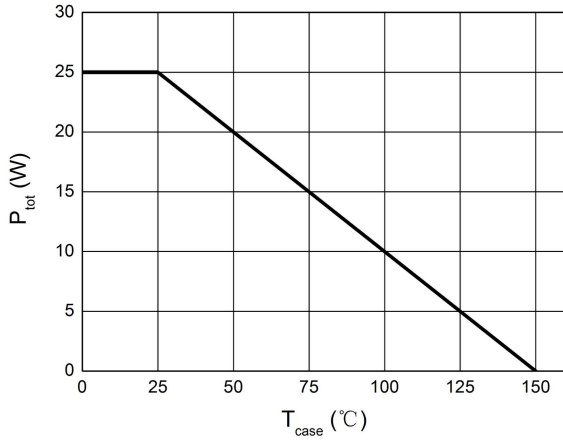


Figure 8. Power Dissipation



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## Typical Characteristics

$T_J=25^\circ\text{C}$  unless otherwise stated

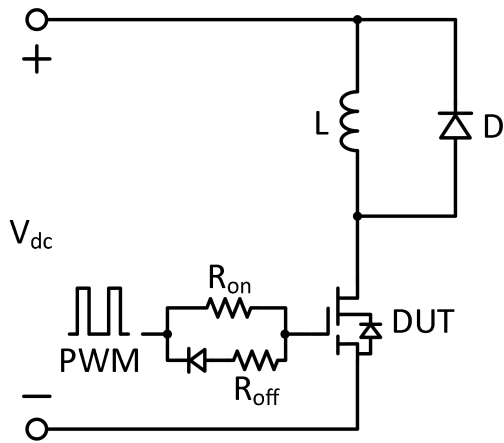


Figure 9. Switching times with inductive load

$V_{DS}=400\text{V}$ ,  $V_{GS}=0\text{V}$  to  $10\text{V}$ ,  $I_D=2.1\text{A}$ ,  
 $R_{G-on(ext)}=6.8\Omega$ ,  $R_{G-off(ext)}=2.2\Omega$ ,  $L=250\mu\text{H}$

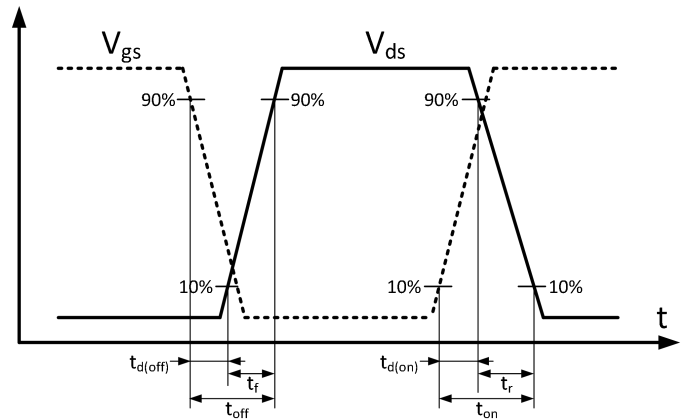


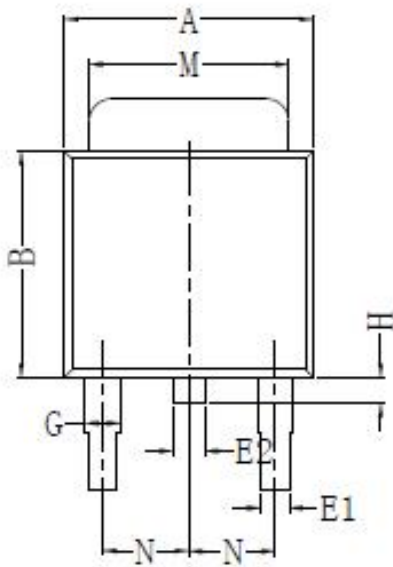
Figure 10. Switching times with waveform

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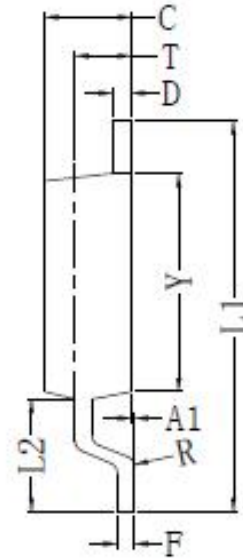
## PACKAGE DIMENSIONS

TO252-2L

Top view



Side view



Symbol	Min. (mm)	Max. (mm)
A	6.30	6.90
A1	0.00	0.16
B	5.70	6.30
C	2.10	2.50
D	0.30	0.70
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.40	10.50
L2	2.70	3.10
H	0.40	1.00
M	5.10	5.50
N	2.09	2.49
R	0.30	
T	1.40	1.60
Y	5.10	6.30