

N-Channel 30V MOSFET

E030N6P0ML1

V_{DS} (V)	$R_{DS(on),max}$ (m Ω)	I_D (A)
30V	6 @ $V_{GS} = 10V$	30

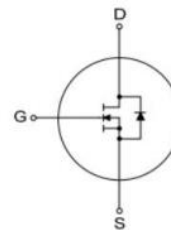
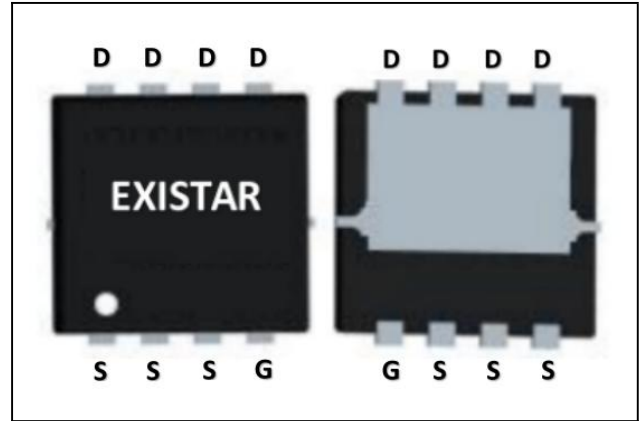
Features

- Low $R_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- Moto driver

PDFN3.3X3.3



N-Channel



RoHS
COMPLIANT
HALOGEN
FREE

Package And Ordering Information

Ordering code	Package	Marking
E030N6P0ML1	PDFN3.3*3.3	E030N6P0ML1

Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
PDFN3.3*3.3	5000	1	5000

Key Performance Parameters

Parameter	Value	Unit
VDS, min @ Tj(max)	30	V
ID, pulse	160	A
RDS(ON), max @ VGS=10V	6	mΩ
Qg	27	nC

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

Parameter	Symbol	Limit	Unit
Drain-source voltage	V _{DS}	30	V
Gate-source voltage	V _{GS}	±20	
Continuous drain current	I _D	T _C =25°C	30
		T _C =100°C	14
Pulsed drain current	I _{D,pulse}	160	A
Avalanche energy, single pulse	E _{AS}	25	mJ
Power dissipation	P _D	T _C =25°C	30
		T _A =25°C	3.6
Operating junction and storage temperature range	T _J , T _{stg}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal resistance, junction-to-case Steady state	R _{θJC}	4.9	°C/W
Thermal resistance, junction-to-ambient Steady state	R _{θJA}	42	

Electrical Characteristics at Tj=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Static						
Drain to source breakdown voltage	V _{(BR)DSS}	30			V	V _{GS} = 0, I _D = 250 μA
Gate-source threshold voltage	V _{GS(th)}	1.3	1.65	2.4	V	V _{DS} = V _{GS} , I _D = 250 μA
Gate-body leakage	I _{GSS}			±100	nA	V _{DS} = 0 V, V _{GS} = ±20 V
Zero gate voltage drain current	I _{DSS}			1	μA	V _{DS} = 30 V, V _{GS} = 0 V
Drain-source on-resistance	R _{DS(on)}		5.2	6	mΩ	V _{GS} = 10 V, I _D = 15 A
Drain-source on-resistance	R _{DS(on)}		7.8	10	mΩ	V _{GS} = 4.5 V, I _D = 10 A
Gate resistance	R _g	0.2	2	5	Ω	f=1MHz

Gate Charge						
Total gate charge	Qg		27	47	nC	V _{DS} = 15 V, I _D = 15 A, V _{GS} = 10 V
Gate-source charge	Qgs		4.5	7.9		
Gate-drain charge	Qgd		5	8.8		
Dynamic						
Turn-on delay time	t _{d(on)}		6.4		ns	V _{DS} = 15 V, I _D = 15 A, V _{GS} = 10 V, R _{GEN} = 3 Ω
Rise time	t _r		51			
Turn-off delay time	t _{d(off)}		25			
Fall time	t _f		15			
Input capacitance	C _{iss}	415		2430	pF	V _{DS} = 15 V, V _{GS} = 0 V, f = 1MHz
Output capacitance	C _{oss}	80		970		
Reverse transfer capacitance	C _{rss}	30		245		
Body Diode						
Diode forward voltage	V _{SD}		0.8	1.2	V	V _{GS} = 0 V, I _F = 15A
Reverse recovery time	t _{rr}		7	28	ns	V _R = 0 V, I _S = 15 A, di/dt = 100 A/μs
Reverse recovery charge	Q _{rr}		1.4	6.4	nC	

Electrical Characteristics Diagrams

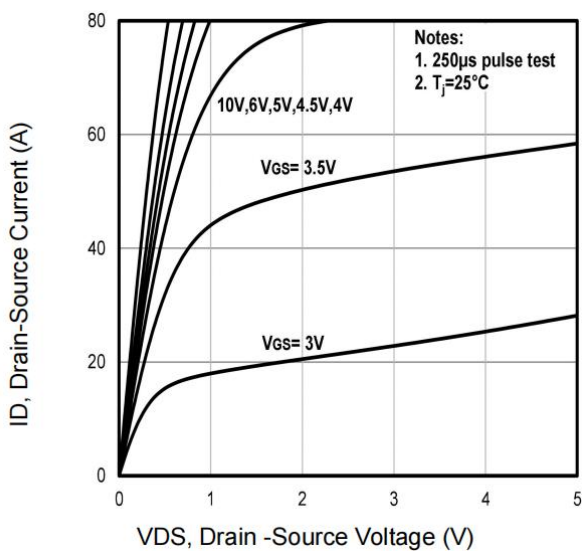


Fig1. Typical Output Characteristics

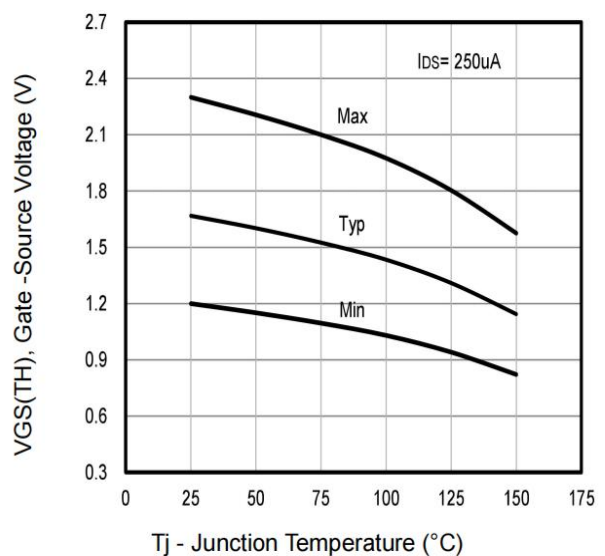


Fig2. Typical V_{GS(TH)} Gate -Source Voltage Vs. T_J



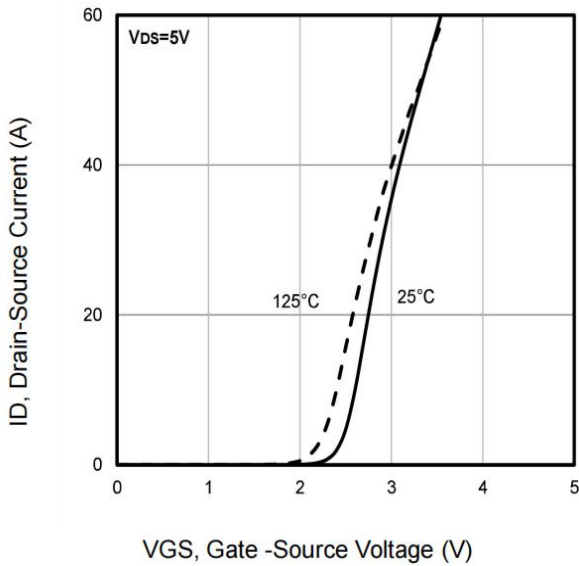


Fig3. Typical Transfer Characteristics

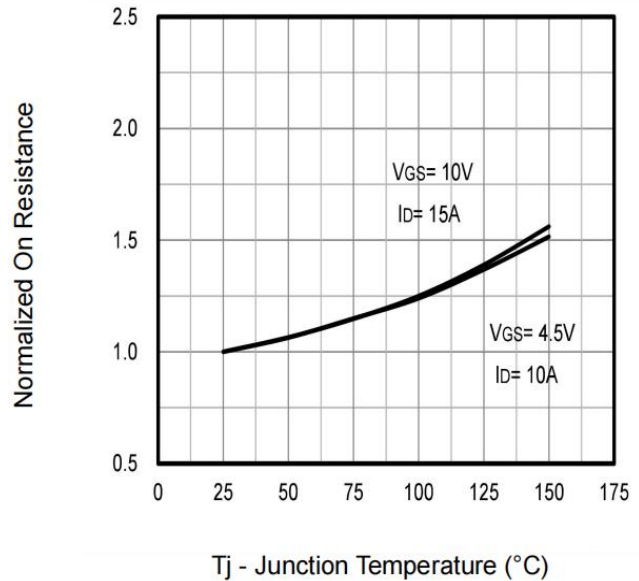


Fig4. Typical Normalized On-Resistance Vs. Tj

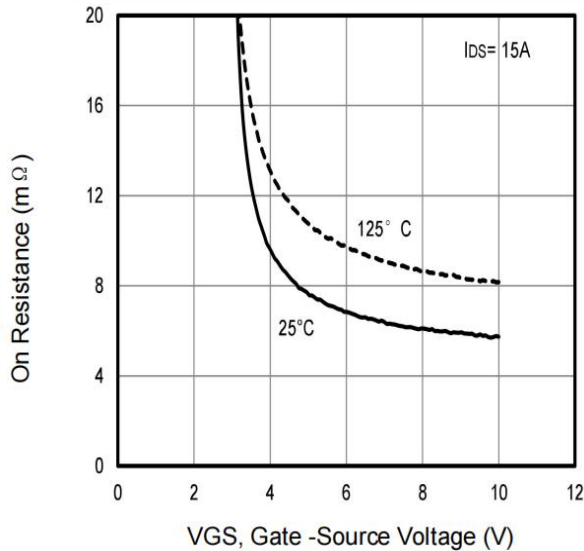


Fig5. Typical On Resistance Vs Gate-Source Voltage

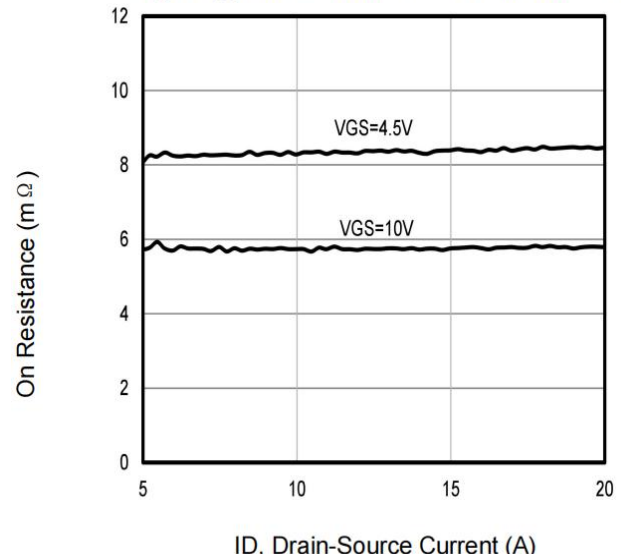


Fig6. Typical On Resistance Vs Drain Current and Gate

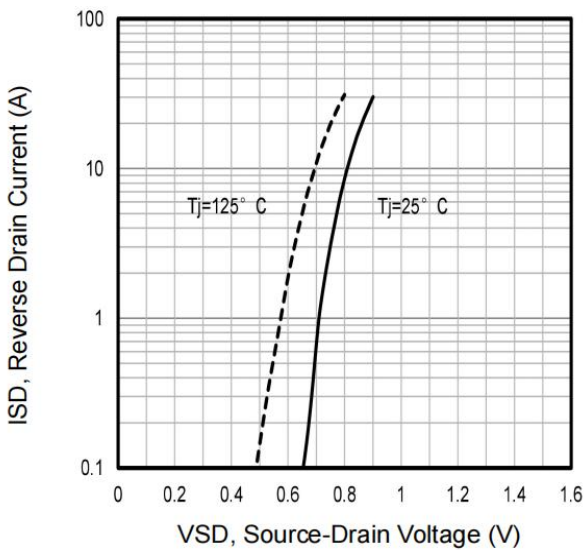


Fig7. Typical Source-Drain Diode Forward Voltage

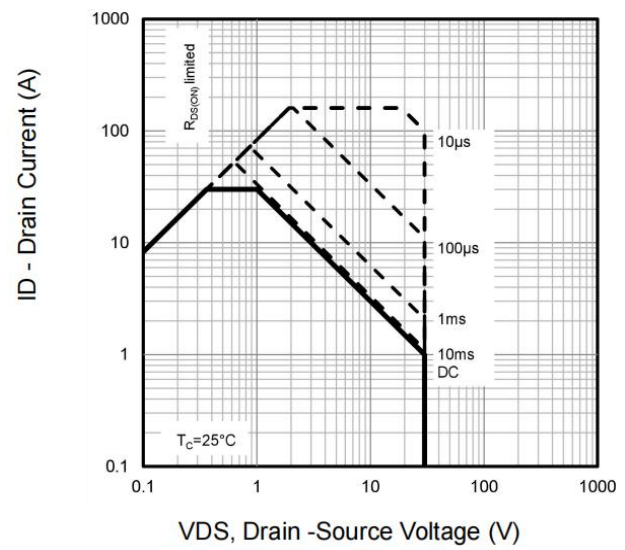


Fig8. Maximum Safe Operating Area



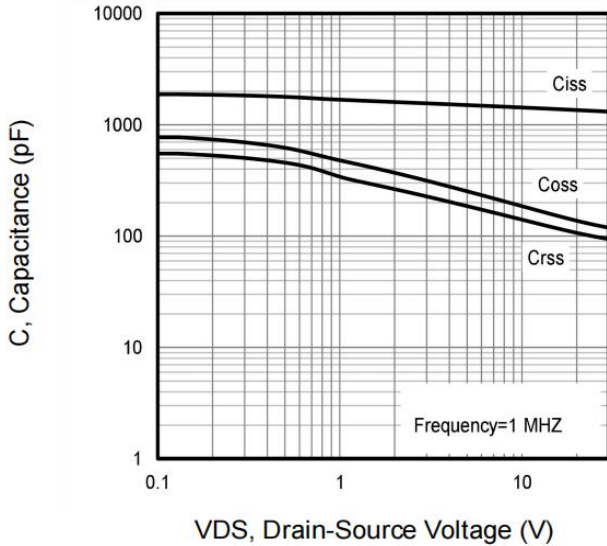


Fig9. Typical Capacitance Vs. Drain-Source Voltage

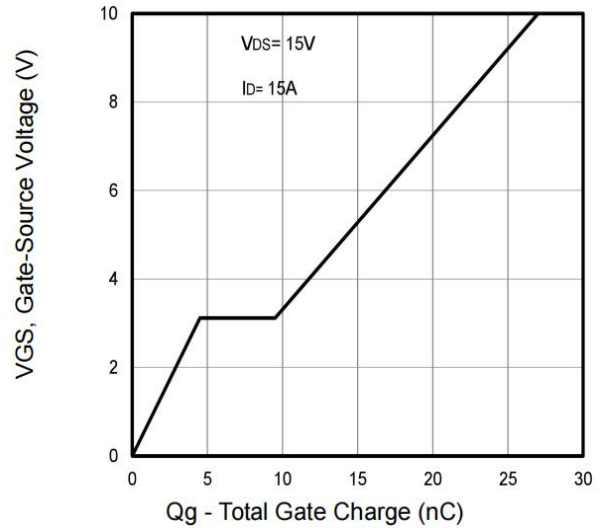


Fig10. Typical Gate Charge Vs. Gate-Source Voltage

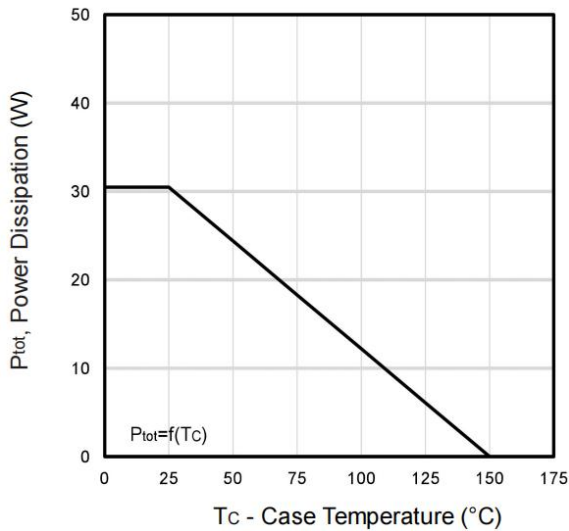


Fig11. Power Dissipation Vs. Case Temperature

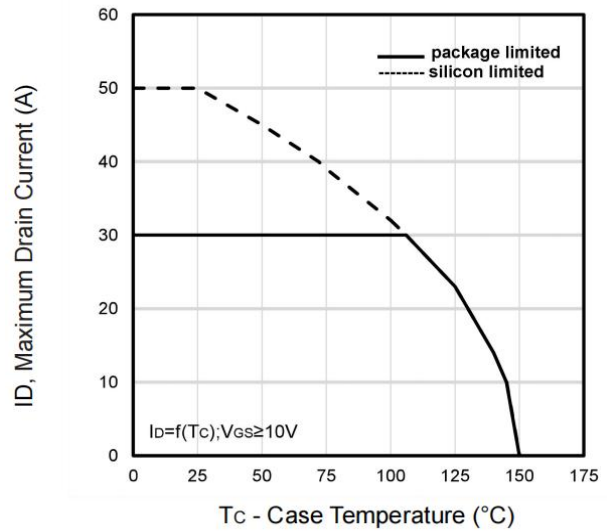


Fig12. Maximum Drain Current Vs. Case Temperature

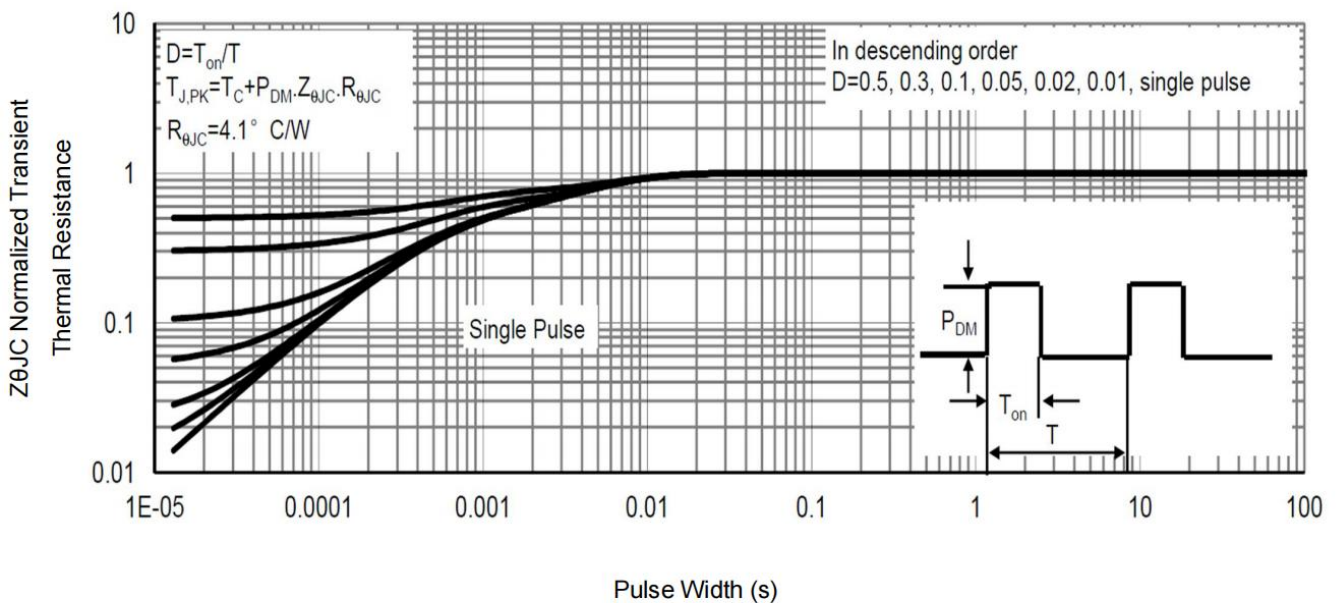


Fig13. Normalized Maximum Transient Thermal Impedance



Test circuits and waveforms

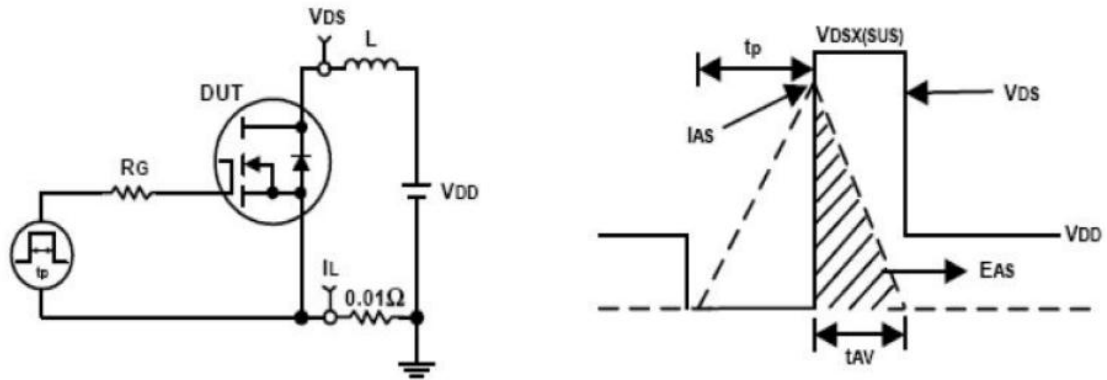


Fig1. Unclamped Inductive Test Circuit and waveforms

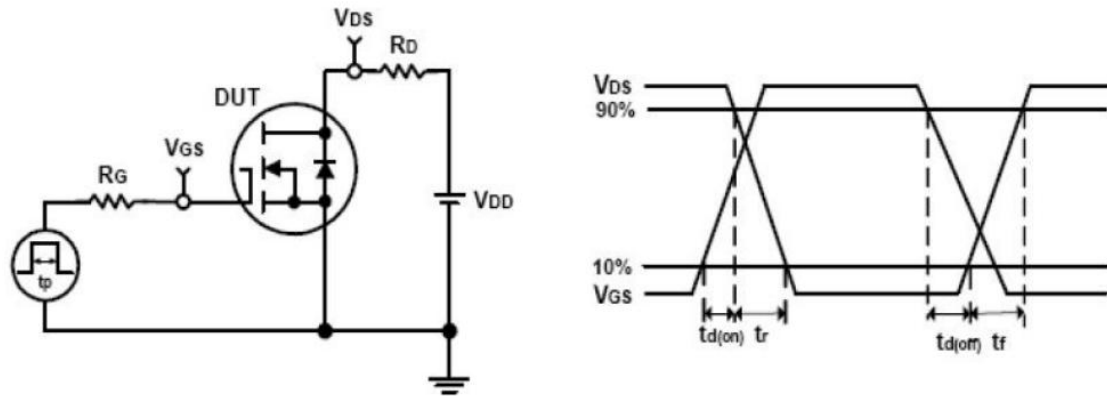
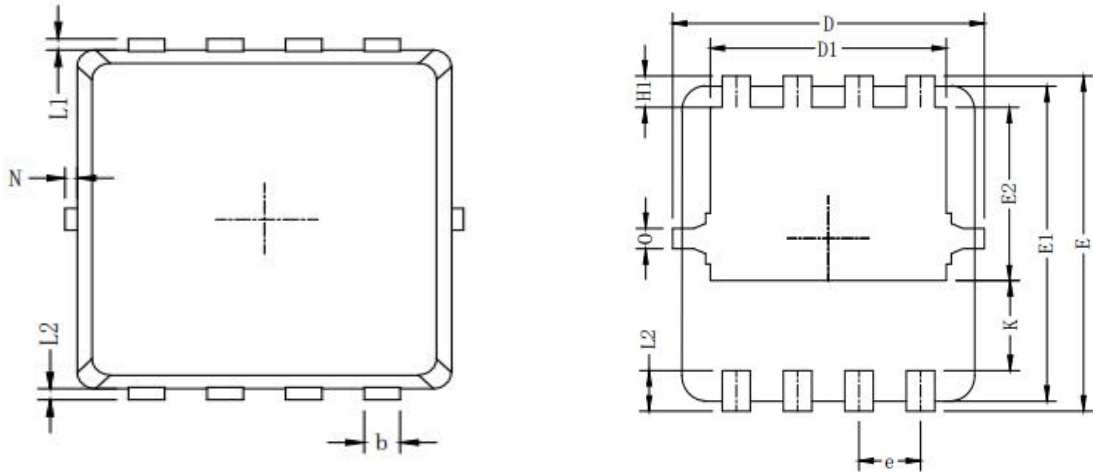


Fig2. Switching Time Test Circuit and waveforms

Package Outline Dimensions

PDFN3.3*3.3



Symbols	Millimeters		
	MIN.	NOM.	MAX.
A	0.65	0.75	0.85
b	0.25	0.30	0.35
C	0.15	0.20	0.25
D	3.00	3.10	3.20
D1	2.40	2.50	2.60
E	3.20	3.30	3.40
E1	3.00	3.10	3.20
E2	1.60	1.70	1.80
e	0.65 BSC.		
H1	0.21	0.31	0.41
H2	0.30	0.40	0.50
K	0.78	0.88	0.98
L1/L2	0.10 REF.		
θ	11°	12°	13°
N	0	-	0.15
0	0.2 REF.		



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