

N-Channel 40V MOSFET

E040N1P5HL1

V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)
40V	1.5 @ V _{GS} = 10V	125

Features

- Low R_{DS(on)} trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

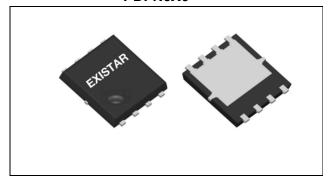
Package And Ordering Information

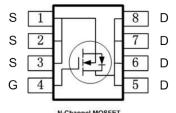
Ordering code	Package	Marking
E040N1P5HL1	PDFN5x6	E040N1P5HL1

Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
PDFN5x6	5000	1	5000

PDFN5X6









Key Performance Parameters

Parameter	Value	Unit
VDS, min @ Tj(max)	40	V
ID, pulse	865	Α
RDS(ON), max @ VGS=10V	1.5	mΩ
Qg	127	nC

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

Parameter	Symbol	Limit	Unit	
Drain-source voltage	V _{DS}	40		
Gate-source voltage	V_{GS}	±20	V	
	T _C =25°C		125	
Continuous drain current	T _C =100°C	- I _D	-	
Pulsed drain current	I _{D,pulse}	865	А	
Avalanche energy, single pulse	E _{AS}	870	mJ	
Dower dissinction	T _C =25°C		136	
Power dissipation	T _A =25°C	P_{D}	2	W
Operating junction and storage temperature range	TJ, T _{stg}	-55 to 150	°C	

Thermal Characteristics

Parameter		Symbol	Max.	Uni t
Thermal resistance, junction-to-case	Steady state	Rejc	1	
Thermal resistance, junction-to-ambient	Steady state	Reja	62	°C/W

Electrical Characteristics at Tj=25°C unless otherwise specified

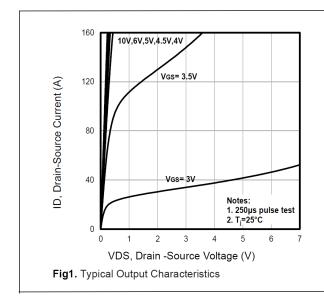
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions	
Static							
Drain to source breakdown voltage	V _{(BR)DSS}	40			V	V _{GS} = 0, I _D = 250 μA	
Gate-source threshold voltage	V _G s(th)	1.3		2.4	V	V _{DS} = V _{GS} , I _D = 250 μA	
Gate-body leakage	I _{GSS}			±100	nA	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	
Zero gate voltage drain current	I _{DSS}			1	μΑ	V _{DS} = 40 V, V _{GS} = 0 V	
Drain-source on-resistance	Ros(on)		1.15	1.5	mΩ	V _{GS} = 10 V, I _D = 40 A	
Drain-source on-resistance	Ros(on)		1.6	2.1	mΩ	V _{GS} = 4.5 V, I _D = 30 A	

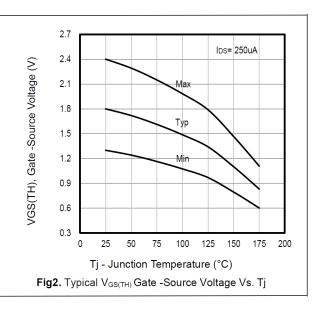
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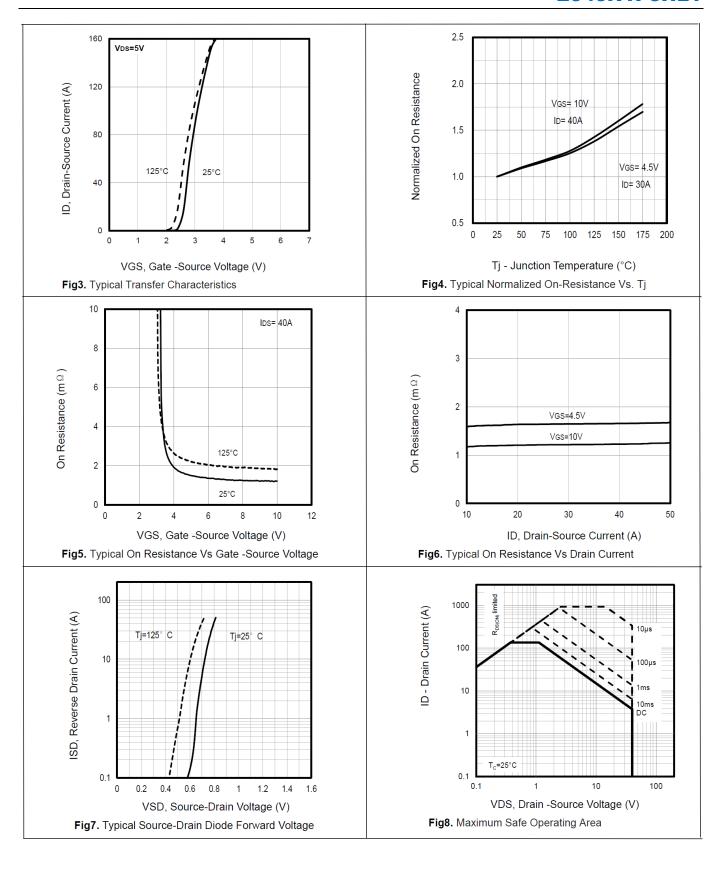
Forward transconductance	gfs		-		S	$V_{DS} = 5 \text{ V}, I_{D} = 30 \text{ A}$	
Gate resistance	Rg		3.8		Ω	f=1MHz	
Gate Charge							
Total gate charge	Qg		124				
Gate-source charge	Qgs		25		nC	$V_{DS} = 20 \text{ V}, I_D = 40 \text{ A}, V_{GS} = 10 \text{ V}$	
Gate-drain charge	Qgd		18				
		[Dynamic	;			
Turn-on delay time	$t_{\sf d(on)}$		11				
Rise time	t r		93		ns	V _{DS} =20 V, I _D =40 A, V _{GS} = 10 V,	
Turn-off delay time	$t_{\sf d(off)}$		144			$R_{GEN} = 3 \Omega$	
Fall time	t _f		90				
Input capacitance	C _{iss}		10530				
Output capacitance	C_{oss}		2045		pF	V _{DS} =20 V, V _{GS} = 0 V, f = 1MHz	
Reverse transfer capacitance	C_{rss}		950				
Body Diode							
Diode forward voltage	V _{SD}			1.2	V	V _{GS} = 0 V, I _F = 40 A	
Reverse recovery time	t _{rr}		57		ns	V _R = 20 V, I _S =40 A, di/dt = 100	
Reverse recovery charge	Qrr		58		nC	A/μs	

Electrical Characteristics Diagrams











Test circuits and waveforms

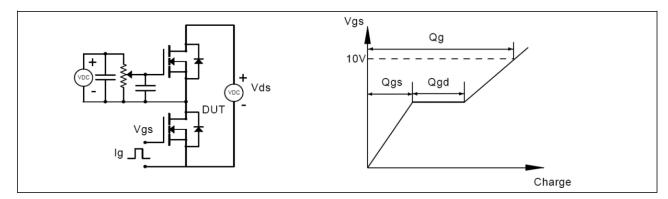


Figure 1. Gate charge test circuit & waveform

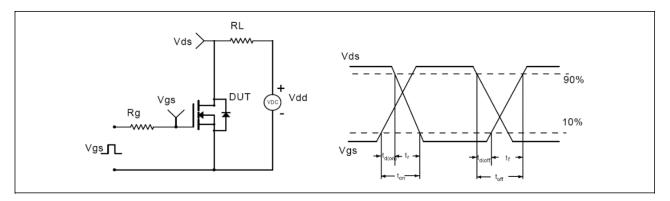


Figure 2. Switching time test circuit & waveforms

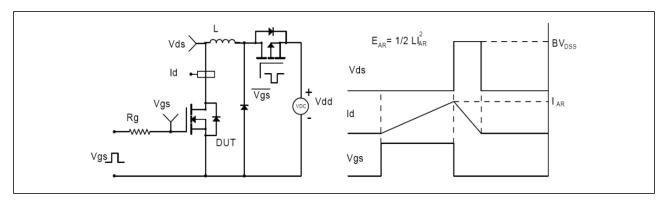


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

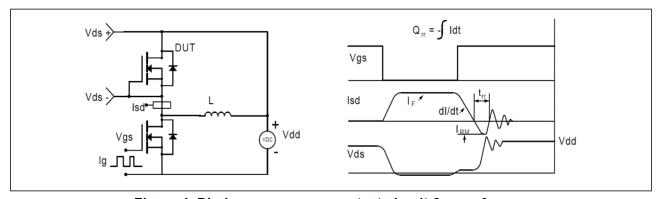
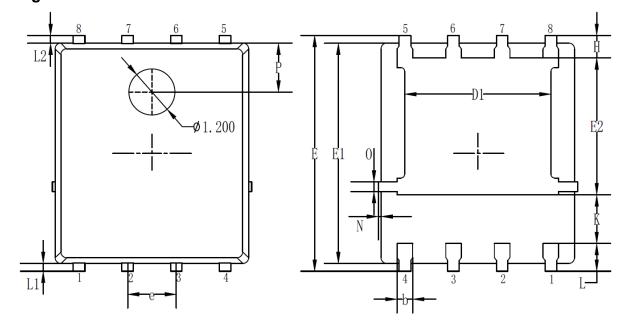
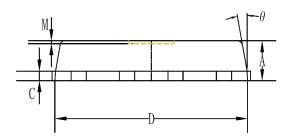


Figure 4. Diode reverse recovery test circuit & waveforms



Package Outline Dimensions





C1-1-	Mil	llimeter	'S			
Symbols	MIN.	NOM.	MAX.			
A	0.90	1.05	1.20			
b	0.35	0.40	0.50			
С	0.20	0. 25	0.35			
D	4.90	5.05	5. 20			
D1	3.72	3.82	3. 92			
Е	0.60	6. 15	6.30			
E1	5.60	5. 75	5. 90			
E2	3. 47	3.57	3.67			
е]	1.27 BSC	·•			
Н	0.48	0.58	0.68			
K	1. 17	1.27	1.37			
L	0.64	0.74 0.84				
L1/L2	(0.20 REF	₹.			
θ	8°	10°	12°			
M	0.08 REF.					
N	0	1	0.15			
0	0.25 REF.					
P	1.28 REF.					



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