

N-Channel 60V MOSFET

E060N8P5CL1

| V _{DS} (V) | R _{DS(on),max} (mΩ) | I _D (A) |
|---------------------|------------------------------|--------------------|
| 60V | 8.5@ V _{GS} = 10V | 64 |

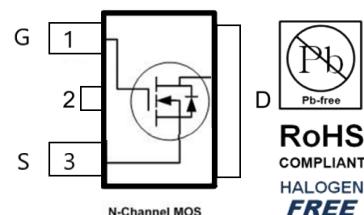
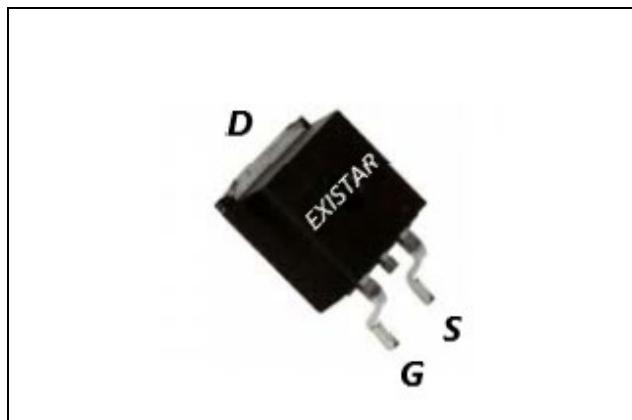
Features

- Low R_{DS(on)} trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

TO252



Package And Ordering Information

| Ordering code | Package | Marking |
|---------------|---------|-------------|
| E060N8P5CL1 | TO252 | E060N8P5CL1 |

Ordering Information

| Package | Units/ Reel | Reels/ Inner Box | Units/ Inner Box |
|---------|-------------|------------------|------------------|
| TO252 | 2500 | 2 | 5000 |

Key Performance Parameters

| Parameter | Value | Unit |
|------------------------|-------|------|
| VDS, min @ Tj(max) | 60 | V |
| ID, pulse | 256 | A |
| RDS(ON), max @ VGS=10V | 8.5 | mΩ |
| Qg | 12 | nC |

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

| Parameter | Symbol | Limit | Unit |
|--|-----------------------------------|----------------|------|
| Drain-source voltage | V _{DS} | 60 | V |
| Gate-source voltage | V _{GS} | ±20 | |
| Continuous drain current | T _C =25°C | I _D | |
| | T _C =100°C | - | |
| Pulsed drain current | I _{D,pulse} | 256 | A |
| Avalanche energy, single pulse | E _{AS} | 80 | mJ |
| Power dissipation | T _C =25°C | P _D | W |
| | T _A =25°C | - | |
| Operating junction and storage temperature range | T _J , T _{stg} | -55 to 150 | °C |

Thermal Characteristics

| Parameter | Symbol | Max. | Unit |
|---|------------------|------|------|
| Thermal resistance, junction-to-case | R _{θJC} | 2 | °C/W |
| Thermal resistance, junction-to-ambient | R _{θJA} | 62 | |

Electrical Characteristics at Tj=25°C unless otherwise specified

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test conditions |
|-----------------------------------|----------------------|------|------|------|------|---|
| Static | | | | | | |
| Drain to source breakdown voltage | V _{(BR)DSS} | 60 | | | V | V _{GS} = 0, I _D = 250 μA |
| Gate-source threshold voltage | V _{GS(th)} | 1.0 | | 2.5 | V | V _{DS} = V _{GS} , I _D = 250 μA |
| Gate-body leakage | I _{GSS} | | | ±100 | nA | V _{DS} = 0 V, V _{GS} = ±20 V |
| Zero gate voltage drain current | I _{DSS} | | | 1 | μA | V _{DS} = 60 V, V _{GS} = 0 V |
| Drain-source on-resistance | R _{Ds(on)} | | 7.5 | 8.5 | mΩ | V _{GS} = 10 V, I _D = 12 A |
| Drain-source on-resistance | R _{Ds(on)} | | 9.7 | 11 | mΩ | V _{GS} = 4.5 V, I _D = 9 A |

| | | | | | | |
|------------------------------|--------------|--|------|-----|----------|--|
| Forward transconductance | g_{fs} | | - | | S | $V_{DS} = 5 \text{ V}, I_D = 20 \text{ A}$ |
| Gate resistance | R_g | | 3.5 | | Ω | $f=1\text{MHz}$ |
| Gate Charge | | | | | | |
| Total gate charge | Q_g | | 12 | | nC | $V_{DS} = 30 \text{ V}, I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}$ |
| Gate-source charge | Q_{gs} | | 3 | | | |
| Gate-drain charge | Q_{gd} | | 2.2 | | | |
| Dynamic | | | | | | |
| Turn-on delay time | $t_{d(on)}$ | | 15 | | ns | $V_{DS} = 30 \text{ V}, I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}, R_{GEN} = 2 \Omega$ |
| Rise time | t_r | | 3 | | | |
| Turn-off delay time | $t_{d(off)}$ | | 28.2 | | | |
| Fall time | t_f | | 3.1 | | pF | $V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, f = 100\text{kHz}$ |
| Input capacitance | C_{iss} | | 968 | | | |
| Output capacitance | C_{oss} | | 277 | | | |
| Reverse transfer capacitance | C_{rss} | | 13.2 | | | |
| Body Diode | | | | | | |
| Diode forward voltage | V_{SD} | | | 1.3 | V | $V_{GS} = 0 \text{ V}, I_F = 20 \text{ A}$ |
| Reverse recovery time | t_{rr} | | 36.2 | | ns | $V_R = 30 \text{ V}, I_S = 25 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$ |
| Reverse recovery charge | Q_{rr} | | 18.6 | | nC | |

Electrical Characteristics Diagrams

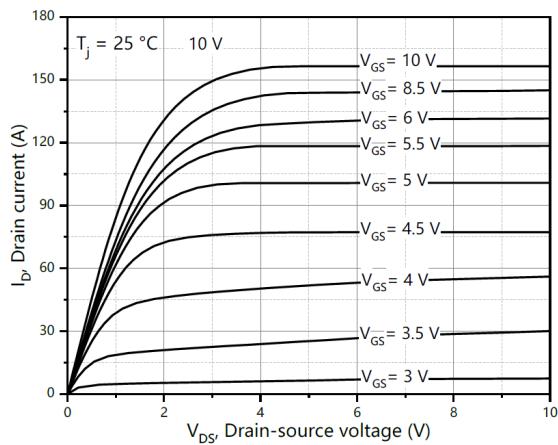


Figure 1. Typ. output characteristics

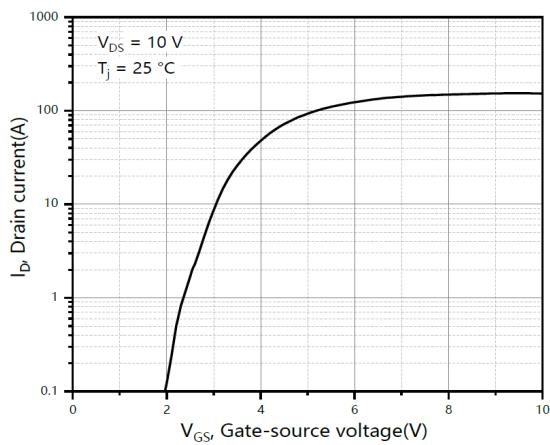


Figure 2. Typ. transfer characteristics

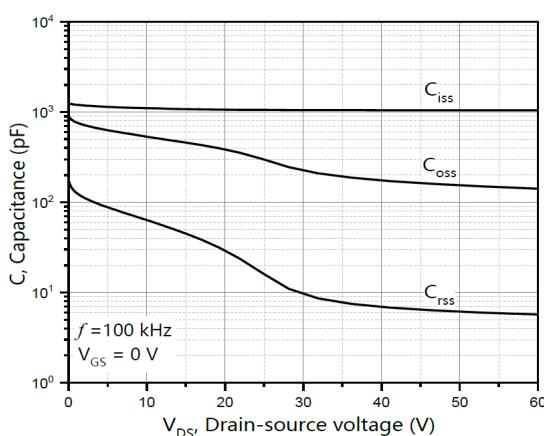


Figure 3. Typ. capacitances

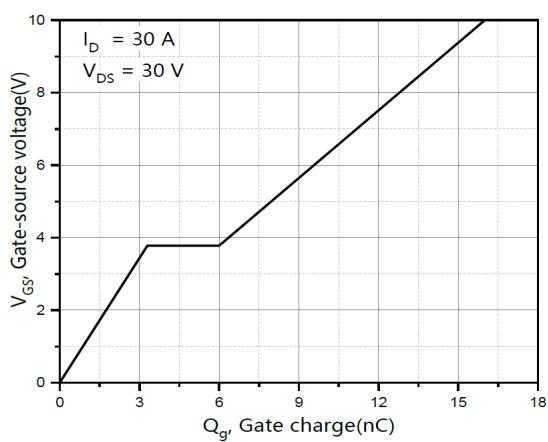


Figure 4. Typ. gate charge

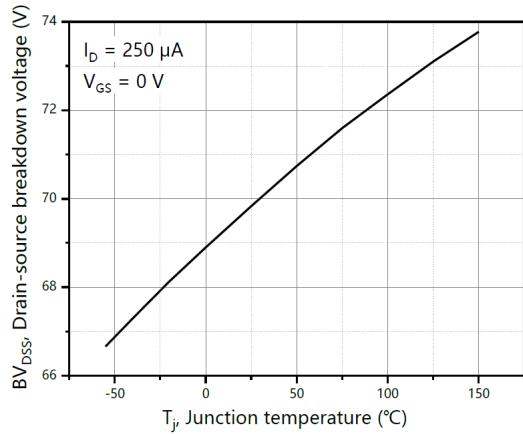


Figure 5. Drain-source breakdown voltage

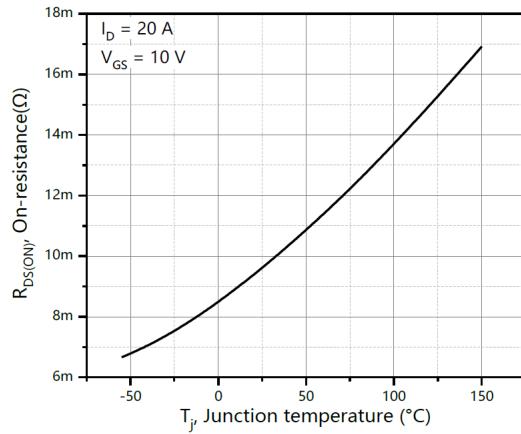


Figure 6. Drain-source on-state resistance

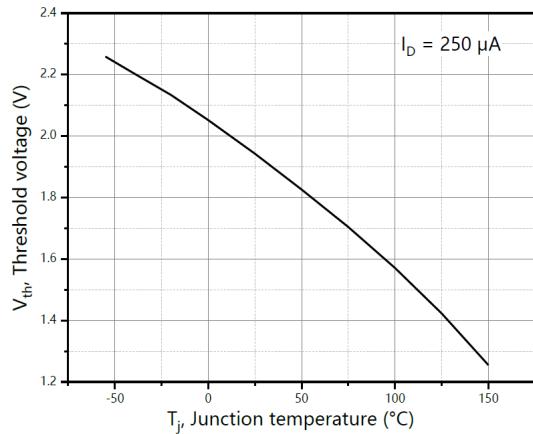


Figure 7. Threshold voltage

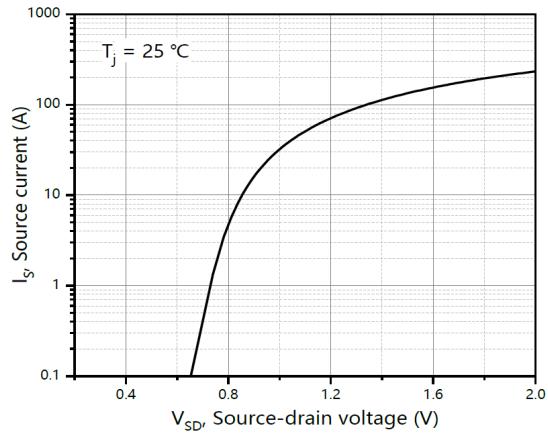


Figure 8. Forward characteristic of body diode

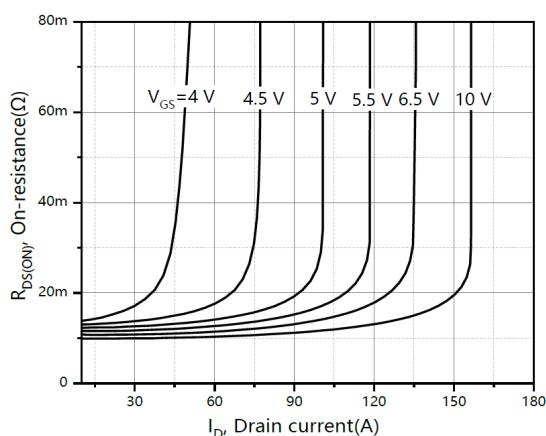


Figure 9. Drain-source on-state resistance

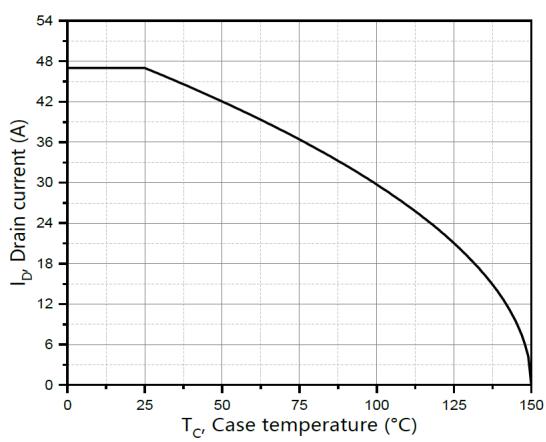


Figure 10. Drain current

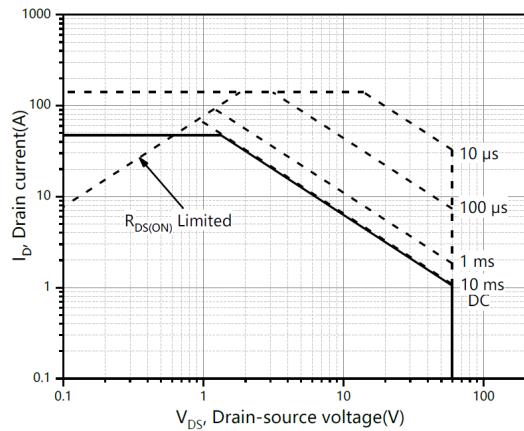


Figure 11. Safe operation area $T_c=25$ °C

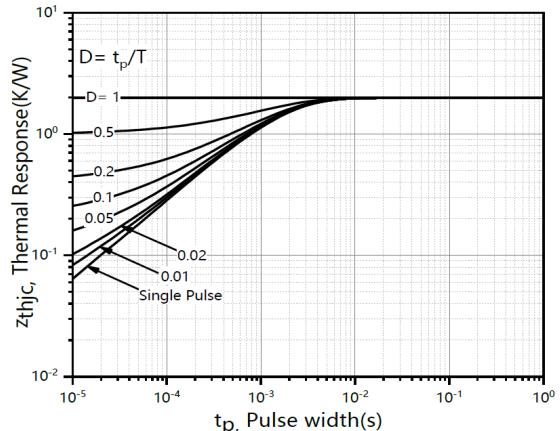


Figure 12. Max. transient thermal impedance

Test circuits and waveforms

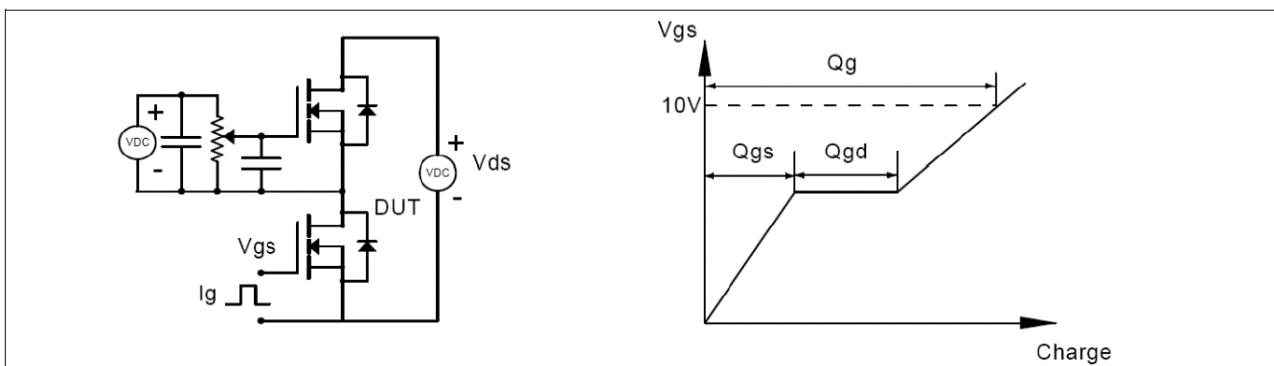


Figure 1. Gate charge test circuit & waveform

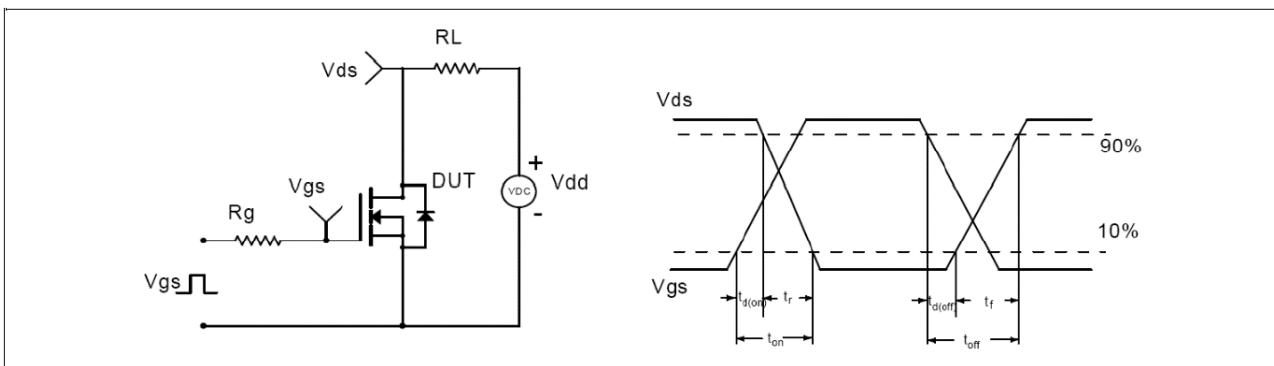


Figure 2. Switching time test circuit & waveforms

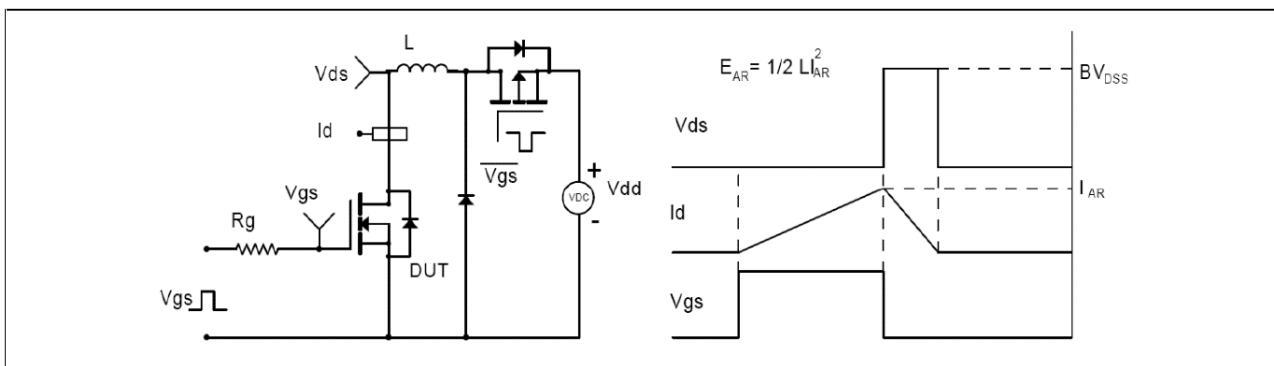


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

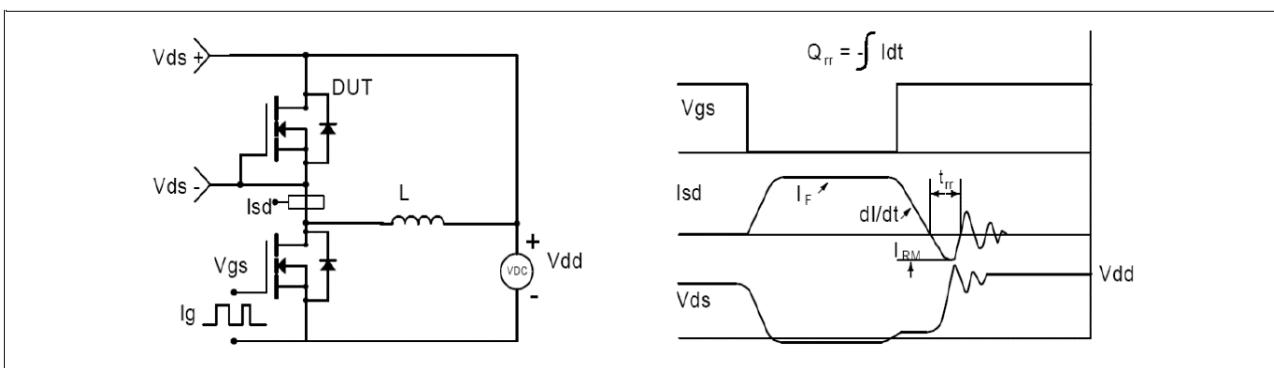
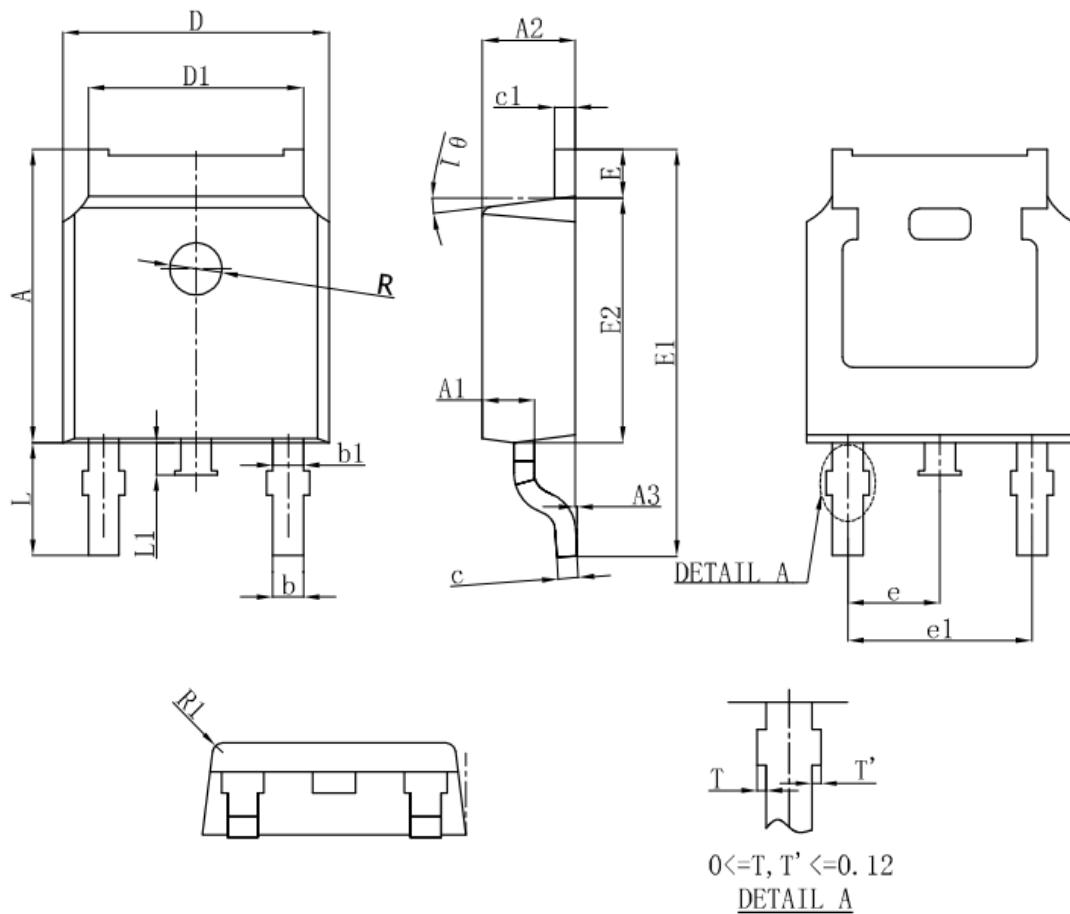


Figure 4. Diode reverse recovery test circuit & waveforms

Package Outline Dimensions



| SYMBOL | MILLIMETER | | |
|----------------|------------|-------|--------|
| | MIN | NOM | MAX |
| A | 7.050 | 7.100 | 7.150 |
| A1 | 0.960 | 1.010 | 1.060 |
| A2 | 2.250 | 2.300 | 2.350 |
| A3 | 0.000 | 0.050 | 0.100 |
| b | 0.760REF. | | |
| b1 | 1.000REF. | | |
| c | 0.508REF. | | |
| c1 | 0.508REF. | | |
| D | 6.550 | 6.600 | 6.650 |
| D1 | 5.220 | 5.320 | 5.420 |
| E | 0.950 | 1.000 | 1.050 |
| E1 | 9.700 | 9.900 | 10.100 |
| E2 | 6.050 | 6.100 | 6.150 |
| e | 2.286BSC | | |
| e1 | 4.572REF. | | |
| L | 2.650 | 2.800 | 2.950 |
| L1 | 0.700 | 0.800 | 0.900 |
| O ₁ | 7° REF. | | |
| R | 0.250REF. | | |

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