

## N-Channel 30V MOSFET

### E030N2P0HL1

$V_{DS}$ (V)	$R_{DS(on),max}$ (m $\Omega$ )	$I_D$ (A)
30V	2 @ $V_{GS} = 10V$	180

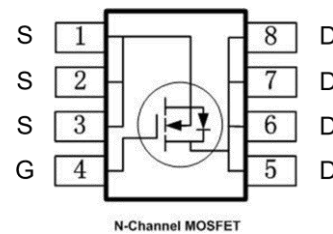
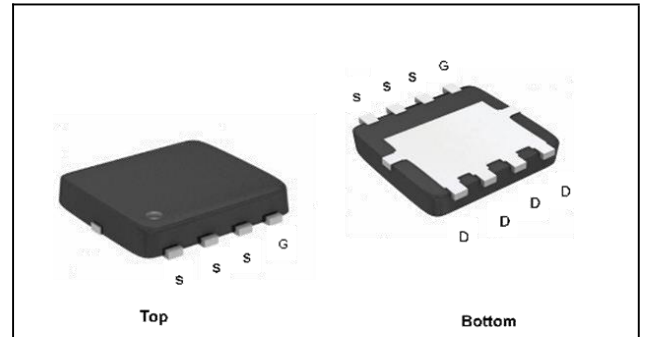
### Features

- Low  $R_{DS(on)}$  trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

### Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

### PDFN5X6



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### Package And Ordering Information

Ordering code	Package	Marking
E030N2P0HL1	PDFN5x6	E030N2P0HL1

### Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
PDFN5x6	5000	1	5000

**Key Performance Parameters**

Parameter	Value	Unit
VDS, min @ Tj(max)	30	V
ID, pulse	720	A
RDS(ON), max @ VGS=10V	2	mΩ
Qg	102	nC

**Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted**

Parameter	Symbol	Limit	Unit
Drain-source voltage	V <sub>DS</sub>	30	V
Gate-source voltage	V <sub>GS</sub>	±20	
Continuous drain current	I <sub>D</sub>	T <sub>C</sub> =25°C	180
		T <sub>C</sub> =100°C	114
Pulsed drain current	I <sub>D,pulse</sub>	720	A
Avalanche energy, single pulse	E <sub>AS</sub>	361	mJ
Power dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C	114
		T <sub>A</sub> =25°C	2.6
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C

**Thermal Characteristics**

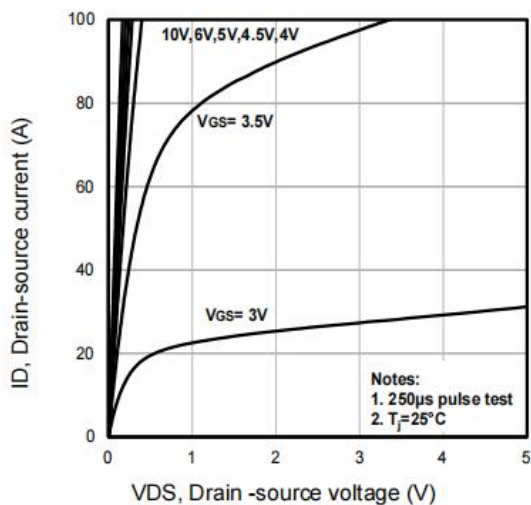
Parameter	Symbol	Max.	Unit
Thermal resistance, junction-to-case	R <sub>θJC</sub>	1.1	°C/W
Thermal resistance, junction-to-ambient	R <sub>θJA</sub>	48	

**Electrical Characteristics at Tj=25°C unless otherwise specified**

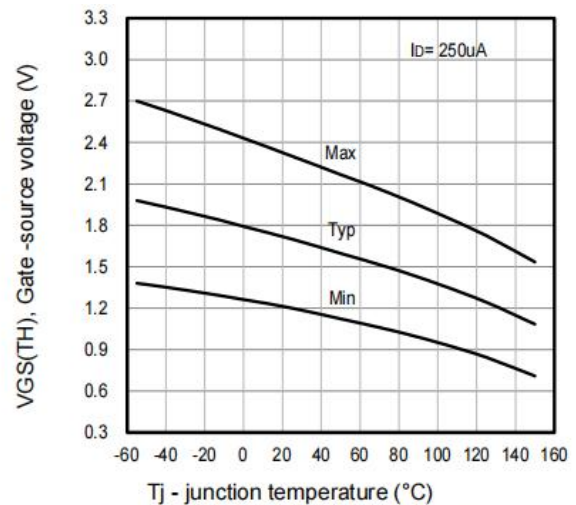
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
<b>Static</b>						
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30			V	V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA
Gate-source threshold voltage	V <sub>GS(th)</sub>	1.2	1.7	2.3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA
Gate-body leakage	I <sub>GSS</sub>			±100	nA	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V
Zero gate voltage drain current	I <sub>DSS</sub>			1	μA	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V
Drain-source on-resistance	R <sub>DS(on)</sub>		1.6	2	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A
Drain-source on-resistance	R <sub>DS(on)</sub>		2.5	3.1	mΩ	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A
Forward transconductance	g <sub>fs</sub>		54		S	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 30 A

Gate resistance	$R_g$		2.3		$\Omega$	$f=1\text{MHz}$
<b>Gate Charge</b>						
Total gate charge	$Q_g$		102		nC	$V_{DS} = 15\text{ V}, I_D = 30\text{ A}, V_{GS} = 10\text{ V}$
Gate-source charge	$Q_{gs}$		15			
Gate-drain charge	$Q_{gd}$		23			
<b>Dynamic</b>						
Turn-on delay time	$t_{d(on)}$		10		ns	$V_{DS} = 15\text{ V}, I_D = 30\text{ A}, V_{GS} = 10\text{ V}, R_{GEN} = 3\ \Omega$
Rise time	$t_r$		81			
Turn-off delay time	$t_{d(off)}$		83			
Fall time	$t_f$		45			
Input capacitance	$C_{iss}$		5095		pF	$V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}, f = 100\text{kHz}$
Output capacitance	$C_{oss}$		635			
Reverse transfer capacitance	$C_{rss}$		530			
<b>Body Diode</b>						
Diode forward voltage	$V_{SD}$		0.8	1	V	$V_{GS} = 0\text{ V}, I_F = 30\text{ A}$
Reverse recovery time	$t_{rr}$		25		ns	$V_R = 25\text{ V}, I_S = 30\text{ A}, di/dt = 100$
Reverse recovery charge	$Q_{rr}$		16		nC	$A/\mu s$

### Electrical Characteristics Diagrams



**Fig1.** Typical output characteristics



**Fig2.** Typical  $V_{GS(TH)}$  gate -source voltage Vs.  $T_J$

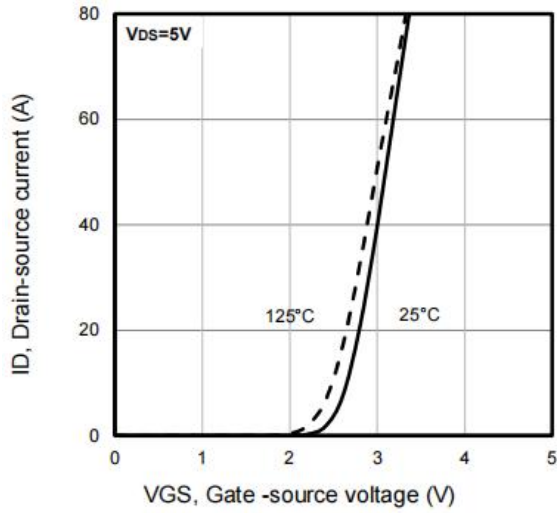


Fig3. Typical transfer characteristics

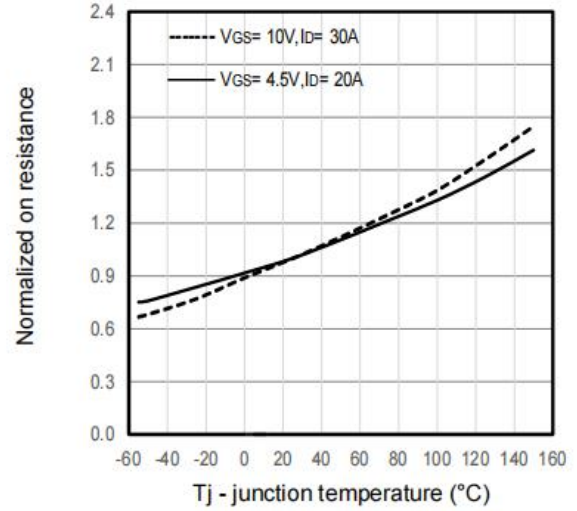


Fig4. Typical normalized on-resistance Vs. Tj

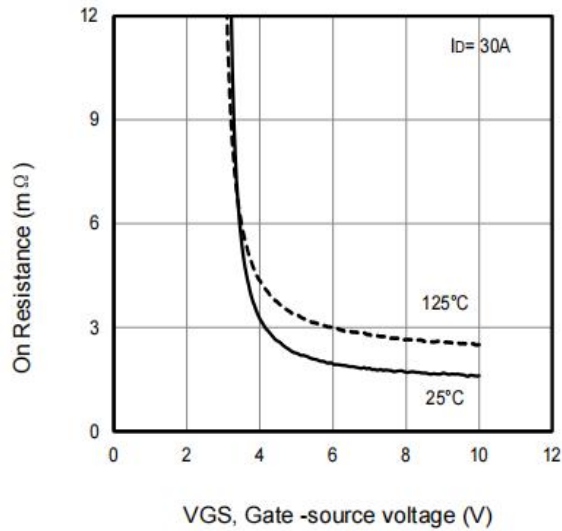


Fig5. Typical on resistance Vs gate-source voltage

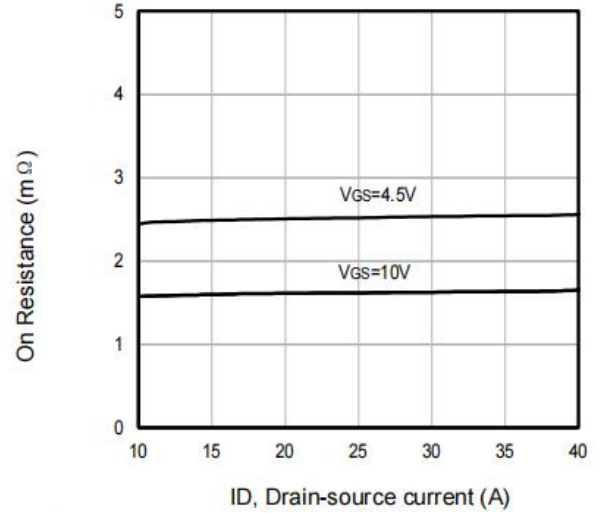


Fig6. Typical on resistance Vs drain current

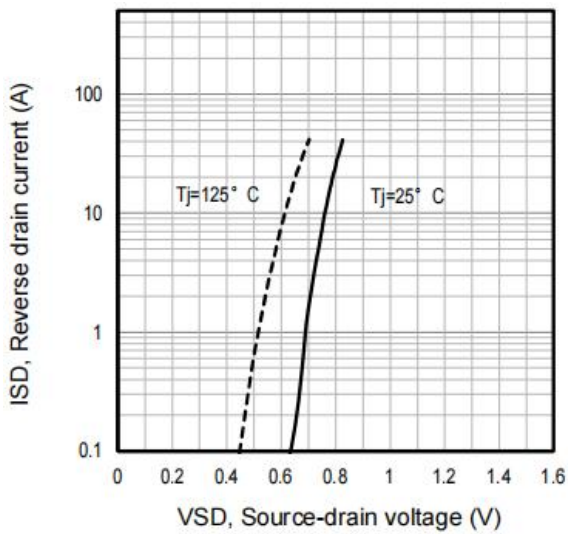


Fig7. Typical source-drain diode forward voltage

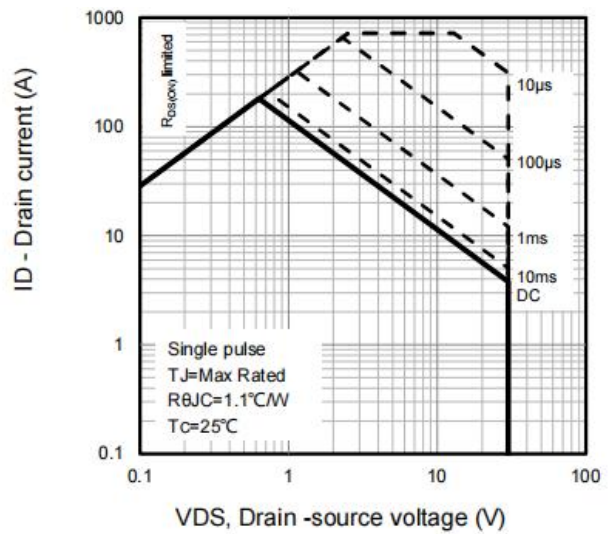


Fig8. Maximum safe operating area



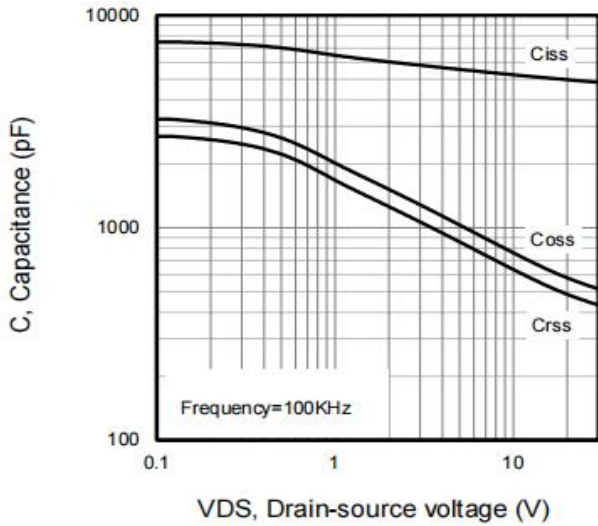


Fig9. Typical capacitance Vs. drain-source voltage

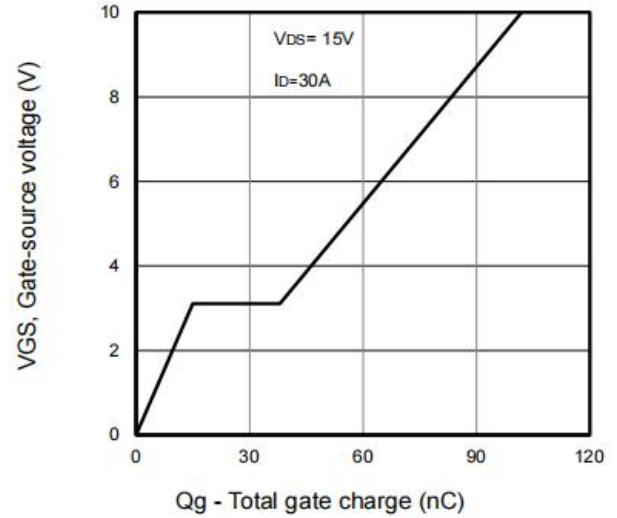


Fig10. Typical gate charge Vs. gate-source voltage

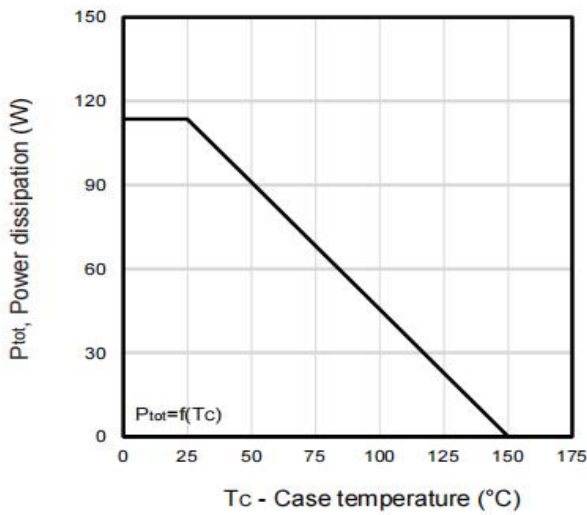


Fig11. Power dissipation Vs. case temperature

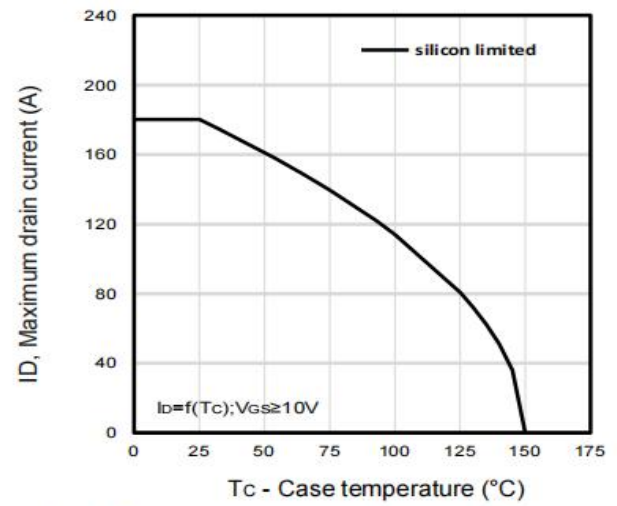


Fig12. Maximum drain current Vs. case temperature

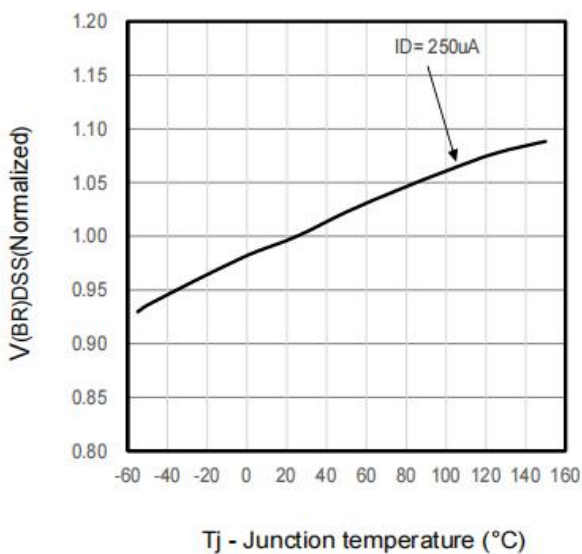


Fig13. Typical V(BR)DSS Vs Tj

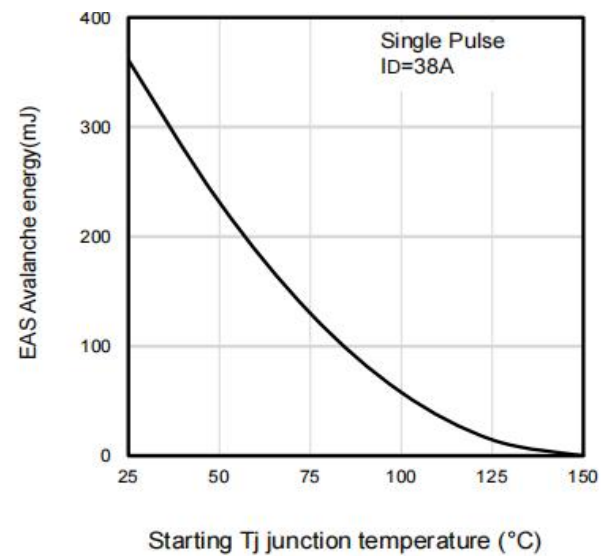


Fig14. Maximum avalanche energy vs temperature (°C)



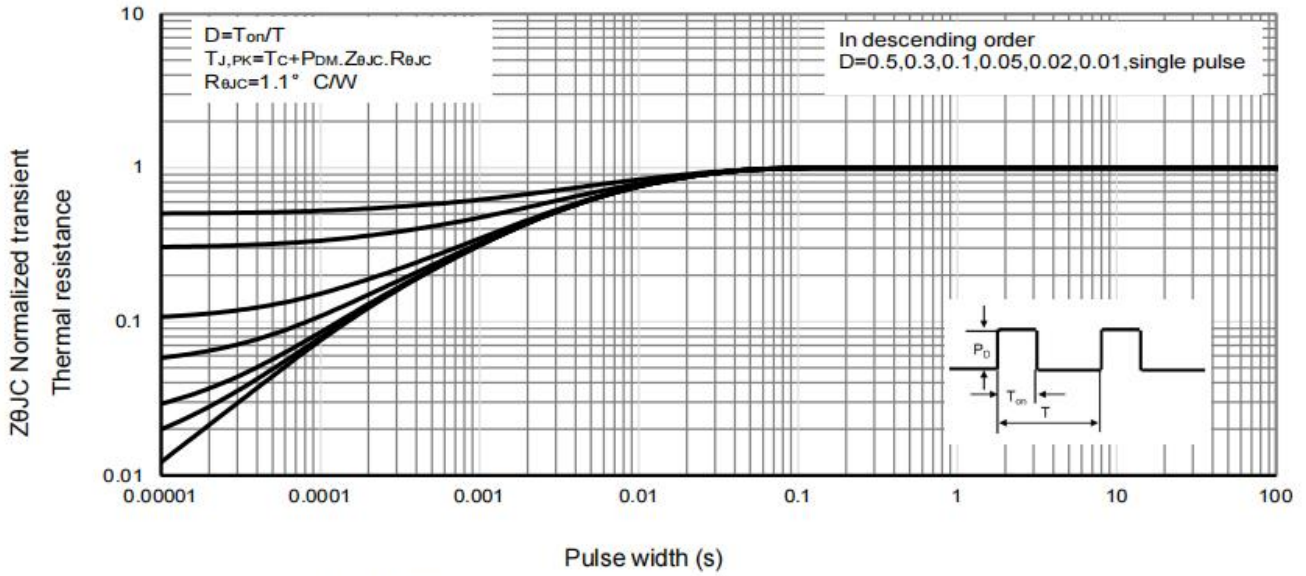


Fig15. Normalized maximum transient thermal impedance

Test circuits and waveforms

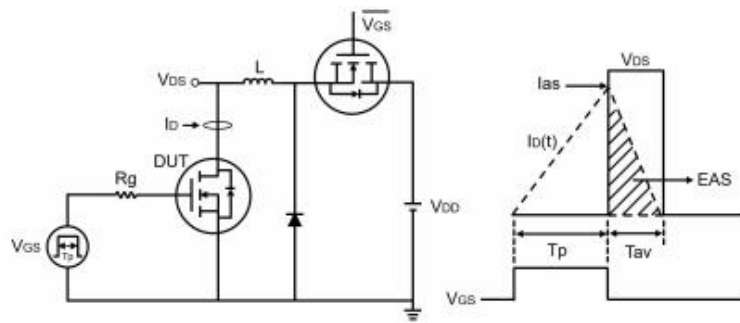


Fig16. Unclamped inductive test circuit and waveforms

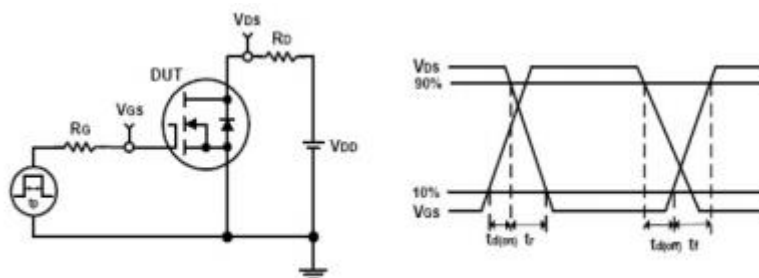
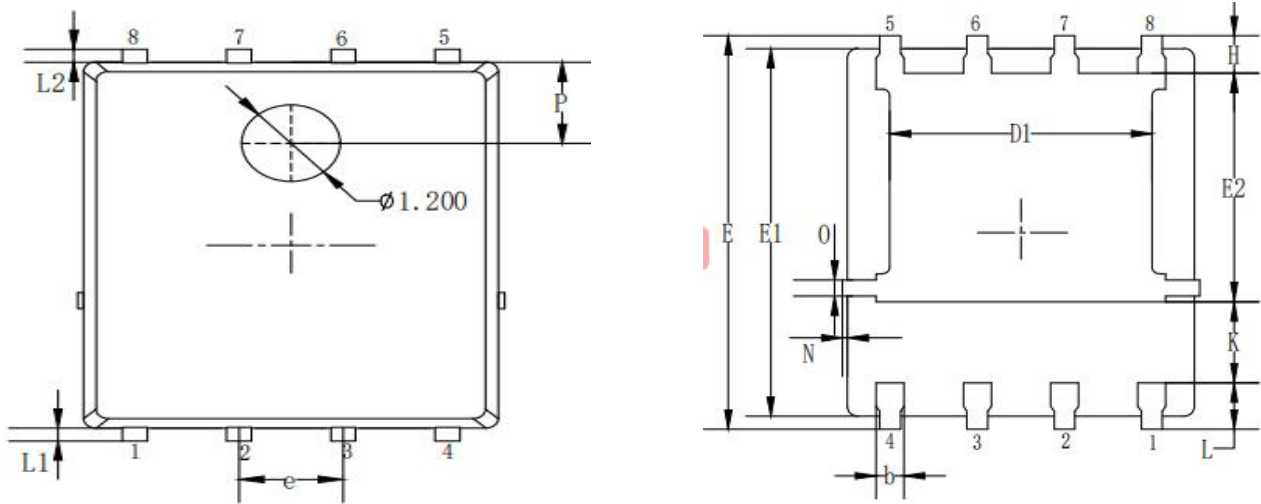


Fig17. Switching time test circuit and waveforms

**Package Outline Dimensions**


Symbols	Millimeters		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.10
D1	3.72	3.82	3.92
E	6.00	6.15	6.25
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.69	0.75
L1/L2	0.10 ~ 0.20		
θ	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

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