

N-Channel 30V MOSFET

E030N8P0KL1

	V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)
N1	30V	8.0 @ V _{GS} = 10V	45
N2	30V	8.0 @ V _{GS} = 10V	45

PDFN5*6D



Features

- Low R_{DS(on)} trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested



Applications

- DC/DC conversion
- Power switch
- Moto driver

Package And Ordering Information

Ordering code	Package	Marking		
E030N8P0KL1	PDFN5*6D	E030N8P0KL1		

Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
PDFN5*6D	5000	1	5000



Key Performance Parameters

Parameter	Value	Unit
VDS, min @ Tj(max)	30	V
ID, pulse	146	Α
RDS(ON), max @ VGS=10V	8.0	mΩ
Qg	21.6	nC

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

Parameter	Symbol	Limit	Unit	
Drain-source voltage	V _{DS}	30		
Gate-source voltage	V_{GS}	±20	V	
	T _A =25°C		45	
Continuous drain current	T _A =100°C	- I _D	32	
Pulsed drain current	I _{D,pulse}	146	А	
Avalanche energy, single pulse		E _{AS}	38	mJ
Dower dissination	T _A =25°C		42	
Power dissipation	T _A =100°C	P_{D}	3.2	W
Operating junction and storage temperature range		T _J , T _{stg}	-55 To 175	°C

Thermal Characteristics

Parameter	Symbol	Max.	Uni t	
Thermal resistance, junction-to-case	Steady state	R _{eJC}	2.6	
Thermal resistance, junction-to-ambient	Reja	60	°C/W	

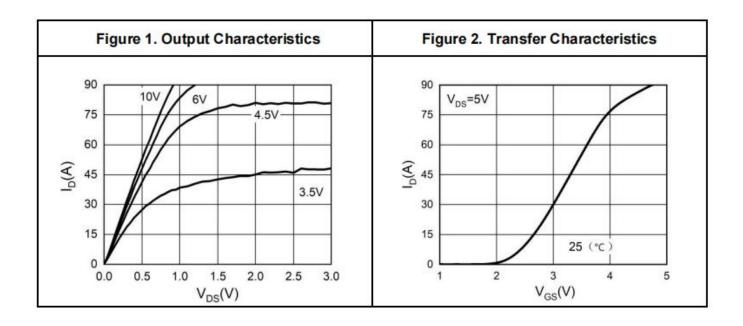
Electrical Characteristics at Tj=25°C unless otherwise specified

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions	
Static							
Drain to source breakdown voltage	V _{(BR)DSS}	30			V	V _{GS} = 0, I _D = 250 μA	
Gate-source threshold voltage	V _G s(th)	1		2.5	V	V _{DS} = V _{GS} , I _D = 250 μA	
Gate-body leakage	I _{GSS}			±100	nA	V _{DS} = 0 V, V _{GS} = ±20 V	
Zero gate voltage drain current	I _{DSS}			1	μA	V _{DS} = 30 V, V _{GS} = 0 V	
Drain-source on-resistance	Ros(on)		6.8	8	mΩ	V _{GS} = 10 V, I _D = 20 A	
Drain-source on-resistance	Ros(on)		9.8	12	mΩ	V _{GS} = 4.5 V, I _D = 15 A	
Forward transconductance	gfs		18		S	V _{DS} = 5 V, I _D = 20 A	

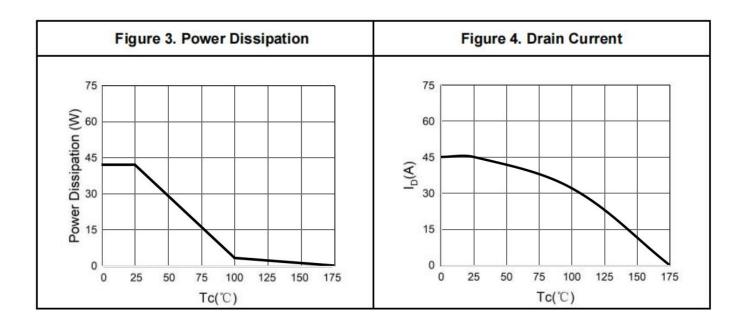


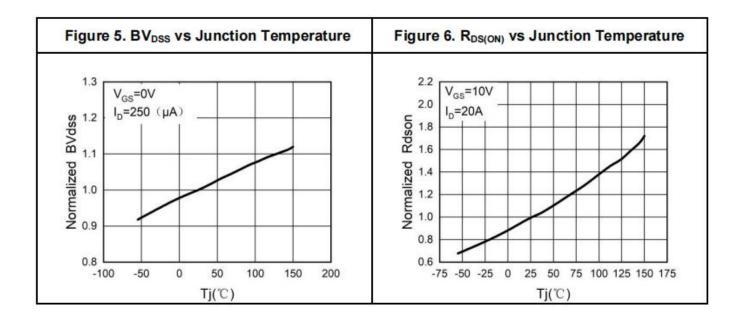
Gate resistance	Rg		1.8		Ω	f=1MHz		
Gate Charge								
Total gate charge	Qg		21.6					
Gate-source charge	Qgs		2.7		nC	V _{DS} = 15 V, I _D = 20 A, V _{GS} = 10 V		
Gate-drain charge	Qgd		4.7					
			ynamic	;				
Turn-on delay time	$t_{d(on)}$		12					
Rise time	tr		2.4			V _{DS} = 15 V, V _{GS} = 10 V,		
Turn-off delay time	$t_{\sf d(off)}$		30.4		ns	$R_L = 0.75 \Omega$, $R_{GEN} = 3 \Omega$		
Fall time	t_f		4		113			
Input capacitance	C _{iss}		1082					
Output capacitance	C _{oss}		147			V _{DS} =15 V, V _{GS} = 0 V, f = 1.0MHz		
Reverse transfer capacitance	C _{rss}		121		pF			
Body Diode								
Diode forward voltage	V _{SD}			1.2	V	V _{GS} = 0 V, I _S = 20 A		
Reverse recovery time	t _{rr}		19.4		ns	1 00 A di/dt 500 A/		
Reverse recovery charge	Qrr		11.6		nC	l		

Electrical Characteristics Diagrams

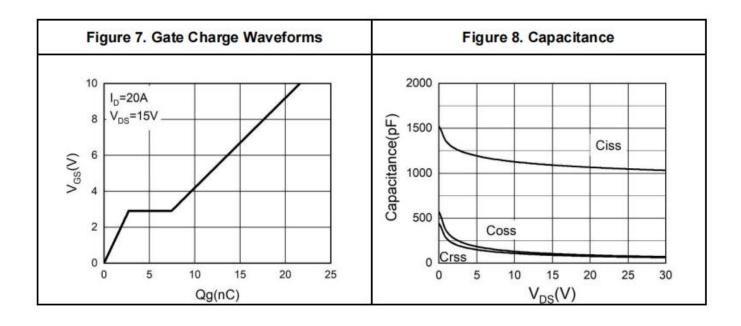


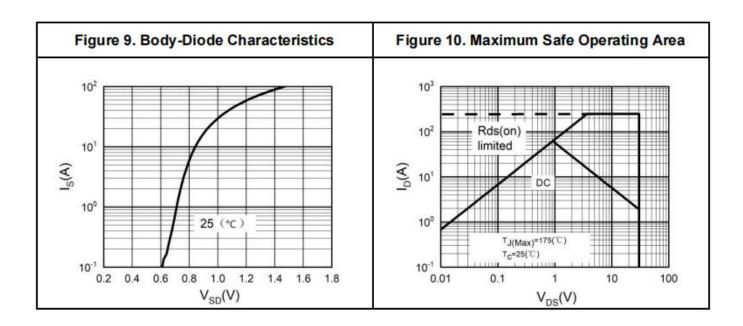






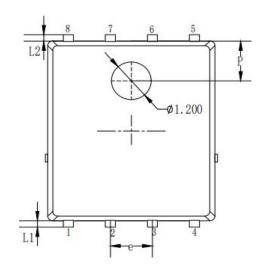


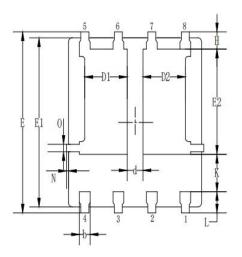






Package Outline Dimensions





0 1 1	Millimeters					
Symbols	MIN.	NOM.	MAX.			
A	0.90	1.05	1.20			
b	0.35	0.40	0.50			
C	0.20	0. 25	0.35			
D	4.90	5. 05	5. 20			
D1/D2	1.51	1, 61	1.71			
d	0.50	0.60	0.70			
Е	6.00	6. 15	6.30			
E1	5. 60	5. 75	5. 90			
E2	3. 47	3. 57	3. 67			
е	1	. 27 BSC				
Н	0.48	0.58	0.68			
K	1.17	1.27	1.37			
L	0.64	0.74	0.84			
L1/L2		0. 20 REF	7.			
θ	8°	10°	12°			
M	0. 08 REF.					
N	0	-	0.15			
0	0. 25 REF.					
P	1. 28 REF.					



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