

N-Channel 60V MOSFET

E060N2P3HL1

V_{DS} (V)	$R_{DS(on),max}$ (m Ω)	I_D (A)
60V	2.3 @ $V_{GS} = 10V$	180

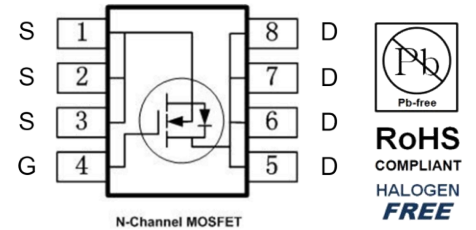
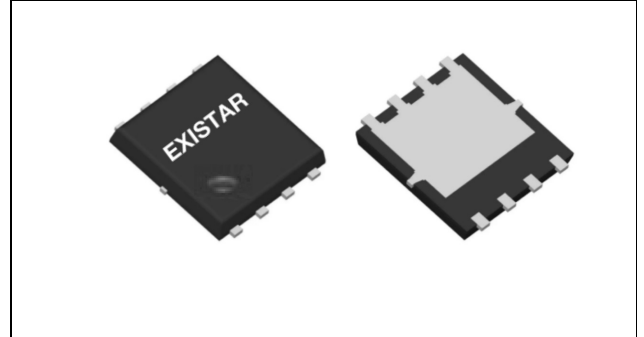
Features

- Low $R_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

PDFN5X6



Package And Ordering Information

Ordering code	Package	Marking
E060N2P3HL1	PDFN5x6	E060N2P3HL1

Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
PDFN5x6	5000	2	10000

Key Performance Parameters

Parameter	Value	Unit
VDS, min @ Tj(max)	60	V
ID, pulse	720	A
RDS(ON), max @ VGS=10V	2.3	mΩ
Qg	103.5	nC

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

Parameter	Symbol	Limit	Unit
Drain-source voltage	V _{DS}	60	V
Gate-source voltage	V _{GS}	±20	
Continuous drain current	I _D	T _C =25°C	180
		T _C =100°C	-
Pulsed drain current	I _{D,pulse}	720	A
Avalanche energy, single pulse	E _{AS}	240	mJ
Power dissipation	P _D	T _C =25°C	132
		T _A =25°C	-
Operating junction and storage temperature range	T _J , T _{stg}	-55 to 150	°C

Thermal Characteristics

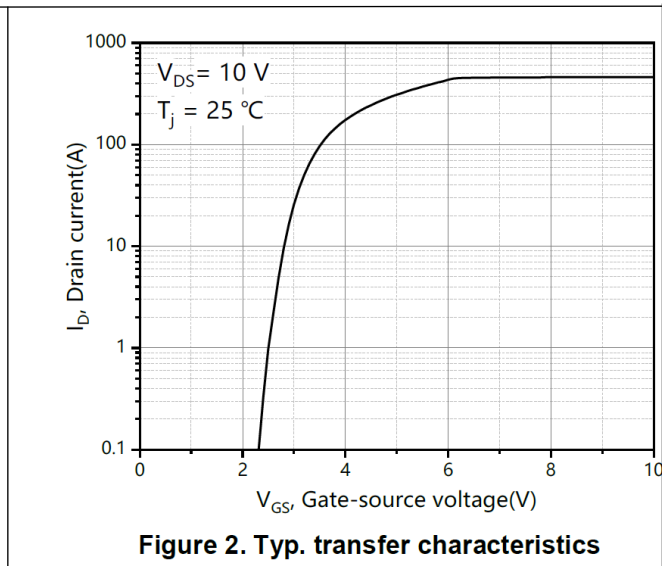
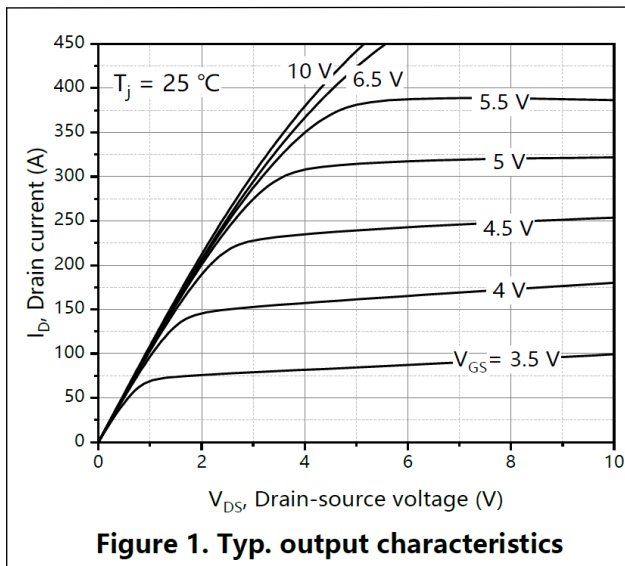
Parameter	Symbol	Max.	Unit
Thermal resistance, junction-to-case	R _{θJC}	0.95	°C/W
Thermal resistance, junction-to-ambient	R _{θJA}	62	

Electrical Characteristics at Tj=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Static						
Drain to source breakdown voltage	V _{(BR)DSS}	60			V	V _{GS} = 0, I _D = 250 μA
Gate-source threshold voltage	V _{GS(th)}	1.0		2.5	V	V _{DS} = V _{GS} , I _D = 250 μA
Gate-body leakage	I _{GSS}			±100	nA	V _{DS} = 0 V, V _{GS} = ±20 V
Zero gate voltage drain current	I _{DSS}			1	μA	V _{DS} = 60 V, V _{GS} = 0 V
Drain-source on-resistance	R _{DS(on)}		2.0	2.3	mΩ	V _{GS} = 10 V, I _D = 30 A
Drain-source on-resistance	R _{DS(on)}		2.8	3.5	mΩ	V _{GS} = 4.5 V, I _D = 30 A

Forward transconductance	g_{fs}		-		S	$V_{DS} = 5\text{ V}, I_D = 30\text{ A}$
Gate resistance	R_g		2.4		Ω	$f = 1\text{ MHz}$
Gate Charge						
Total gate charge	Q_g		104		nC	$V_{DS} = 50\text{ V}, I_D = 50\text{ A}, V_{GS} = 10\text{ V}$
Gate-source charge	Q_{gs}		17			
Gate-drain charge	Q_{gd}		16.8			
Dynamic						
Turn-on delay time	$t_{d(on)}$		32.2		ns	$V_{DS} = 50\text{ V}, I_D = 50\text{ A}, V_{GS} = 10\text{ V}, R_{GEN} = 2\ \Omega$
Rise time	t_r		53.3			
Turn-off delay time	$t_{d(off)}$		93.2			
Fall time	t_f		25.3			
Input capacitance	C_{iss}		6638		pF	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 100\text{ kHz}$
Output capacitance	C_{oss}		1275			
Reverse transfer capacitance	C_{rss}		58.1			
Body Diode						
Diode forward voltage	V_{SD}			1.3	V	$V_{GS} = 0\text{ V}, I_F = 20\text{ A}$
Reverse recovery time	t_{rr}		86.8		ns	$V_R = 50\text{ V}, I_S = 50\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		130		nC	

Electrical Characteristics Diagrams



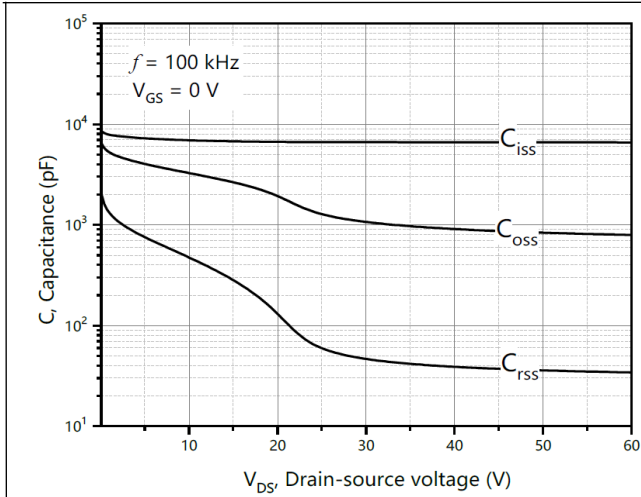


Figure 3. Typ. capacitances

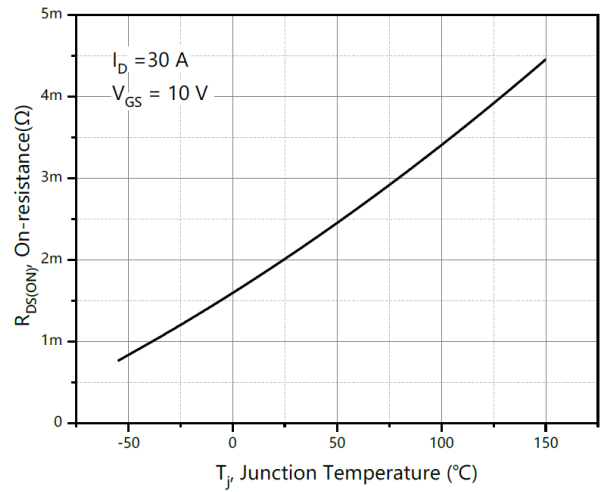


Figure 4. Drain-source on-state resistance

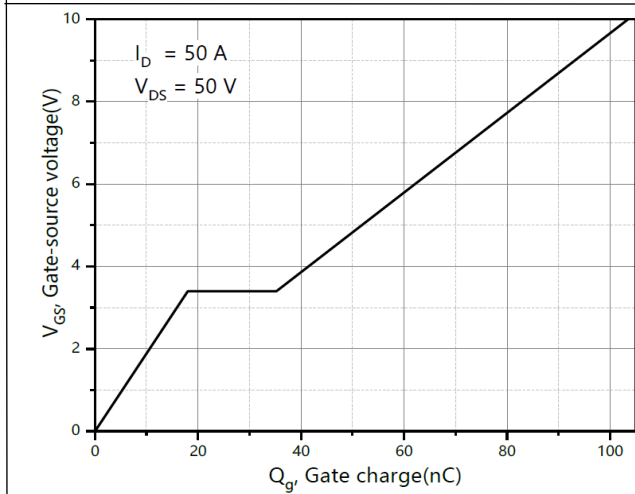


Figure 5. Typ. gate charge

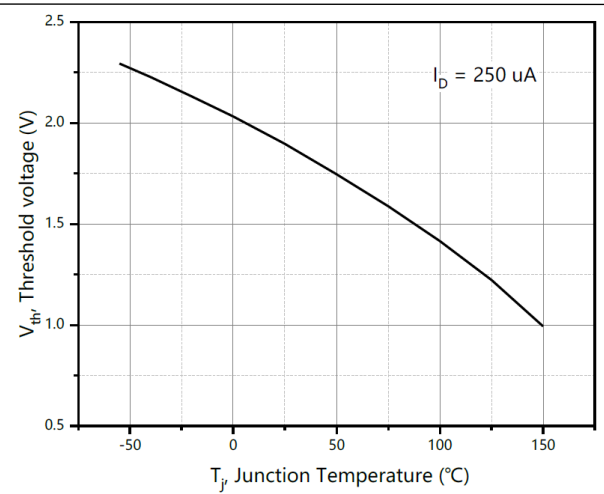


Figure 6. Threshold voltage

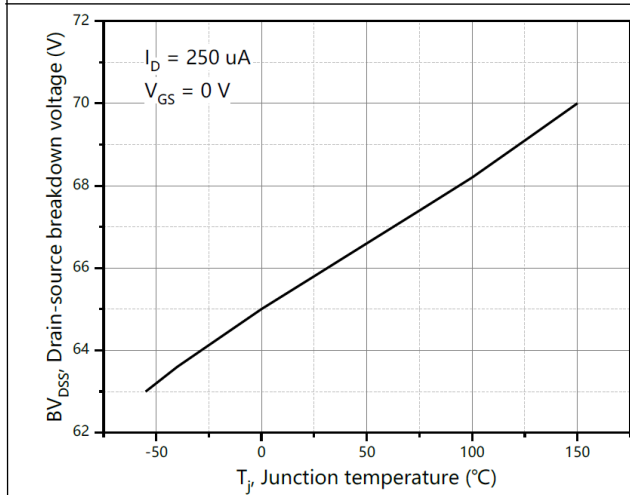


Figure 7. Drain-source breakdown voltage

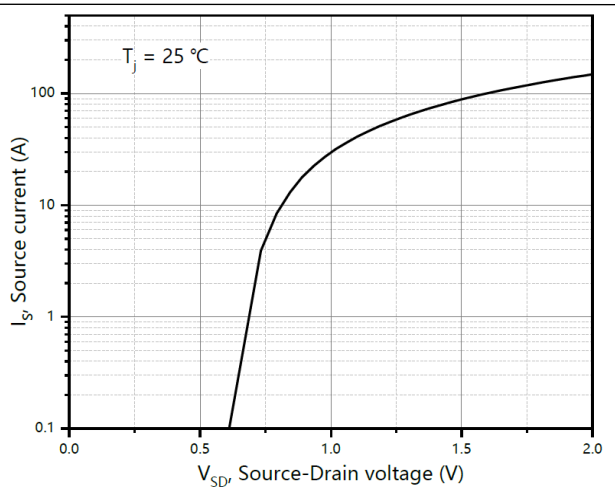


Figure 8. Forward characteristic of body diode

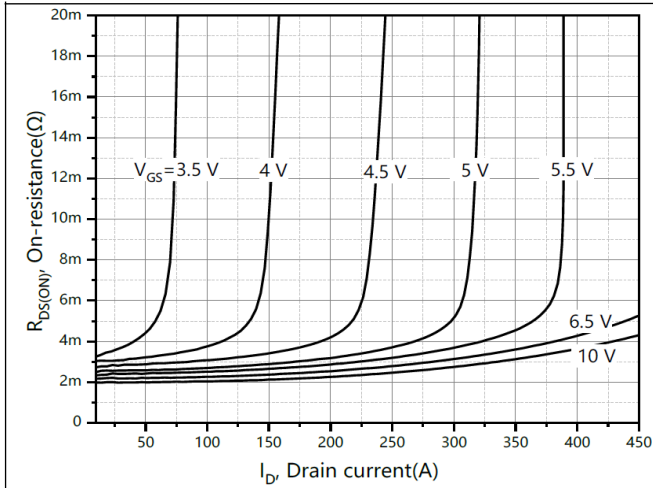


Figure 9. Drain-source on-state resistance

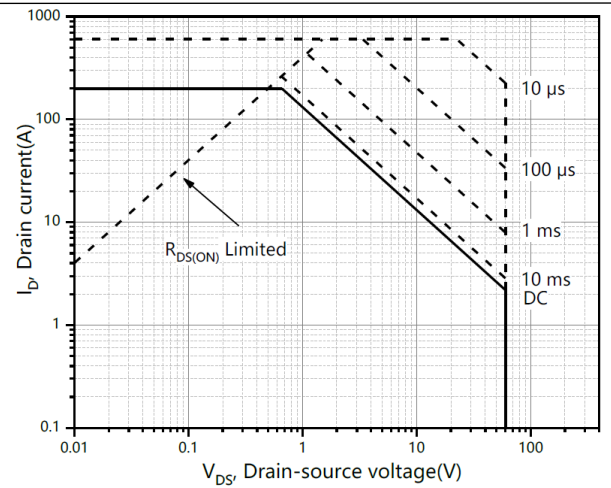


Figure 10. Safe operation area for $T_C=25\text{ }^\circ\text{C}$

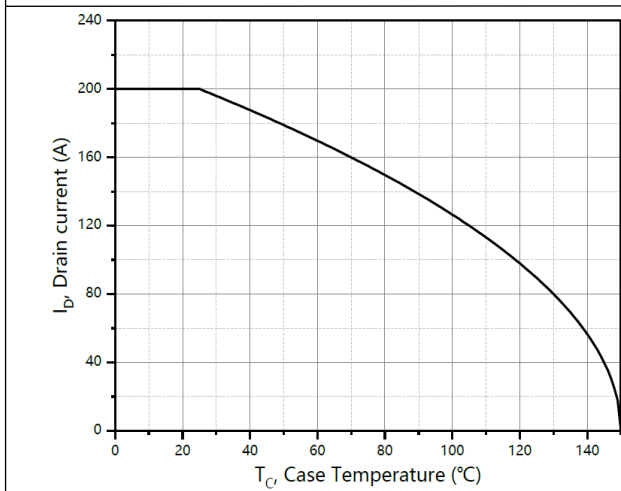


Figure 11. Drain current

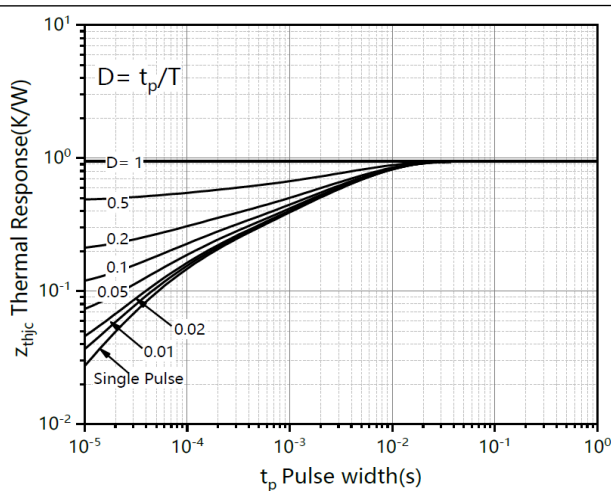


Figure 12. Max transient thermal impedance



Test circuits and waveforms

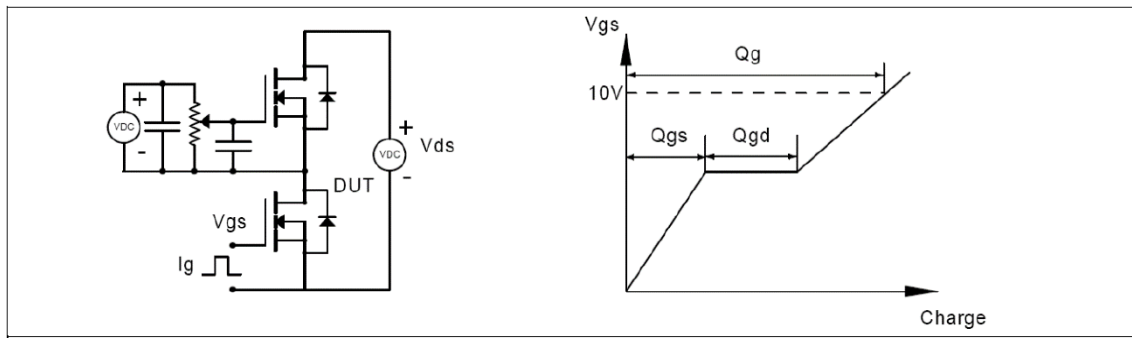


Figure 1. Gate charge test circuit & waveform

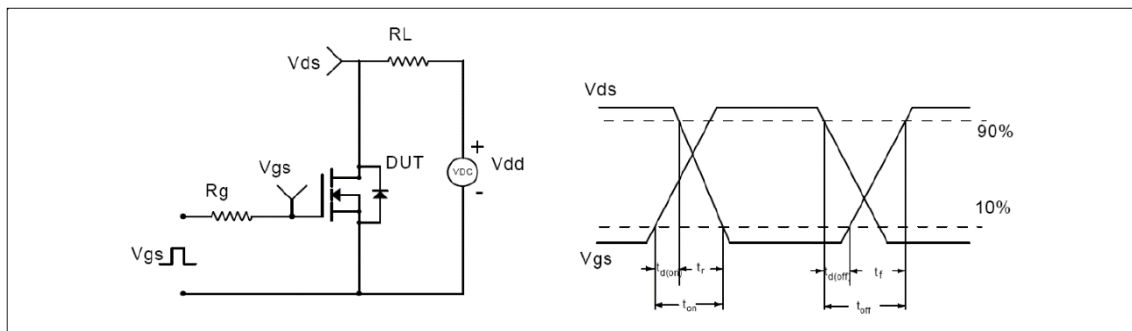


Figure 2. Switching time test circuit & waveforms

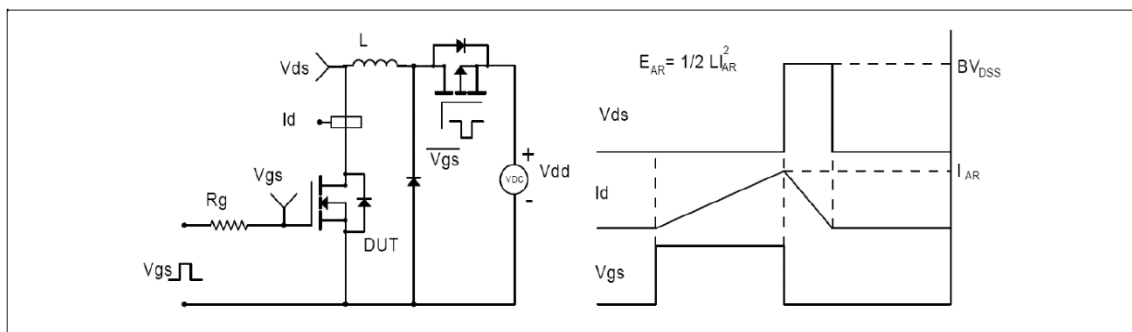


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

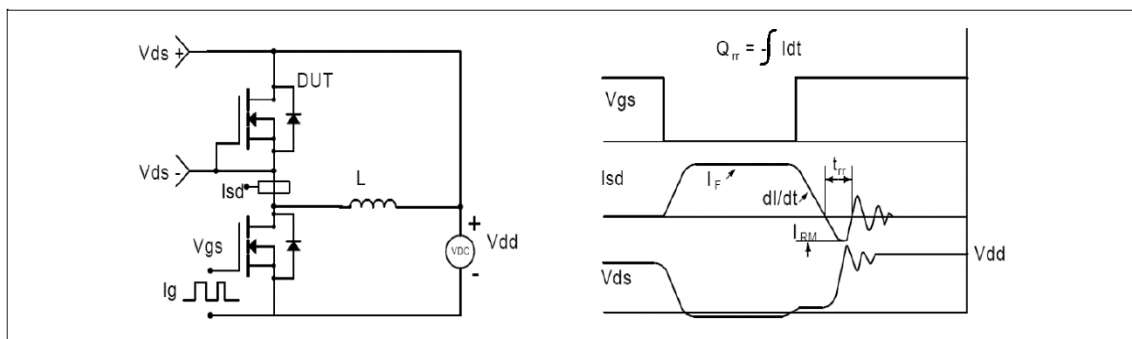
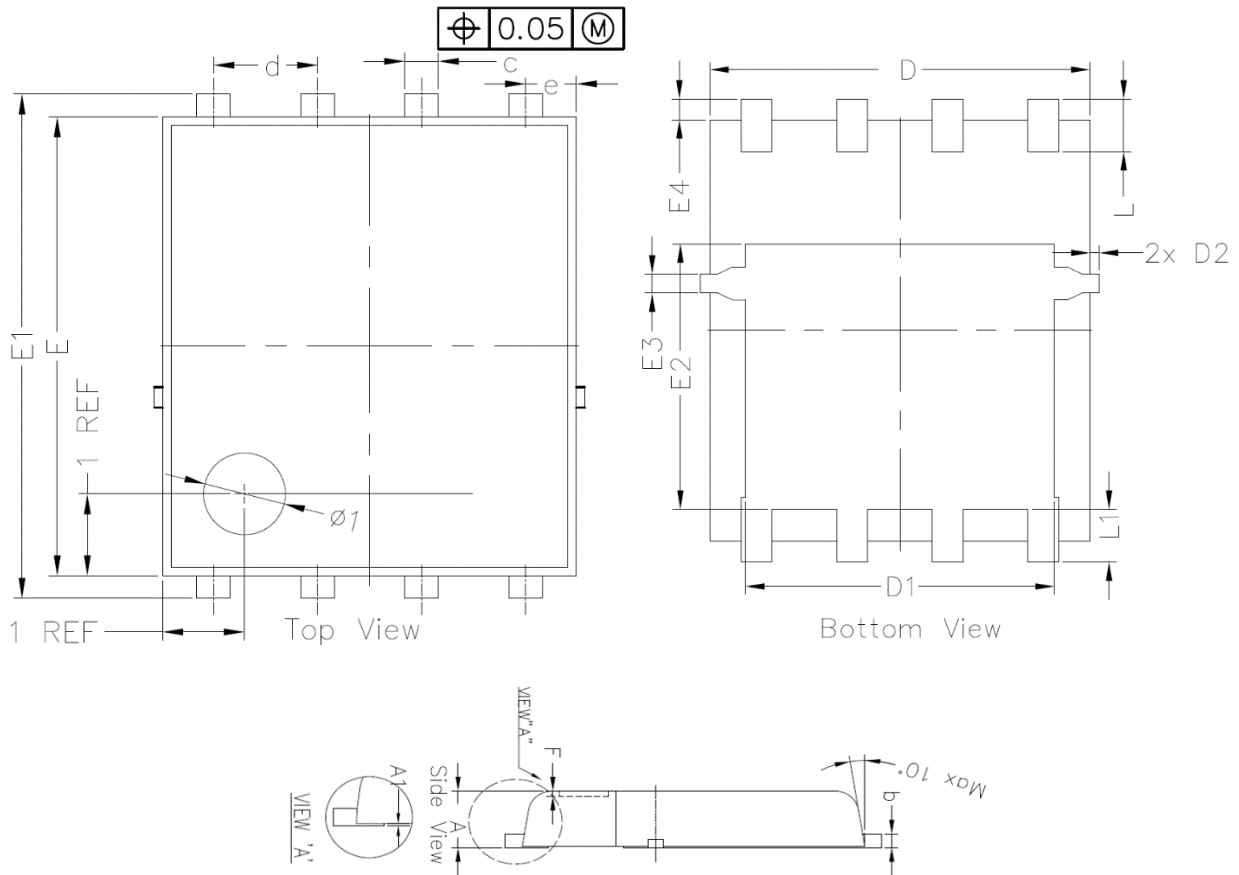


Figure 4. Diode reverse recovery test circuit & waveforms

Package Outline Dimensions


SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
* A	0.900	1.000	1.100	0.035	0.039	0.043
A1	0.000	---	0.050	0.000	----	0.002
b	0.246	0.254	0.312	0.010	0.010	0.012
* c	0.310	0.410	0.510	0.012	0.016	0.020
d	1.27 BSC			0.050 BSC		
* D	4.950	5.050	5.150	0.195	0.199	0.203
D1	4.000	4.100	4.200	0.157	0.161	0.165
* D2	---	---	0.125	---	---	0.005
e	0.62 BSC			0.024 BSC		
* E	5.500	5.600	5.700	0.217	0.220	0.224
* E1	6.050	6.150	6.250	0.238	0.242	0.246
E2	3.425	3.525	3.625	0.135	0.139	0.143
E3	0.150	0.250	0.350	0.006	0.010	0.014
* E4	0.175	0.275	0.375	0.007	0.011	0.015
F	-	-	0.100	-	-	0.004
* L	0.500	0.600	0.700	0.02	0.02	0.03
L1	0.600	0.700	0.800	0.02	0.03	0.03

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